High Performance Schottky Rectifier, 2.1 A

Anode

-0



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SMA

PRODUCT SUMMARY					
Package	SMA				
I <sub>F(AV)</sub>	2.1 A				
V <sub>R</sub>	60 V				
V <sub>F</sub> at I <sub>F</sub>	0.63				
I <sub>RM</sub>	7.5 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
Diode variation	Single die				
E <sub>AS</sub>	2.0 mJ				

### FEATURES

- Small foot print, surface mountable
- Low forward voltage drop

- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

The VS-10MQ060NPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNIT				
I <sub>F(AV)</sub>	DC	2.1	A			
V <sub>RRM</sub>		60	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	40	A			
V <sub>F</sub>	1.5 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.63	V			
TJ	Range	-55 to +150	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-10MQ060NPbF	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	60	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>		v		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDI	TEST CONDITIONS		UNITS	
Maximum average forward current See fig. 4	I <sub>F(AV)</sub>	50 % duty cycle at $T_L$ = 120 °C, rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)		1.5	А	
Maximum peak one cycle non-repetitive surge current I <sub>FSM</sub> See fig. 6		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	40	A	
		10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	10		
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 4 mH		2.0	mJ	
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1.0	А	

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
Maximum forward voltage drop See fig. 1		1 A	T.I = 25 °C	0.63	V	
	V <sub>EM</sub> <sup>(1)</sup>	1.5 A	1j=25 C	0.71		
	VFM ("	1 A	T <sub>J</sub> = 125 °C	0.57		
		1.5 A	$1_{\rm J} = 125$ C	0.63		
Maximum reverse leakage current See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	0.5	mA	
		T <sub>J</sub> = 125 °C	VR - Haled VR	7.5		
Threshold voltage	V <sub>F(TO)</sub>		0.45	V		
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum 86		86.8	mΩ	
Typical junction capacitance	CT	$V_R = 10 V_{DC}, T_J = 25 \text{ °C}, \text{ test signal} = 1 \text{ MHz}$		31	pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 2.0		nH		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/µs			V/µs	

#### Note

 $^{(1)}$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	$T_{J}$ <sup>(1)</sup> , $T_{Stg}$		-55 to +150	°C	
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	°C/W	
Approximate weight			0.07	g	
Approximate weight			0.002	oz.	
Marking device		Case style SMA (similar D-64)	V1	Н	

#### Note

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ (1) thermal runaway condition for a diode on its own heatsink

= 150 °C

= 125 °C

= 25 °C

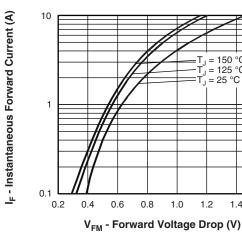


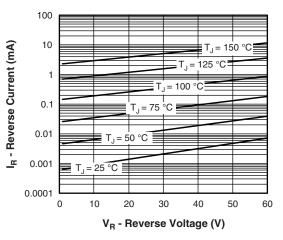
Fig. 1 - Maximum Forward Voltage Drop Characteristics

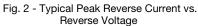
1.0

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1.4

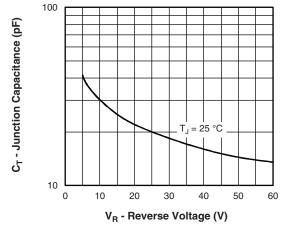
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Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

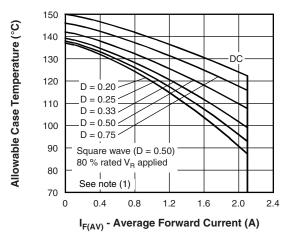


Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature

#### Note

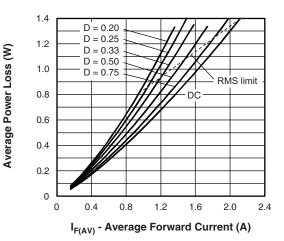


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

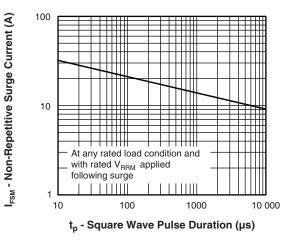


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

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### **ORDERING INFORMATION TABLE**

Device code	VS-	10	м	Q	060	N	TR	PbF
	1	2	3	4	5	6	7	8
	1 -	Visł	nay Sem	niconduc	ctors pro	oduct		
	2 -	Cur	rent rati	ng				
	3 -	M =	SMA					
	4 -	Q =	Schottk	ty "Q" se	eries			
	5 -	Volt	tage rati	ng (060	= 60 V)			
	6 -	N =	new SN	/A				
	7 -	• N	one = bo	ox (1000	) pieces	)		
		• TI	R = tape	and ree	el (7500	pieces	)	
	8 -	PbF	= lead	(Pb)-fre	е			

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95018				
Part marking information		www.vishay.com/doc?95029		
Deckeding information	Tape and reel	www.vishay.com/doc?95034		
Packaging information	Bulk	www.vishay.com/doc?95397		

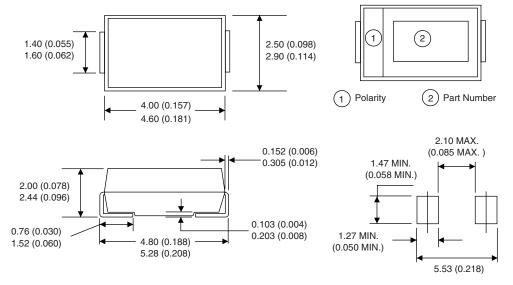


# **Outline Dimensions**

## Vishay High Power Products

**SMA** 

### **DIMENSIONS** in millimeters (inches)



Soldering pad



Vishay

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