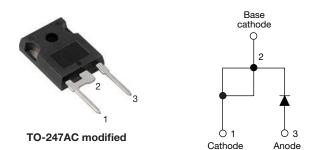
VS-60EPS16PbF, VS-60EPS16-M3

Vishay Semiconductors

High Voltage Input Rectifier Diode, 60 A



PRODUCT SUMMARY					
Package	TO-247AC modified (2-pins)				
I _{F(AV)}	60 A				
V _R	1600 V				
V _F at I _F	1.15 V				
I _{FSM}	950 A				
T _J max.	150 °C				
Diode variation	Single die				

FEATURES

- Very low forward voltage drop
- 150 °C max. operating junction temperature
 Designed and qualified according to
- JEDEC[®]-JESD47
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Input rectification
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNITS					
I _{F(AV)}	Sinusoidal waveform	60	А				
V _{RRM}		1600	V				
I _{FSM}		950	А				
V _F	60 A, T _J = 25 °C	1.15	V				
TJ		-40 to 150	°C				

VOLTAGE RATINGS								
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA					
VS-60EPS16PbF, VS-60EPS16-M3	1600	1700	1					

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	VALUES	UNITS					
Maximum average forward current	I _{F(AV)}	T _C = 118 °C, 180° conduction half sine wave	60				
Maximum peak one cycle		10 ms sine pulse, rated V _{RRM} applied	800	А			
non-repetitive surge current	IFSM	10 ms sine pulse, no voltage reapplied	950				
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	3200	A ² s			
Maximum r-t for fusing	1-1	10 ms sine pulse, no voltage reapplied	4525	A-2			
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied	45 250	A²√s			

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VS-60EPS16PbF, VS-60EPS16-M3

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS			
Maximum forward voltage drep	V	30 A, T _J = 25 °C		1.0	V			
Maximum forward voltage drop	V _{FM}	60 A, T _J = 25 °C	1.15	v				
Forward slope resistance	rt	T.I = 150 °C	3.96	mΩ				
Threshold voltage	V _{F(TO)}	1]=130 0	0.74	V				
Maximum reverse lookage ourrent	I _{RM}	T _J = 25 °C	$V_{B} = Rated V_{BBM}$	0.1	mA			
Maximum reverse leakage current		T _J = 150 °C	VR = naled VRRM	1.0				

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-40 to 150	°C				
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	0.35					
Maximum thermal resistance, junction to ambient	R _{thJA}		40	°C/W				
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.2					
Approximate weight			6	g				
Approximate weight			0.21	oz.				
Mounting torque			6.0 (5)	kgf ⋅ cm				
Mounting torque maximum			12 (10)	(lbf ⋅ in)				
Marking device		Case style TO-247AC modified (JEDEC)	60EF	PS16				



VS-60EPS16PbF, VS-60EPS16-M3

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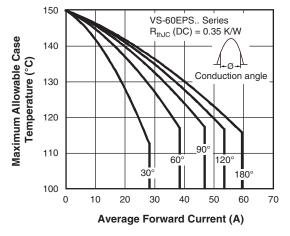


Fig. 1 - Current Rating Characteristics

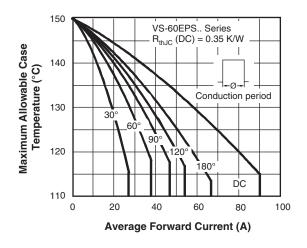


Fig. 2 - Current Rating Characteristics

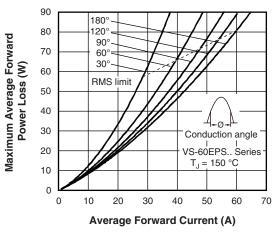


Fig. 3 - Forward Power Loss Characteristics

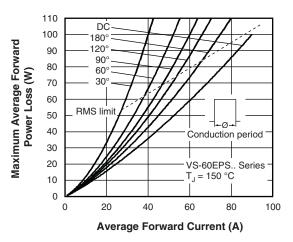
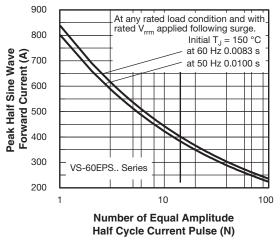
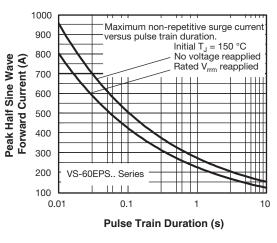


Fig. 4 - Forward Power Loss Characteristics









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VS-60EPS16PbF, VS-60EPS16-M3

Vishay Semiconductors

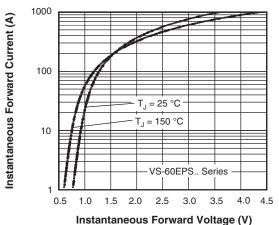


Fig. 7 - Forward Voltage Drop Characteristics

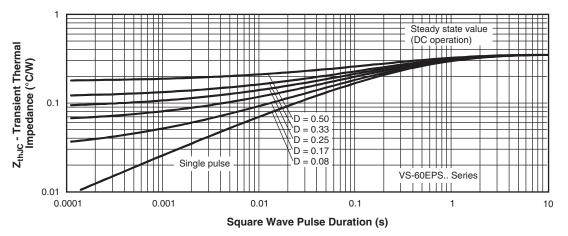


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics



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ORDERING INFORMATION TABLE

Device code	VS-	60	Е	Р	S	16	PbF
	(1)	(2)	(3)	(4)	(5)	(6)	(7)
	1 -	- Visł	nay Sem	niconduc	ctors pro	duct	
	2 -	Curi	rent rati	ng (60 =	60 A)		
	3 -	Circ	uit confi	guratior	1:		
	_		Single o	liode			
	4 -		kage:				
	E	-		AC mod	ified		
	5 -		e of silic Standar	d recove	orv rocti	fior	
	6 -			ng (16 =	-		
	7 -		•	tal digit:		,	
		PbF	= Lead	(Pb)-fre	e and R	oHS co	mpliant
		-M3	= Halog	jen-free,	RoHS of	complia	nt and t

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-60EPS16PbF	25	500	Antistatic plastic tubes				
VS-60EPS16-M3	25	500	Antistatic plastic tubes				

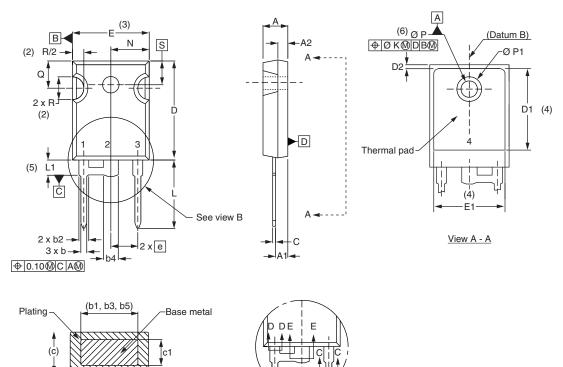
LINKS TO RELATED DOCUMENTS						
Dimensions		www.vishay.com/doc?95541				
Part marking information	TO-247AC modified PbF	www.vishay.com/doc?95255				
Part marking mornation	TO-247AC modified -M3	www.vishay.com/doc?95442				



Vishay Semiconductors

TO-247 modified

DIMENSIONS in millimeters and inches



b2. b4) Section C - C, D - D, E - E

(4)

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	INCHES		
STNIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
D2	0.51	1.30	0.020	0.051		
E	15.29	15.87	0.602	0.625	3	
E1	13.72	-	0.540	-		
е	5.46	BSC	0.215	BSC		
ØК	2.	54	0.010			
L	14.20	16.10	0.559	0.634		
L1	3.71	4.29	0.146	0.169		
N	7.62 BSC 0.3					
ØΡ	3.56	3.66	0.14	0.144		
Ø P1	-	6.98	-	0.275		
Q	5.31	5.69	0.209	0.224		
R	4.52	5.49	0.178	0.216		
S	5.51	BSC	0.217	BSC		

Notes

- ⁽¹⁾ Dimensioning and tolerance per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- ⁽⁴⁾ Thermal pad contour optional with dimensions D1 and E1
- ⁽⁵⁾ Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension c

Revision: 07-Apr-15

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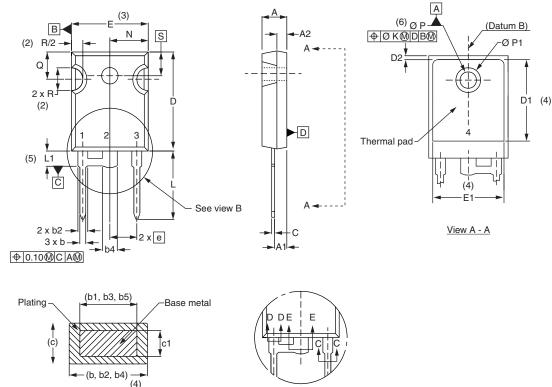
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TO-247 - 50 mils L/F modified

DIMENSIONS in millimeters and inches



Section C - C, D - D, E - E



View	В

SYMBOL	MILLIN	LLIMETERS INCHES		NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	1.37	0.046	0.054	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.35	0.020	0.053	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46 BSC		0.215	BSC	
ØК	0.254		0.0)10	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62 BSC		0.	.3	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	7.39	-	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217 BSC		

Notes

- ⁽¹⁾ Dimensioning and tolerance per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- ⁽⁴⁾ Thermal pad contour optional with dimensions D1 and E1
- ⁽⁵⁾ Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension c and Q

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