TOSHIBA Intelligent Power Device Silicon Monolithic Power MOS Integrated Circuit

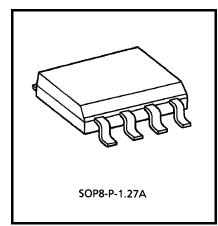
TPD1033F

High-side Power Switch for Motors, Solenoids, and Lamp Drivers

The TPD1033F is a monolithic power IC for high-side switches. The IC has a vertical MOS FET output that can be directly driven from a CMOS or TTL logic circuit (e.g., an MPU). The device is equipped with intelligent self-protection and diagnostic functions.

Features

- A monolithic power IC with a new structure combining a control block (Bi-CMOS) and a vertical power MOS FET (π-MOS) on a single chip
- One side of load can be grounded to a high-side switch
- Can directly drive a power load from a microprocessor.
- Built-in protection against thermal shutdown and load short-circuiting.



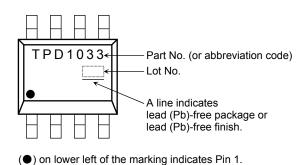
Weight: 0.08 g (typ.)

- Incorporates a diagnosis function that allows diagnosis output to be read externally in the event of load short-circuiting, opening, or overheating.
- Up to -10 V of counterelectromotive force from an L load can be applied.
- Low on-resistance $: R_{ON} = 220 \text{ m}\Omega \text{ (max)}$
- Low operating current : IDD = 1 mA (typ.), (@VDD = 12 V, VIN = 0 V)
- 8-pin SOP package for surface mounting can be packed in tape.

Pin Assignment

IN 1 8 N.C. DIAG 2 7 OUT GND 3 6 OUT VDD 4 5 OUT (top view)

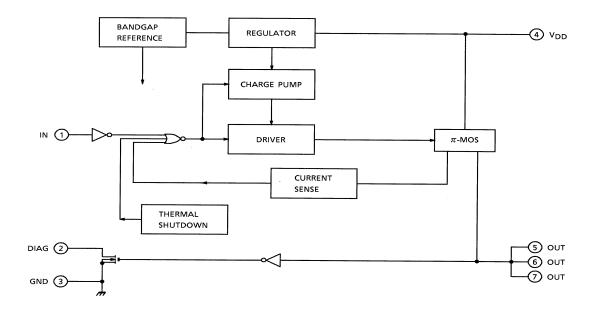
Marking



Note: Due to its MOS structure, this product is sensitive to static electricity.

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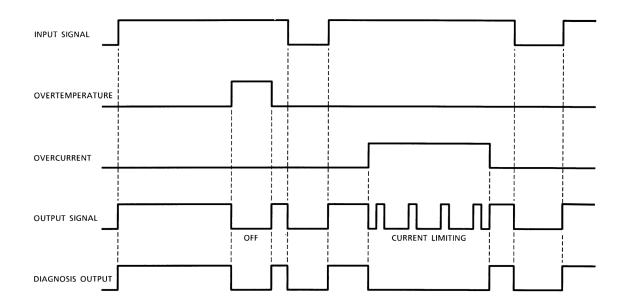
Block Diagram



Pin Description

Pin No.	Symbol	Function
1	IN	Input pin. Input is CMOS-compatible, with pull-down resistor connected. Even if the input is open, output will not accidentally turn on.
2	DIAG	Self-diagnosis detection pin. Goes low when overheating is detected or when output is short-circuited with input on (high). n-channel open drain.
3	GND	Ground pin.
4	V _{DD}	Power pin.
5, 6, 7	OUT	Output pin. When the load is short-circuited and current in excess of the detection current (8 A typ.) flows to the output pin, the output automatically turns on or off.

Timing Chart



Truth Table

Input Signal	Output Signal	Diagnosis Output	State	
Н	Н	Н	Normal	
L	L	L	Nomiai	
Н	L	L	Load short-circuited	
L	L	L	Load Short-circuited	
Н	Н	Н	· Load open	
L	Н	Н	Load open	
Н	L	L	Overtemperature	
L	L	L	Overtemperature	

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Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unite
Drain-source voltage		V _{DS}	60	V
Supply voltage	DC	V _{DD (1)}	25	V
Supply voltage	Pulse	V _{DD (2)}	60 (Rs = 1Ω,τ= 250 ms)	V
Input voltage	DC	V _{IN (1)}	-0.5 ~ 12	V
Input voltage	Pulse	V _{IN (2)} V _{DD (1)} + 1.5 (t = 100 ms)		V
Diagnosis output voltage		V _{DIAG}	-0.5 ~ 25	V
Output current		Io	Internally limited	Α
Input current		I _{IN}	± 10	mA
Diagnosis output current		I _{DIAG}	5	mA
Power dissipation (Ta = 25°C)		D-	1.4 Note 1	W
		P _D	2.4 Note 2	۷V
Operating temperature		T _{opr}	- 40 ~ 110	°C
Channel temperature		T _{ch}	150	°C
Storage temperature		T _{stg}	- 55 ~ 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Resistance

Characteristic	Symbol	Test Condition	Unit	
Thermal resistance	R _{th (ch-a)}	89.3 (Note 1)	°C/W	
Themal resistance	rvtn (cn-a)	52.1 (Note 2)		

Note 1: Mounted on glass epoxy board (25.4 mm × 25.4 mm × 0.8 mm) (DC)

Note 2: Mounted on glass epoxy board (25.4 mm × 25.4 mm × 0.8 mm) (t $_{W}$ ≤ 10 s)

Electrical Characteristics (Unless otherwise specified, T_{ch} = - 40 to 110°C, V_{DD} = 8~18 V)

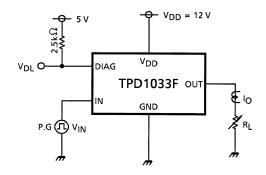
Characteristic		Symbol	Test Cir- cuit	Test Condition	Min	Тур.	Max	Unit
Operating supply voltage		V _{DD} (opr)	_	_	5	12	18	V
Supply current		I _{DD}	_	V _{DD} = 12 V, V _{IN} = 0 V	_	1	5	mA
Input voltage		V _{IH}	_	V _{DD} = 12 V, I _O = 2 A	3.5	_	_	V
input voltage		V _{IL}	l	V _{DD} = 12 V, I _O = 1.2 mA	1	_	1.5	V
Input current		I _{IN (1)}	l	V _{DD} = 12 V, V _{IN} = 5 V	1	50	200	μA
		I _{IN (2)}	1	V _{DD} = 12 V, V _{IN} = 0 V	-0.2	_	0.2	μΑ
On-voltage		V _{DS} (ON)	_	V _{DD} = 12 V, I _O = 2 A, T _{ch} = 25°C	_	_	0.44	٧
On-resistance		R _{DS} (ON)	_	V _{DD} = 12 V, I _O = 2 A, T _{ch} = 25°C	_	_	0.22	Ω
Output leakage current		I _{OL}	_	V _{DD} = 18 V, V _{IN} = 0 V	_	_	1.2	mA
Diagnosis output voltage	"L" Level	V _{DL}	_	V _{DD} = 12 V, I _{DL} = 2 mA	_	_	0.4	V
Diagnosis output current	"H" Level	I _{DH}	_	V _{DD} = 18 V, V _{DH} = 18 V	_	_	10	μΑ
Overcurrent protection		I _{S (1)} Note 3	1	V - 40 V T - 25°0	4	6	8	А
		I _{S (2)} Note 4	2	V _{DD} = 12 V, T _{ch} = 25°C	4	8	12	А
-	Temperature	Ts	_	_	150	160	200	°C
Thermal shutdown	Hysteresis	ΔTs	_		_	10	_	°C
Open detection resistance		R _{ops}	_	V _{DD} = 8 V	1	20	100	kΩ
Switching time	0 11 11		3	$V_{DD} = 12 \text{ V}, R_L = 5\Omega,$	10	100	_	μs
Switching time		toff	3	T _{ch} = 25°C	10	30	_	μs

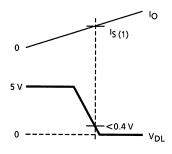
Note 3: Overcurrent detection value when load is short circuited and V_{IN} = "L" \rightarrow "H"

Note 4: Overcurrent detection value when load current is increased while $V_{\mbox{\scriptsize IN}}$ = "H"

Test Circuit 1

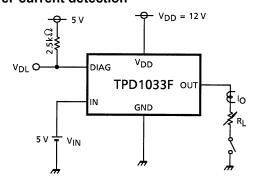
Over-current detection

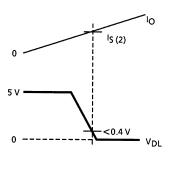




Test Circuit 2

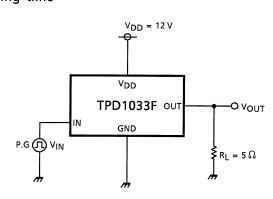
Over-current detection

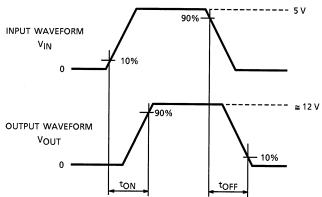


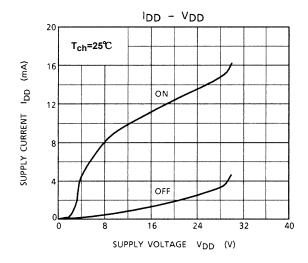


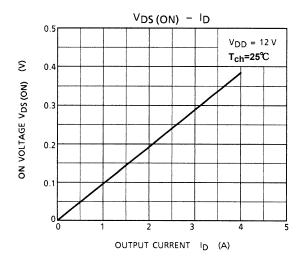
Test Circuit 3

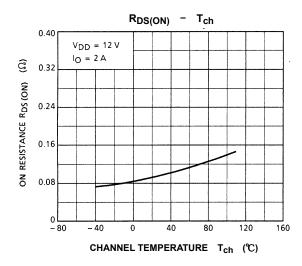
Switching time

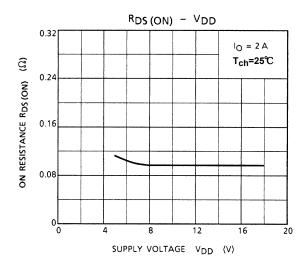


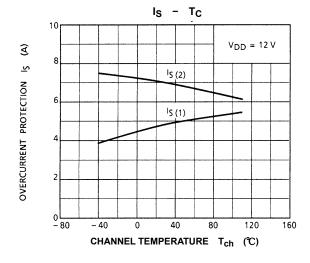


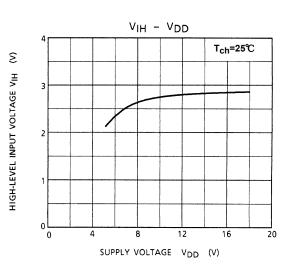


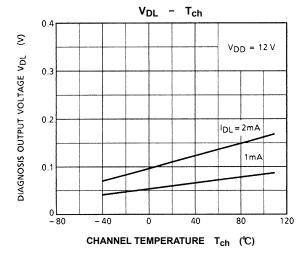


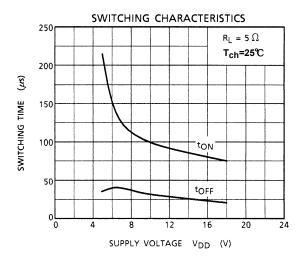


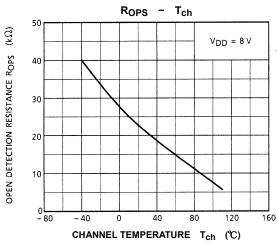


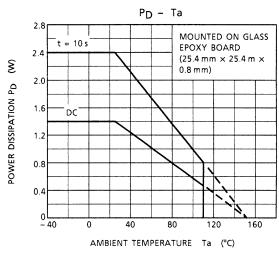


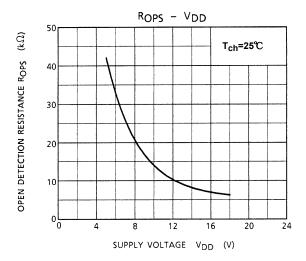






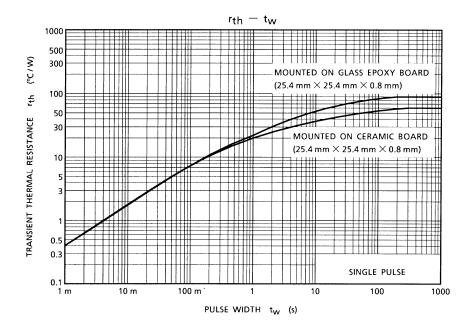






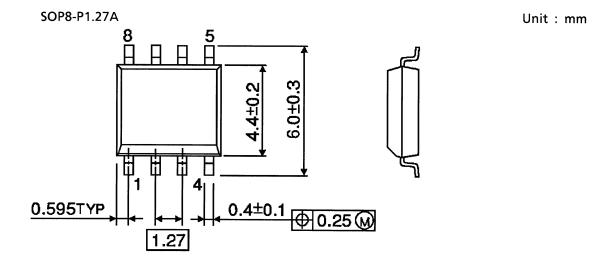
Precaution:

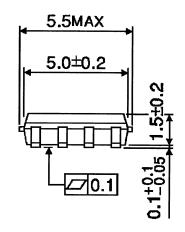
1. Since there is no built-in protection against reverse connection of batteries, etc., provide such protection using external circuits.

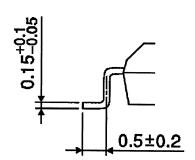


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Package Dimensions







Weight: 0.08 g (typ.)

RESTRICTIONS ON PRODUCT USE

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