

## 2ch ULTRA LOW NOISE LOW DROPOUT VOLTAGE REGULATOR

### ■ GENERAL DESCRIPTION

The NJM2898 is a 2ch ultra low noise low dropout voltage regulator designed for VCO Applications.

Advanced Bipolar technology achieves low noise, high ripple rejection and low quiescent current.

### ■ PACKAGE OUTLINE

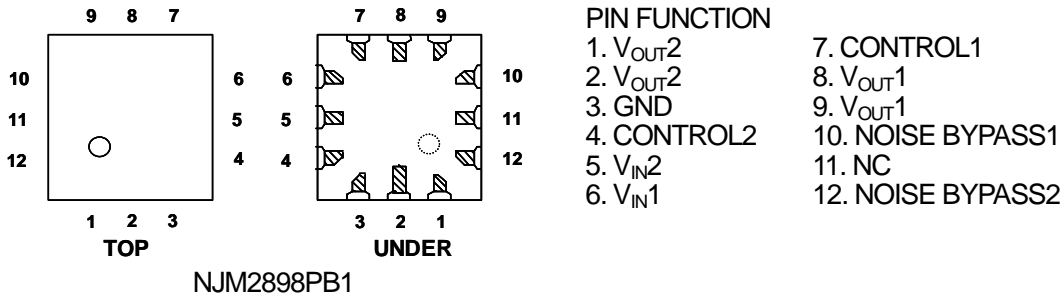


NJM2898PB1

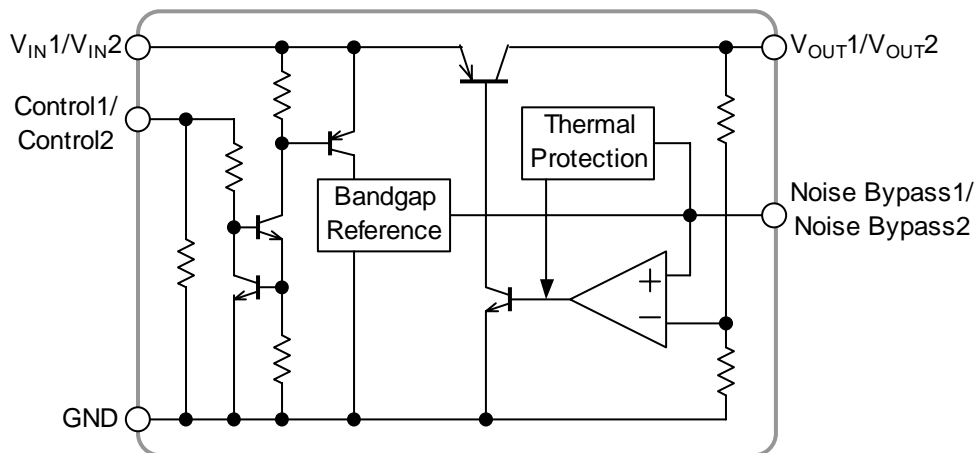
### ■ FEATURES

- High Ripple Rejection      75dB typ. (f=1kHz, Vo=3V Version)
- Output Noise Voltage      Vno=19μVrms typ. (Cp=0.01μF, Co=1.0μF(Ceramic))  
Vno=12μVrms typ. (Cp=0.1μF, Co=10μF(Tantalum))
- Output capacitor with 1.0uF ceramic capacitor
- Output Current              Io(max.)=100mA × 2ch
- High Precision Output      Vo±1.0%
- Low Dropout Voltage        0.10V typ. (Io=60mA)
- ON/OFF Control            (Active High)
- Internal Short Circuit Current Limit
- Internal Thermal Overload Protection
- Bipolar Technology
- Package Outline              FFP12-B1 (2.0×2.0×0.85mm)

### ■ PIN CONFIGURATION



### ■ EQUIVALENT CIRCUIT



# NJM2898

## ■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Input Voltage	V <sub>IN</sub>	+14	V
Control Voltage	V <sub>CONT</sub>	+14(*1)	V
Power Dissipation	P <sub>D</sub>	350(*2)	mW
Operating Temperature	Topr	-40 ~ +85	°C
Storage Temperature	Tstg	-40 ~ +125	°C

(\*1): When input voltage is less than +14V, the absolute maximum control voltage is equal to the input voltage.

(\*2): Mounted on glass epoxy board based on EIA/JEDEC. (114.3x76.2x1.6mm: 2Layers)

## ■ ELECTRICAL CHARACTERISTICS

(1CH/2CH: V<sub>IN</sub>=V<sub>o</sub>+1V, C<sub>IN</sub>=0.1μF, C<sub>o</sub>=1.0μF: V<sub>o</sub>≥2.7V (C<sub>o</sub>=2.2μF: V<sub>o</sub>≤2.6V), C<sub>p</sub>=0.01μF, Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V <sub>o</sub>	I <sub>o</sub> =30mA	-1.0%	—	+1.0%	V
Quiescent Current	I <sub>Q</sub>	I <sub>o</sub> =0mA, except I <sub>cont</sub> , per 1ch	—	120	180	μA
Quiescent Current at Control OFF	I <sub>Q(OFF)</sub>	V <sub>CONT</sub> =0V, per 1ch	—	—	100	nA
Output Current	I <sub>o</sub>	V <sub>o</sub> -0.3V	100	130	-	mA
Line Regulation	ΔV <sub>o</sub> /ΔV <sub>IN</sub>	V <sub>IN</sub> =V <sub>o</sub> +1V ~ V <sub>o</sub> +6V, I <sub>o</sub> =30mA	—	—	0.10	%/V
Load Regulation	ΔV <sub>o</sub> /ΔI <sub>o</sub>	I <sub>o</sub> =0 ~ 100mA	—	—	0.03	%/mA
Dropout Voltage	ΔV <sub>L-O</sub>	I <sub>o</sub> =60mA	—	0.10	0.18	V
Ripple Rejection	RR	e <sub>in</sub> =200mVrms, f=1kHz, I <sub>o</sub> =10mA, V <sub>o</sub> =3V Version	—	75	—	dB
Average Temperature Coefficient of Output Voltage	ΔV <sub>o</sub> /ΔTa	Ta=0 ~ 85°C, I <sub>o</sub> =10mA	—	± 50	—	ppm/°C
Output Noise Voltage1	V <sub>NO1</sub>	f=10Hz ~ 80kHz, I <sub>o</sub> =10mA, C <sub>p</sub> =0.01μF, C <sub>o</sub> =1.0μF(Ceramic), V <sub>o</sub> =3V Version	—	19	—	μVrms
Output Noise Voltage2	V <sub>NO2</sub>	f=10Hz ~ 80kHz, I <sub>o</sub> =10mA, C <sub>p</sub> =0.1μF, C <sub>o</sub> =10μF(Tantalum), V <sub>o</sub> =3V Version	—	12	—	μVrms
Control Voltage for ON-state	V <sub>CONT(ON)</sub>		1.6	—	—	V
Control Voltage for OFF-state	V <sub>CONT(OFF)</sub>		—	—	0.6	V

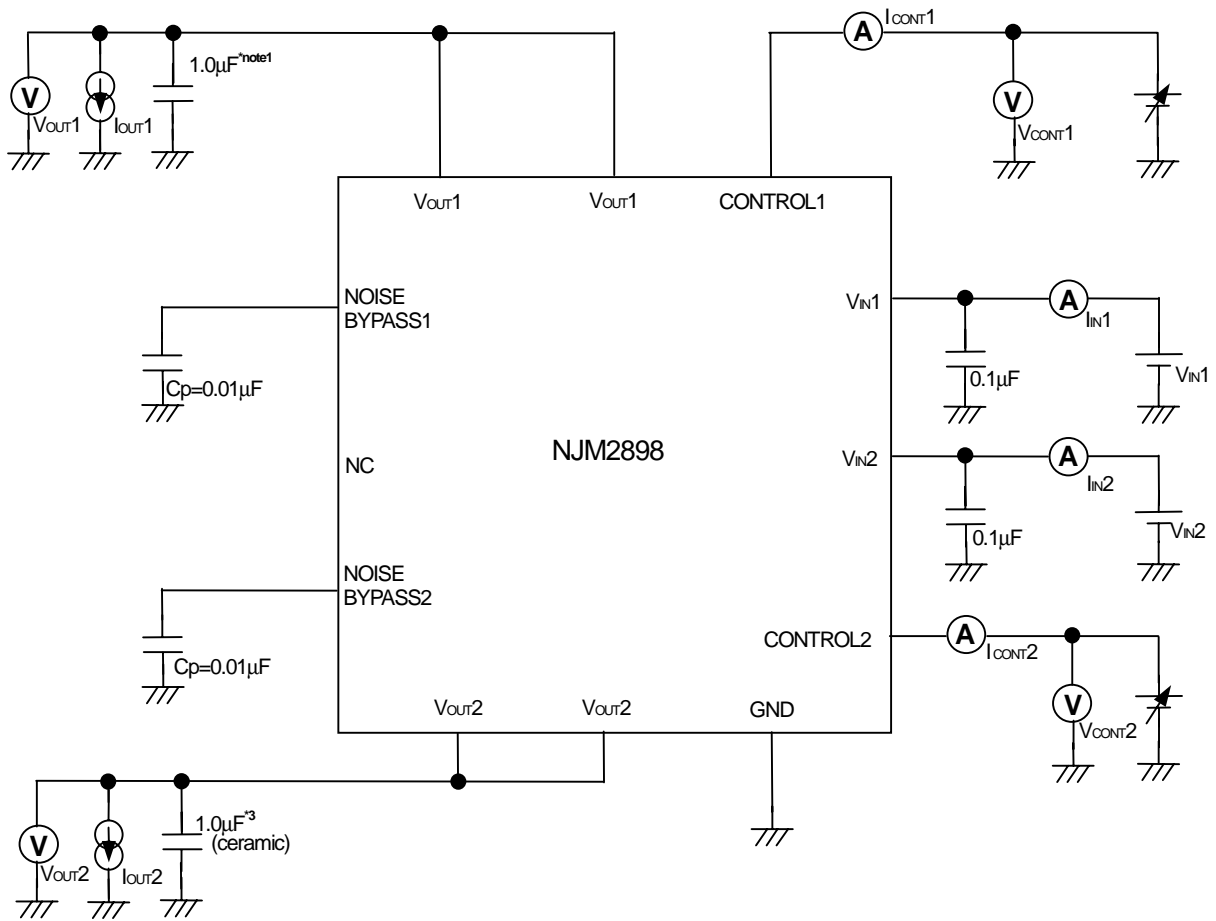
The above specification is a common specification for all output voltages.

Therefore, it may be different from the individual specification for a specific output voltage.

## ■ OUTPUT VOLTAGE RANK LIST

Device Name	V <sub>OUT</sub>	
	CH1	CH2
NJM2898PB1-2828	2.8V	2.8V
NJM2898PB1-JJ	2.85V	2.85V
NJM2898PB1-0303	3.0V	3.0V
NJM2898PB1-0521	5.0V	2.1V

## ■ TEST CIRCUIT

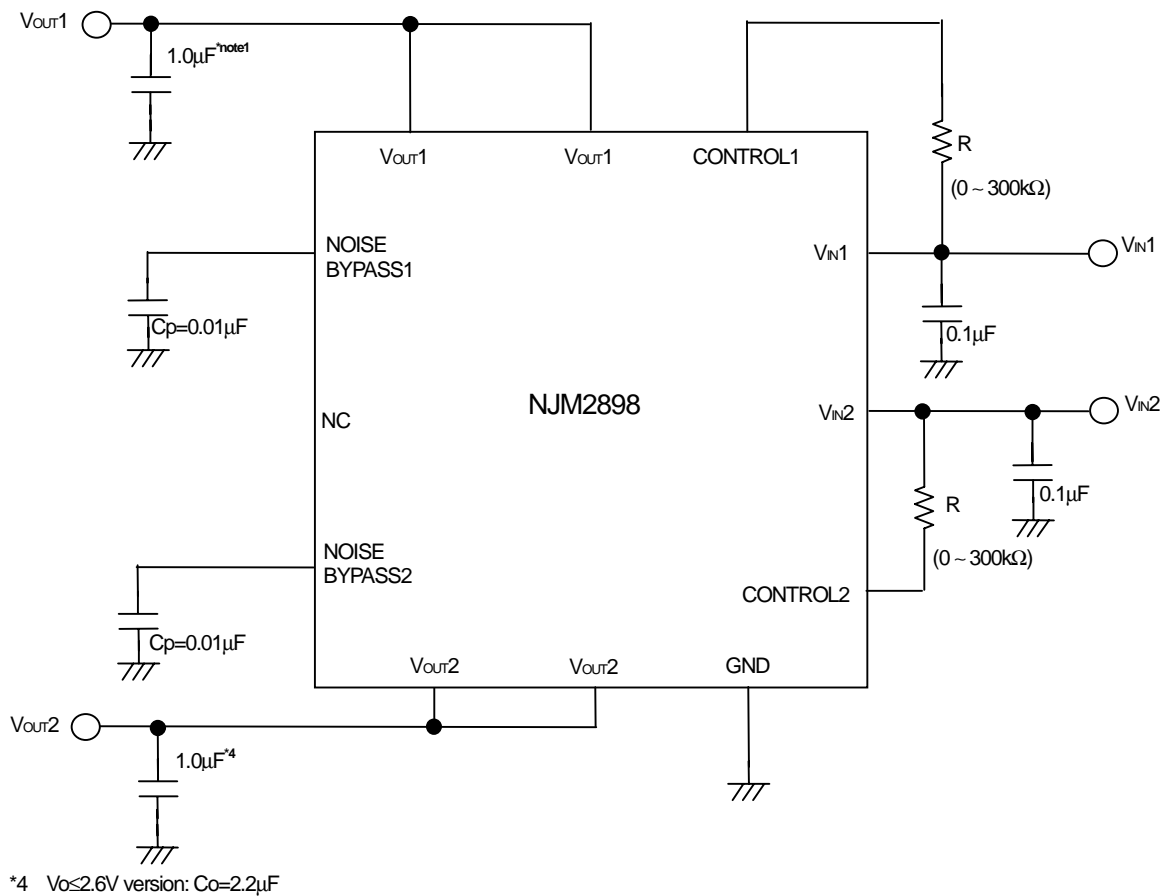


\*3  $V_o \leq 2.6V$  version:  $C_o = 2.2\mu F$  (ceramic)

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## ■ TYPICAL APPLICATION

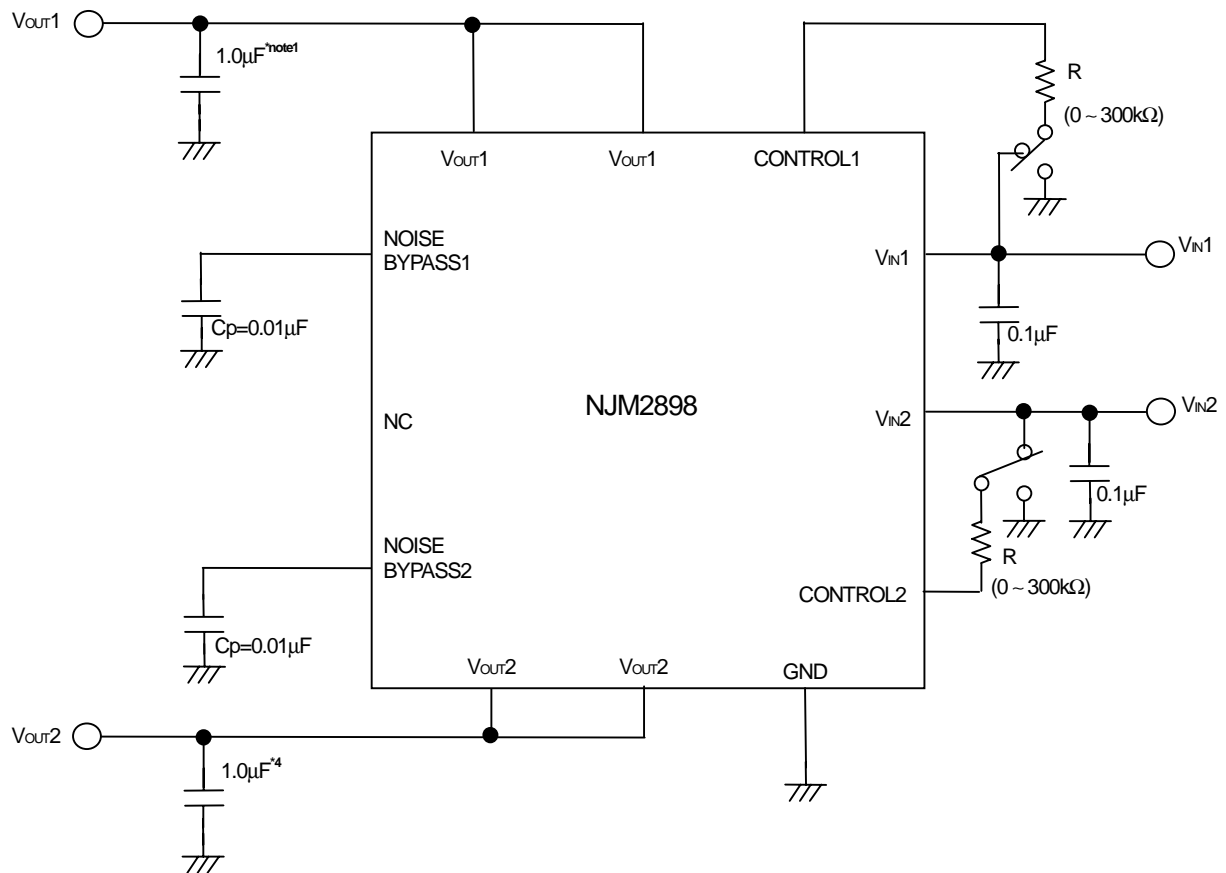
① In the case where ON/OFF Control is not required:



Connect control terminal to  $V_{IN}$  terminal

The quiescent current can be reduced by using a resistance "R". Instead, it increases the minimum operating voltage. For further information, please refer to Figure "Output Voltage vs. Control Voltage".

② In use of ON/OFF CONTROL:



\*4  $V_{OS} \leq 2.6V$  version:  $C_o = 2.2\mu F$

State of control terminal:

- “H” → output is enabled.
- “L” or “open” → output is disabled.

**\*Noise bypass Capacitance Cp**

Noise bypass capacitance Cp reduces noise generated by band-gap reference circuit.

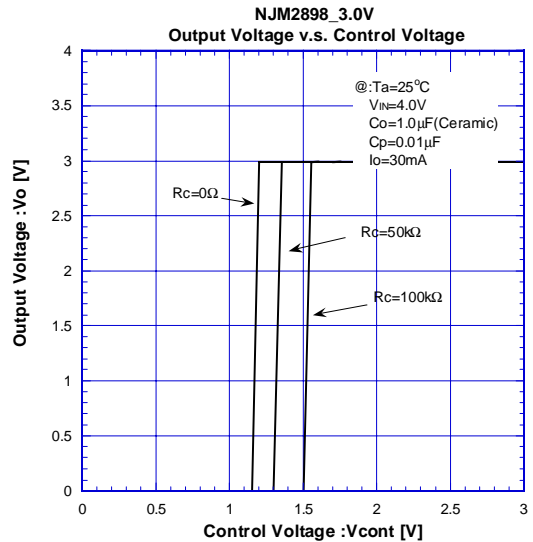
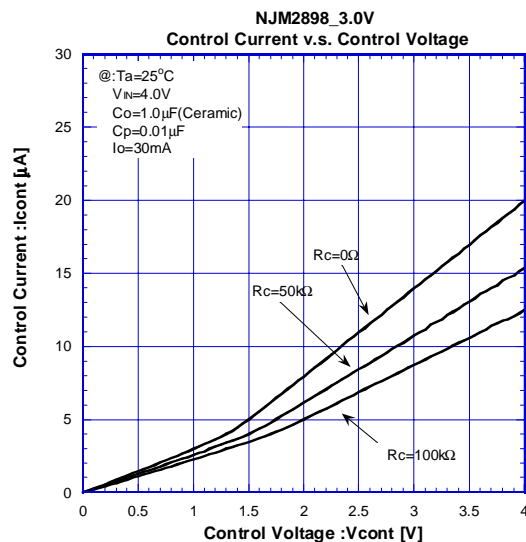
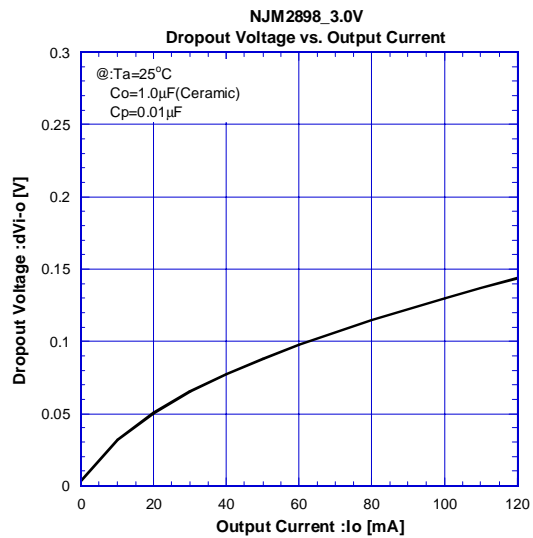
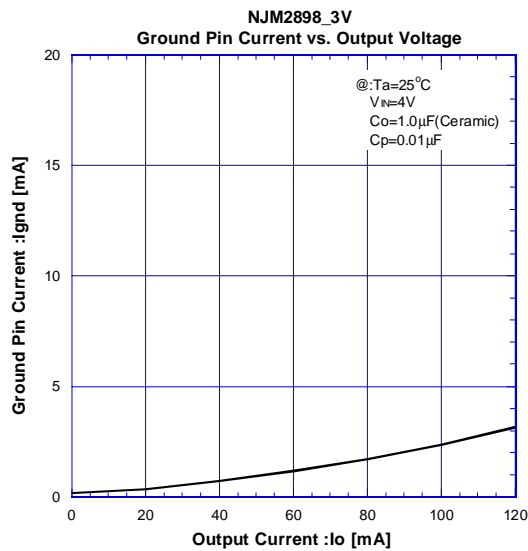
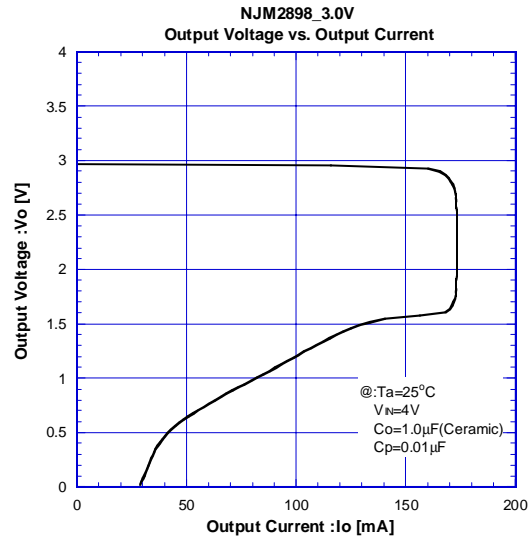
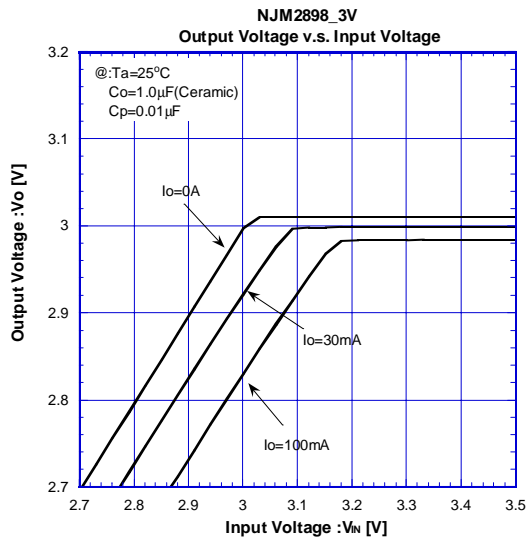
Noise level and ripple rejection will be improved when larger Cp is used.

Use of smaller Cp value may cause oscillation.

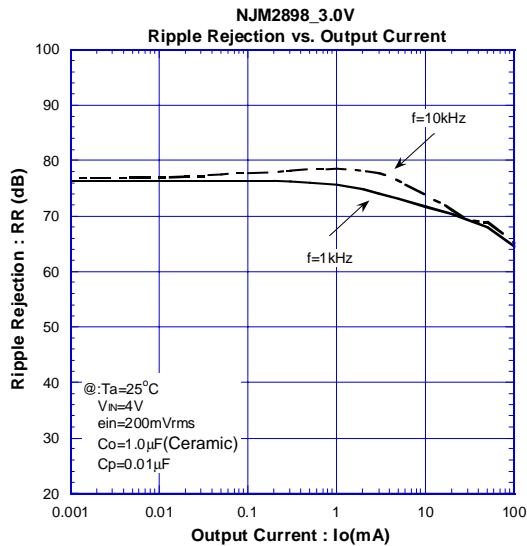
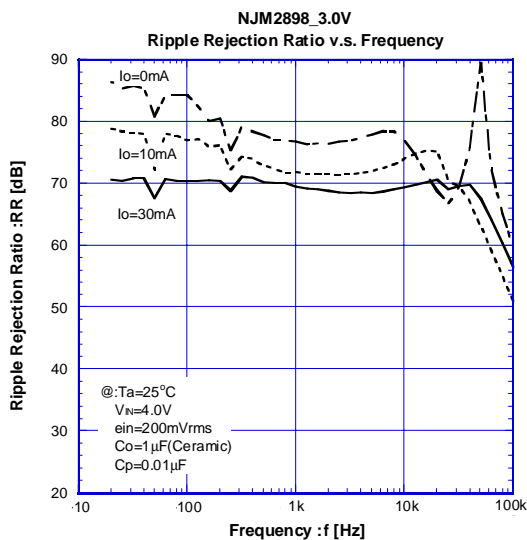
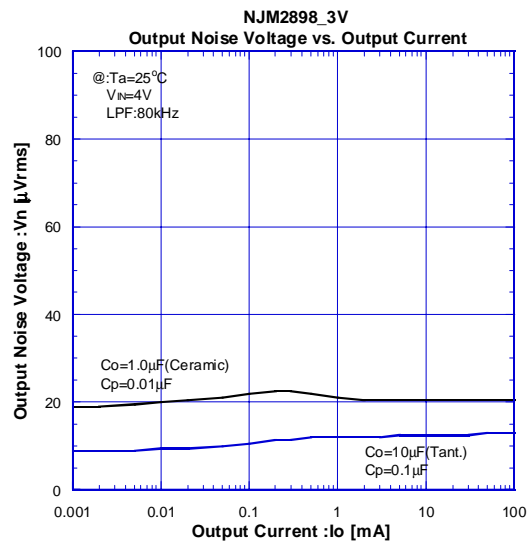
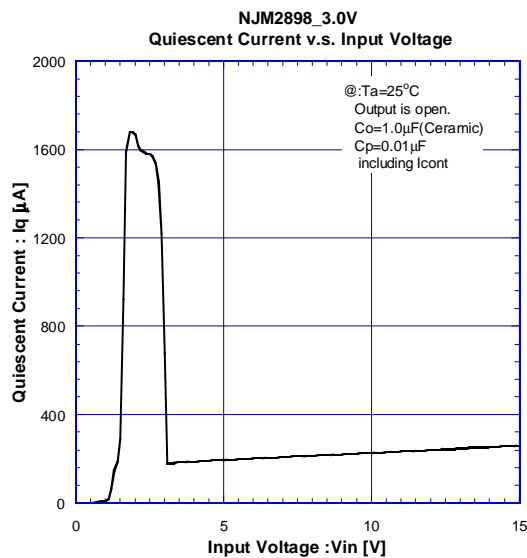
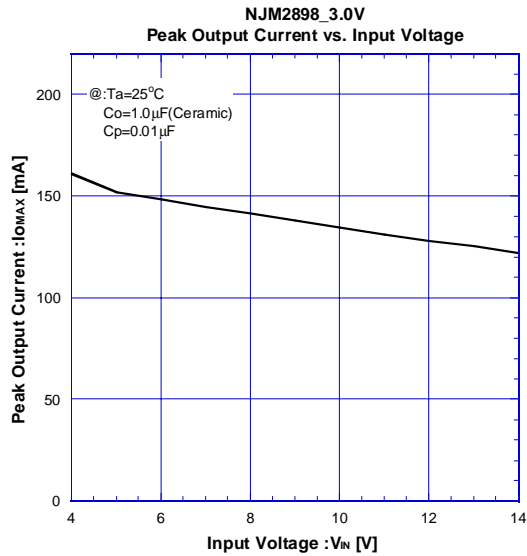
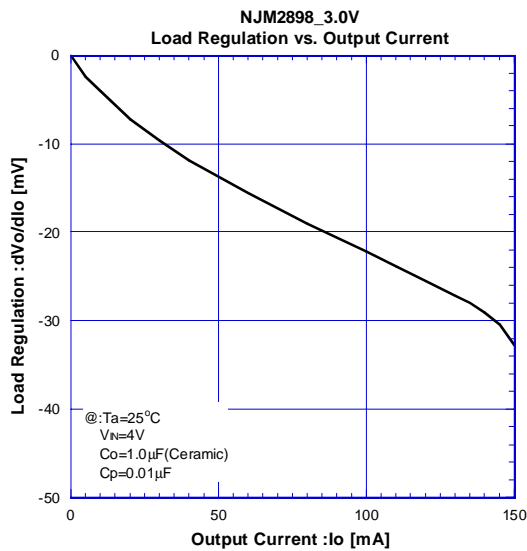
Use the Cp value of 0.01uF greater to avoid the problem.

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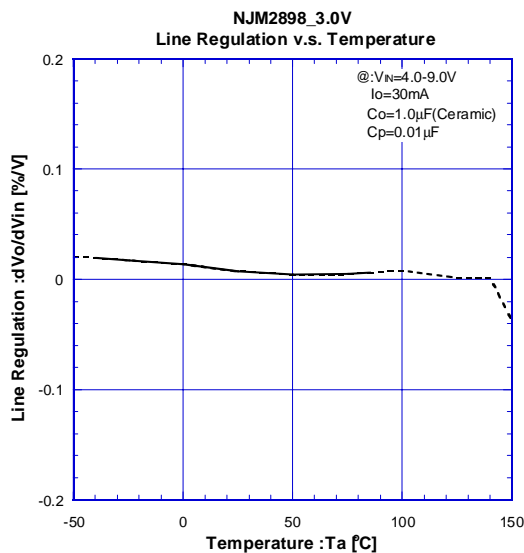
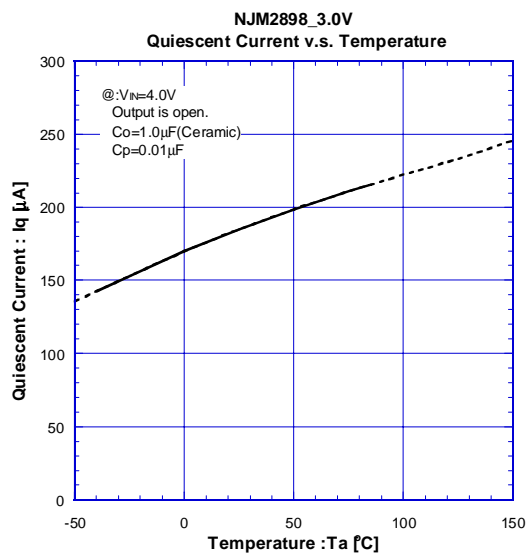
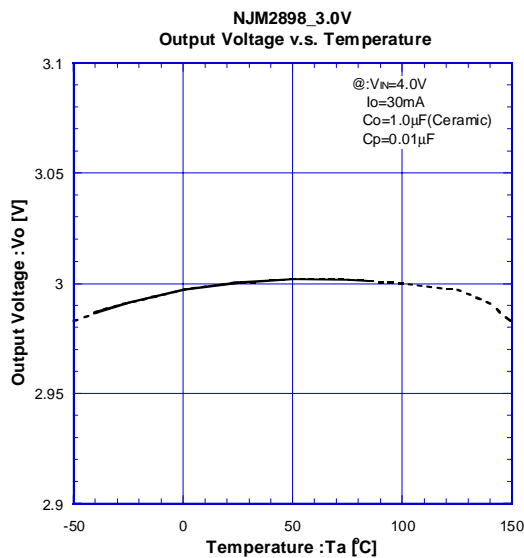
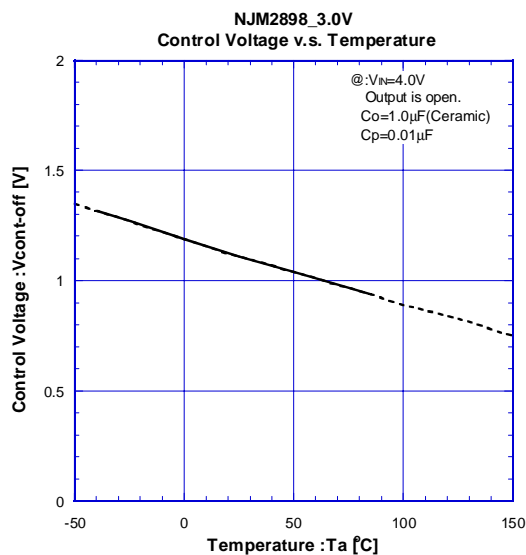
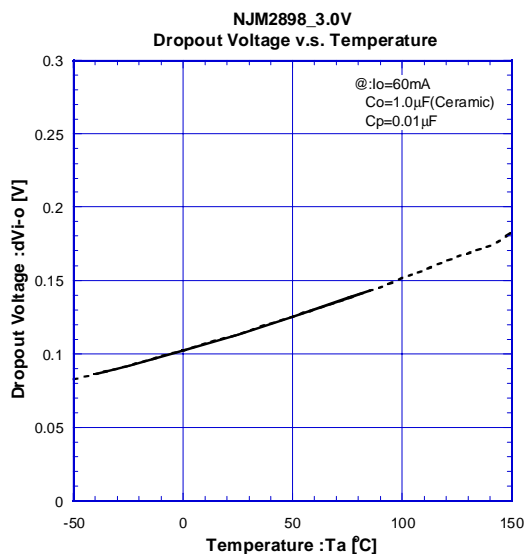
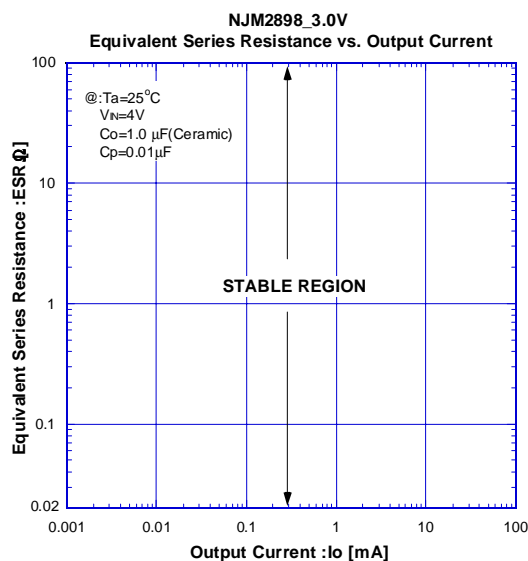
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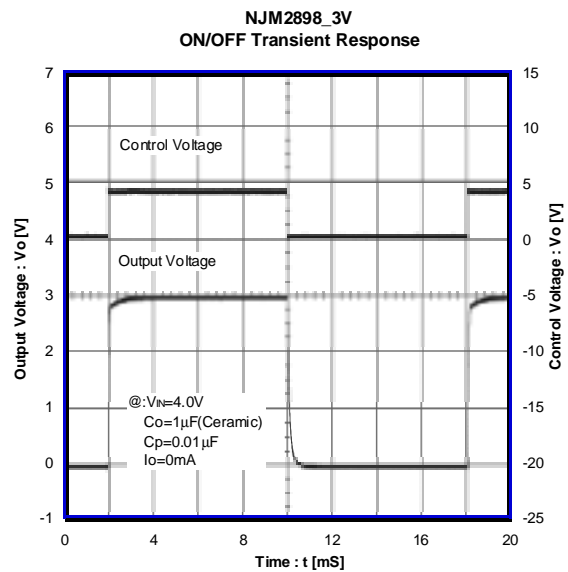
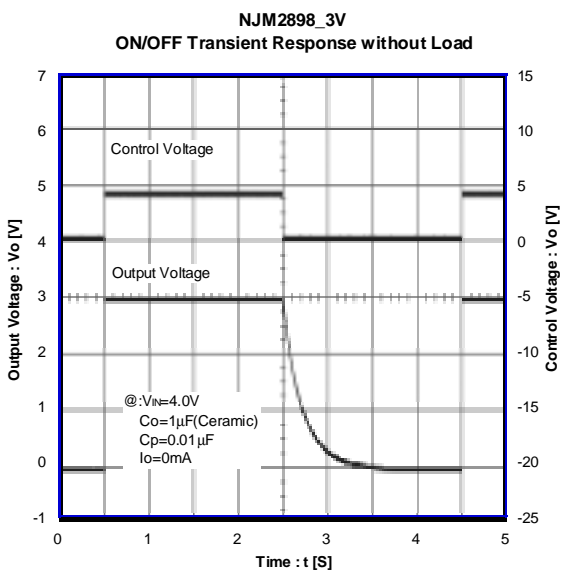
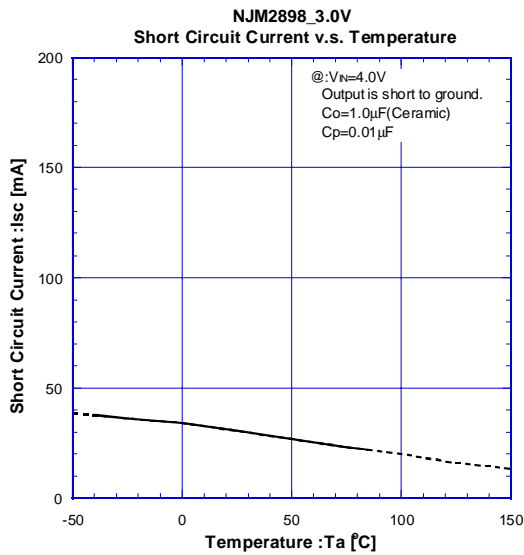
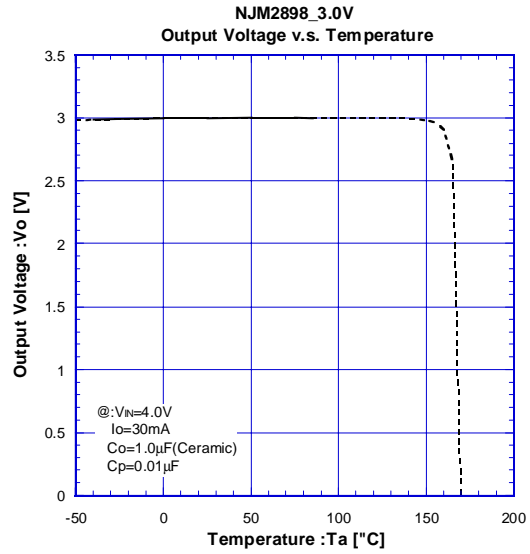
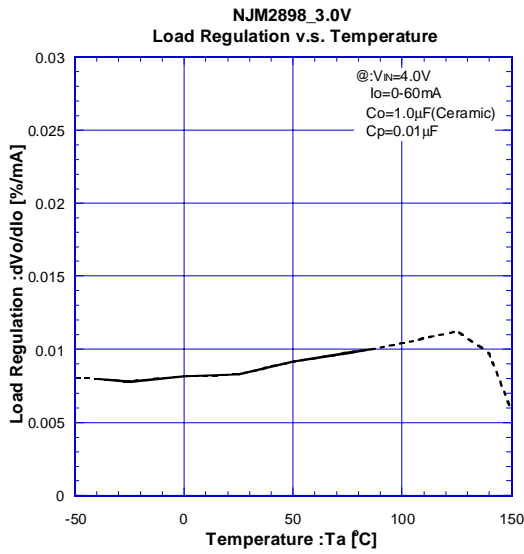


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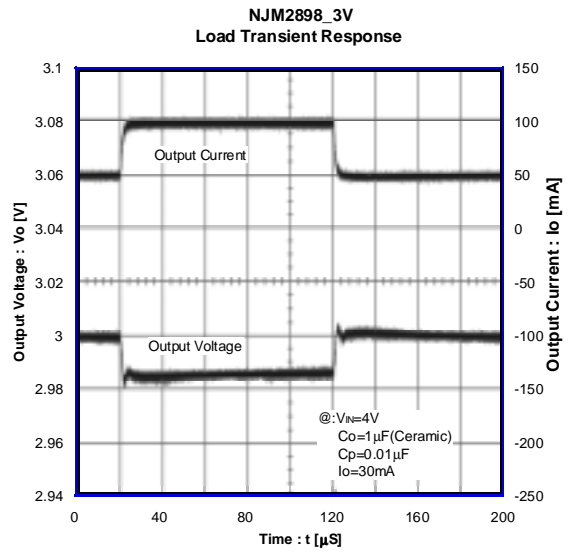
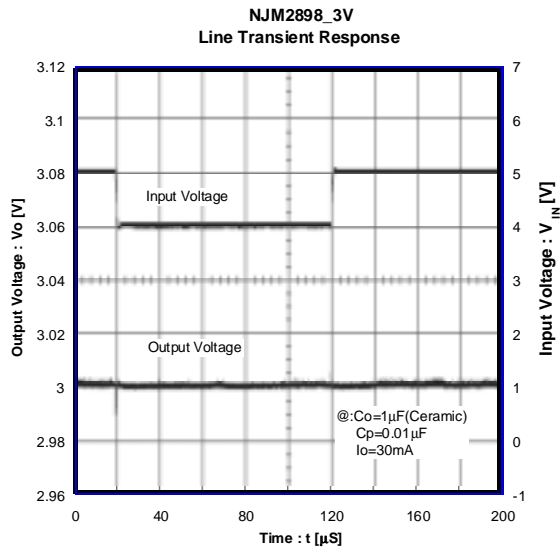




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**[CAUTION]**

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