MCP37X3X-200
16-Bit 200 Msps ADC VTLA
Evaluation Kit
User's Guide

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Object of Declaration: MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Kit

EU Declaration of Conformity

Manufacturer: Microchip Technology Inc.
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This declaration of conformity is issued by the manufacturer.

The development/evaluation tool is designed to be used for research and development in a laboratory environment. This development/evaluation tool is not a Finished Appliance, nor is it intended for incorporation into Finished Appliances that are made commercially available as single functional units to end users under EU EMC Directive 2004/108/EC and as supported by the European Commission's Guide for the EMC Directive 2004/108/EC (8th February 2010).

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Signed for and on behalf of Microchip Technology Inc. at Chandler, Arizona, USA

Derek Carlson   12-Sep-14
VP Development Tools   Date

MCP37X3X-200 16-BIT 200 MSPS ADC VTLA EVALUATION KIT USER'S GUIDE

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Preface

NOTICE TO CUSTOMERS

All documentation becomes dated, and this manual is no exception. Microchip tools and documentation are constantly evolving to meet customer needs, so some actual dialogs and/or tool descriptions may differ from those in this document. Please refer to our web site (www.microchip.com) to obtain the latest documentation available.

Documents are identified with a "DS" number. This number is located on the bottom of each page, in front of the page number. The numbering convention for the DS number is "DSXXXXXXXXA", where "XXXXXXXX" is the document number and "A" is the revision level of the document.

For the most up-to-date information on development tools, see the MPLAB® IDE on-line help. Select the Help menu, and then Topics to open a list of available on-line help files.

INTRODUCTION

This chapter contains general information that will be useful to know before using the MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Kit. Items discussed in this chapter include:

- Document Layout
- Conventions Used in this Guide
- Recommended Reading
- The Microchip Web Site
- Customer Support
- Document Revision History

DOCUMENT LAYOUT

This document describes how to use the MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Kit to demonstrate the performance of the MCP37X2X-200 and MCP37X3X-200 device families. The manual layout is as follows:

- **Chapter 1. "Product Overview"** – Important information about the MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Kit.
- **Chapter 2. "Installation and Operation"** – Includes instructions on how to get started with the MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Kit.
- **Appendix A. "Schematics and Layouts"** – Refer to the board's web page for the complete Schematics and Layouts.
- **Appendix B. "Bill of Materials (BOM)"** – Refer to the board's web page for the complete Bill of Materials.

CONVENTIONS USED IN THIS GUIDE

This manual uses the following documentation conventions:

DOCUMENTATION CONVENTIONS

Description	Represents	Examples
Arial font:		
Italic characters	Referenced books	*MPLAB® IDE User's Guide*
	Emphasized text	...is the *only* compiler...
Initial caps	A window	the Output window
	A dialog	the Settings dialog
	A menu selection	select Enable Programmer
Quotes	A field name in a window or dialog	"Save project before build"
Underlined, Italic text with right angle bracket	A menu path	*File>Save*
Bold characters	A dialog button	Click **OK**
	A tab	Click the **Power** tab
N'Rnnnn	A number in verilog format, where N is the total number of digits, R is the radix and n is a digit.	4'b0010, 2'hF1
Text in angle brackets < >	A key on the keyboard	Press <Enter>, <F1>
Courier New font:		
Plain Courier New	Sample source code	`#define START`
	Filenames	`autoexec.bat`
	File paths	`c:\mcc18\h`
	Keywords	`_asm, _endasm, static`
	Command-line options	`-Opa+, -Opa-`
	Bit values	`0, 1`
	Constants	`0xFF, 'A'`
Italic Courier New	A variable argument	`file`.o, where `file` can be any valid filename
Square brackets [ ]	Optional arguments	`mcc18 [options] file [options]`
Curly brackets and pipe character: { | }	Choice of mutually exclusive arguments; an OR selection	`errorlevel {0|1}`
Ellipses...	Replaces repeated text	`var_name [, var_name...]`
	Represents code supplied by user	`void main (void) { ... }`

RECOMMENDED READING

This user's guide describes how to use the MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Kit. Another Other document is listed below. The following Microchip document is available and recommended as a supplemental reference resource.

- **MCP37221/31-200 and MCP37D21/31-200 Data Sheet –** *"200 Msps, 14-/16-Bit Low-Power ADC with 8-Channel MUX"* **(DS20005322)**

THE MICROCHIP WEB SITE

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- **General Technical Support** – Frequently Asked Questions (FAQs), technical support requests, online discussion groups, Microchip consultant program member listing
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- Technical Support

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Technical support is available through the web site at:
http://www.microchip.com/support.

DOCUMENT REVISION HISTORY

Revision B (November 2014)

- Changed document and device name to MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Kit.
- Removed schematics in **Appendix A. "Schematics and Layouts"**.
- Added a Notice to Customers in **Appendix A. "Schematics and Layouts"**.
- Minor typographical corrections.

Revision A (July 2014)

- Initial Release of this Document.

NOTES:

Chapter 1. Product Overview

1.1 INTRODUCTION

This chapter provides an overview of the MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Kit and covers the following topics:

- What is the MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Board (ADM00505)?
- What is the MCP37XXX High-Speed Pipeline ADC Data Capture Card (ADM00506)?
- What is the Pipeline ADC Utility Software?
- MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Kit Requirements

1.2 WHAT IS THE MCP37X3X-200 16-BIT 200 MSPS ADC VTLA EVALUATION BOARD (ADM00505)?

The MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Board (ADM00505) is intended to demonstrate the performance of the MCP37X2X-200 and MCP37X3X-200 device families. This evaluation board is used together with:

- MCP37XXX High-Speed Pipeline ADC Data Capture Card (ADM00506)
- Pipeline ADC Utility Software

Refer to Figure 1-1 for the system setup.

Note 1: The ADC part is available on this board.
2: This board collects ADC data from the evaluation board and passes it to the PC GUI.
3: This GUI allows the user to test the performance of the MCP37X3X-200 device.
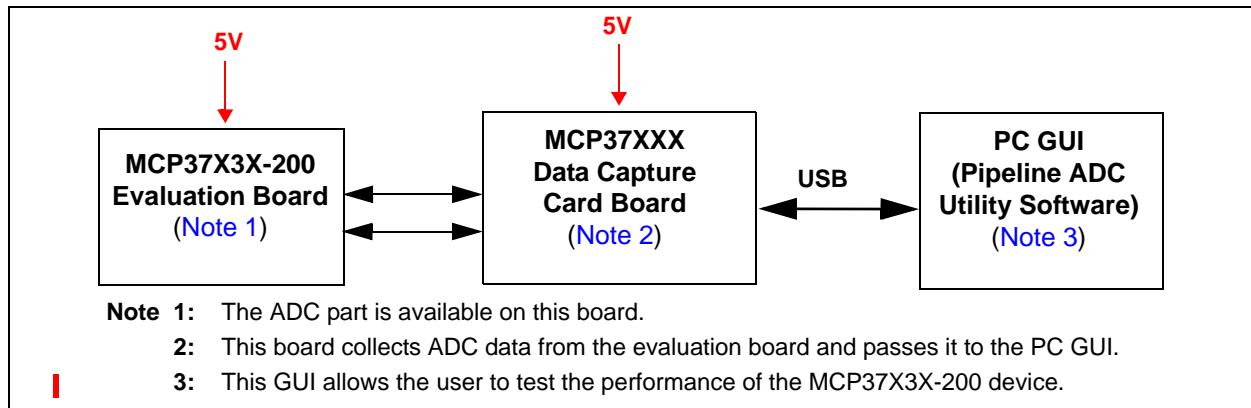
FIGURE 1-1:   *Evaluation Kit System Setup.*

This evaluation board supports the following Microchip high-speed ADC device families:

- The MCP37X2X-200/MCP37X3X-200, which is Microchip Technology Inc.'s baseline 14-/16-bit 200 Msps pipelined analog-to-digital converter (ADC) device family, featuring a built-in high-order digital decimation filter, gain and offset adjustment per channel, and fractional delay recovery.
- The MCP37D2X-200/MCP37D3X-200 device family, which features digital down-conversion and CW beamforming capability, in addition to the features offered by the baseline device family.

The MCP37X3X-200 16-bit 200 Msps ADC VTLA Evaluation Board (ADM00505) comes with the MCP37D31-200 device on board. All conditions and features can be evaluated using this device. Refer to Figure 1-2 for close-up photos of the evaluation board.

(a) Top View

MCP37D31-200
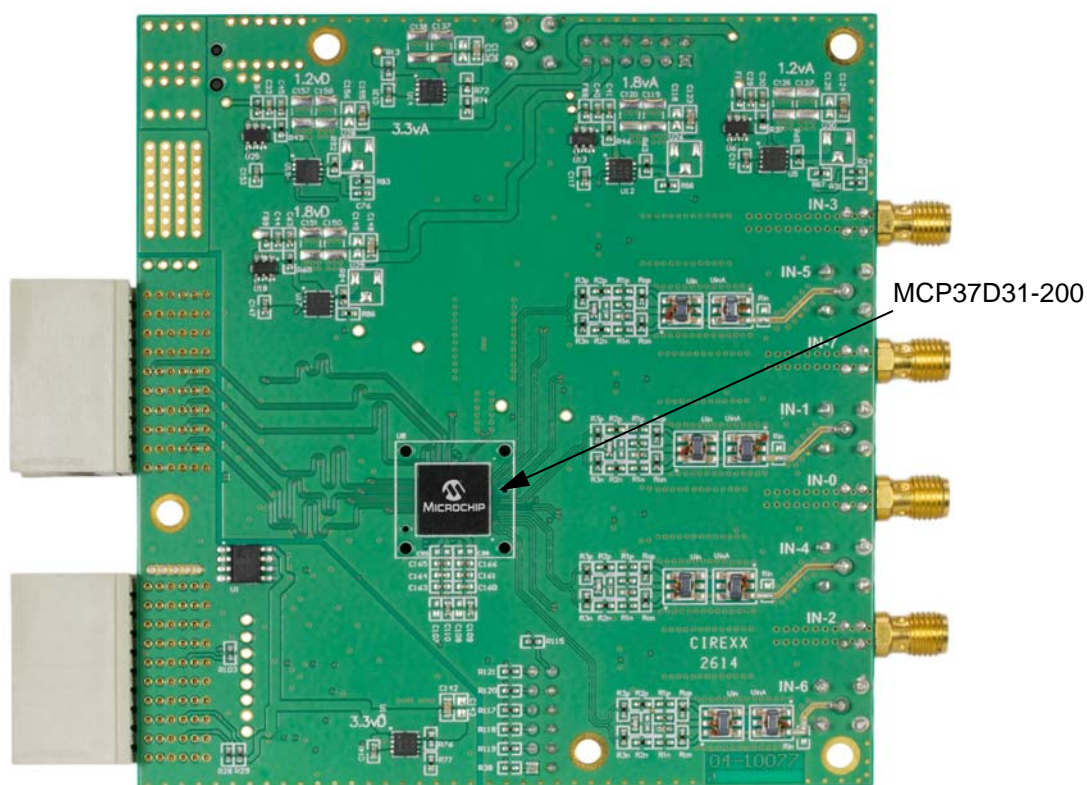
(b) Bottom View

FIGURE 1-2: *MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Board (ADM00505).*

1.3 WHAT IS THE MCP37XXX HIGH-SPEED PIPELINE ADC DATA CAPTURE CARD (ADM00506)?

The MCP37XXX High-Speed Pipeline ADC Data Capture Card (ADM00506) is an FPGA-based memory buffer for the digital data received from the Analog-to-Digital Converter (ADC) evaluation board. Figure 1-1 shows the connection of the data capture card directly between the Evaluation Board and the PC GUI.

The data capture card connects to the PC through a USB cable, providing the user with two functionalities:

• The ability to send user commands directly to the device from the PC GUI.
• The ability to collect data from the evaluation board and send it to the PC GUI.

Refer to Figure 1-3 for close-up photos of the data capture card.
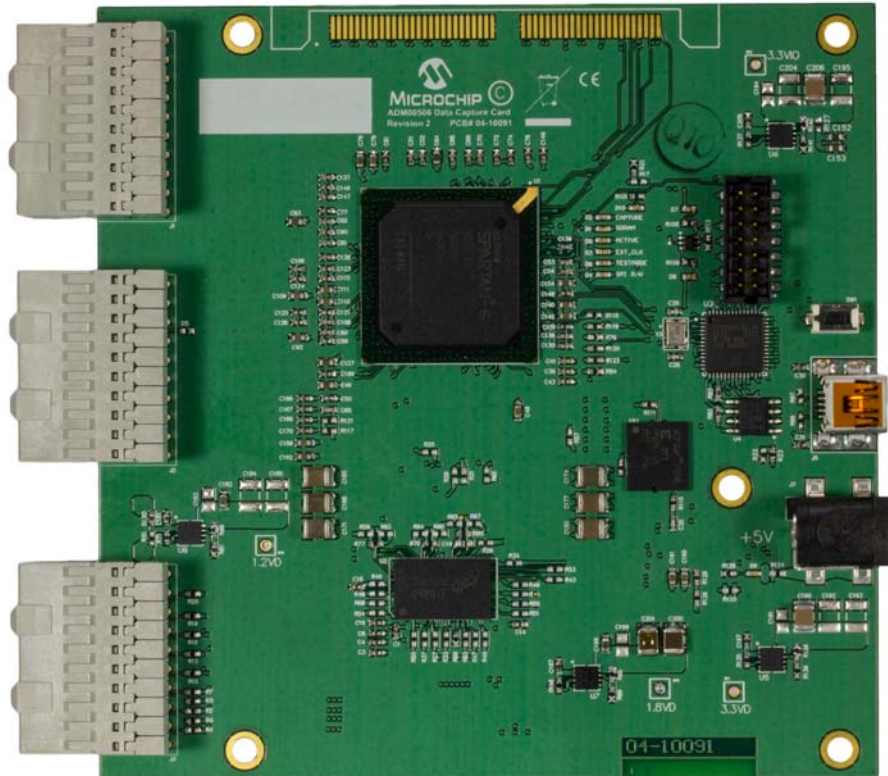
(a) Top View
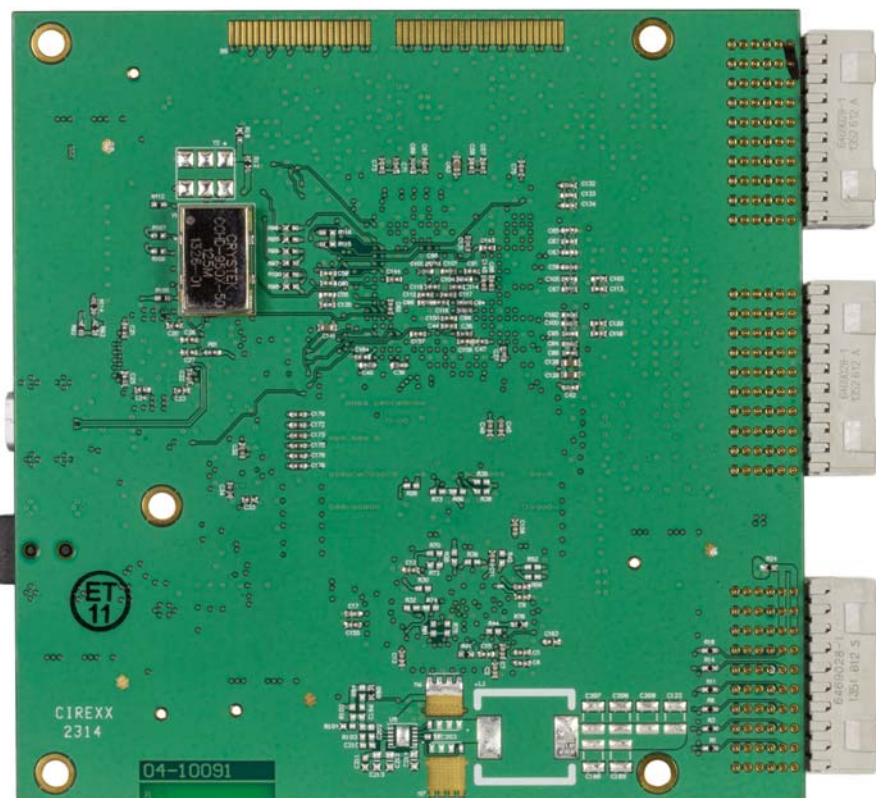
(b) Bottom View

FIGURE 1-3: *MCP37XXX High-Speed Pipeline ADC Data Capture Card (ADM00506).*

1.4   WHAT IS THE PIPELINE ADC UTILITY SOFTWARE?

The Pipeline ADC Utility software is the graphical user interface (GUI) used to communicate with and to configure the operating parameters of the device. The software communicates with the part through the data capture card via a USB cable, allowing the user to program the internal ADC registers. When the user interacts with the software (for example, by updating the registers), the user's commands are passed to the MCP37X3X-200 device via the data capture card. Once the commands are executed by the evaluation board, the software receives the requested data from the data capture card. The software will then analyze the data, perform a FFT or other analysis, and display the results. Refer to Figure 1-1 for a diagram of the system setup. Figure 1-4 shows a screen shot of the graphical interface.
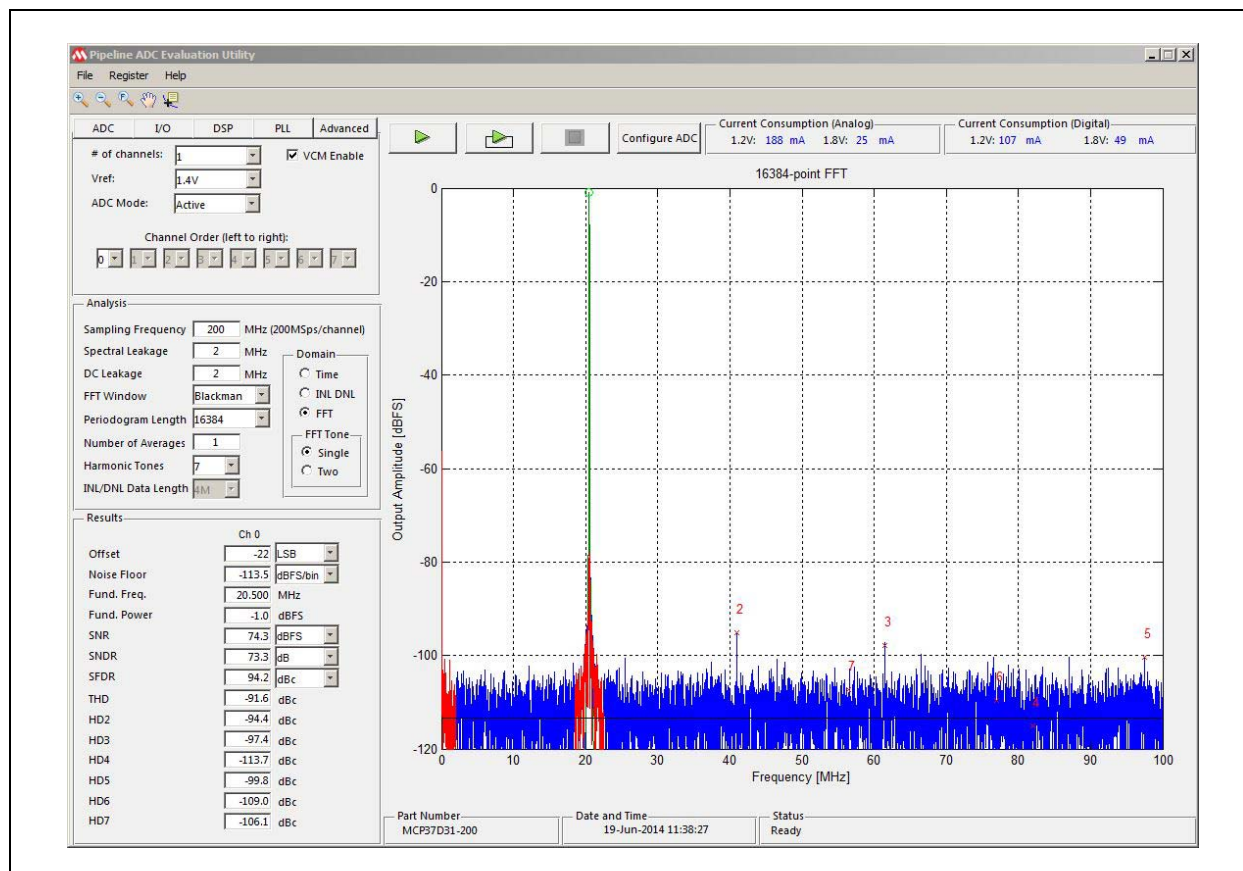
FIGURE 1-4:   *Pipeline ADC Utility Software Displaying a Typical FFT Waveform.*

The Pipeline ADC Utility software is available for download on Microchip's web site. For instructions on using the GUI, refer to the software's supporting documentation included with the installation file, as well as within the application Help menu.

1.5   MCP37X3X-200 16-BIT 200 MSPS ADC VTLA EVALUATION KIT REQUIREMENTS

The evaluation kit requires the following:
- MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Board (ADM00505)
- MCP37XXX High-Speed Pipeline ADC Data Capture Card (ADM00506)
  - Sold separately
- Two 5V Wall-Mountable Switching Power Supplies
  - Included with the evaluation board and data capture card

- USB-A to USB Mini-B Cable
  - Included with the data capture card
- Pipeline ADC Utility Software
  - Available on the Microchip web site
- Important Information Sheet

Chapter 2. Installation and Operation

2.1 MCP37X3X-200 16-BIT 200 MSPS ADC VTLA EVALUATION KIT FEATURES

This MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Kit is a fully assembled, programmed and tested solution (with the exception of a clock source) to evaluate and demonstrate the MCP37X2X-200/MCP37X3X-200 operating performance.

The evaluation kit has the following features:

- Programmable Full-Scale Analog Input Range
- Dynamic Performance Monitoring
- Evaluation of Performance Metrics, such as: SNR, SFDR, INL, DNL etc.
- User-Programmable Registers
- Power Consumption Evaluation
- Digital Post Processing Options: Decimation Filter, Digital Down-Conversion, CW Beamforming, etc.

The evaluation kit features a sample rate of up to 200 Msps. Since the device can support up to 8 input channels, the effective sample rate per channel is a function of the number of channels enabled. Refer to Table 2-1 for a detailed list of the input sample rate per channel.

For information about the MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Kit features, refer to the MCP37221-200/MCP37231-200/MCP37D21-200/MCP37D31-200 Data Sheet.

TABLE 2-1: INPUT SAMPLE RATE VS. NUMBER OF CHANNELS SELECTED

No. of Channels Selected	Sample Rate per Channel (in Msps)
1	200
2	100
3	66.67
4	50
5	40
6	33.33
7	28.57
8	25

2.2 GETTING STARTED

2.2.1 Configuration Requirements

To power-up and run the evaluation kit, the following are required:

- Pipeline ADC Utility Software
- MCP37X3X-200 16-Bit 200 Msps ADC VTLA Evaluation Board (ADM00505)
- MCP37XXX High-Speed Pipeline ADC Data Capture Card (ADM00506)
- Two 5V Wall-Mountable Switching Power Supplies
- A USB-A to USB Mini-B Cable

2.2.2 Evaluation Kit Set-Up

1. Connect the evaluation board and the data capture card as shown in Figure 2-1.
2. Power up both boards using the supplied 5V Power Supplies.
3. Connect the data capture card to a computer using the provided USB cable.
4. Press the reset button on both boards simultaneously.
5. Run the Pipeline ADC Utility Software.

For help with troubleshooting the boards and operating the GUI, please refer to the software's supporting documentation.

WARNING

Avoid connecting a power supply with a voltage greater than 5V. Doing so can damage the voltage regulators, requiring them to be replaced.

2.2.3 Power Input Connection

This evaluation kit comes with two Mean Well wall-mountable switching power supplies (GS06U-1P1J), one for each of the boards. These power supplies are able to provide a 5V, 1A maximum output. They can be connected to an AC wall outlet rated between 90V AC and 264V AC, at a frequency of 47 Hz to 63 Hz. The other end of the power supply is a 2.1 mm barrel plug that connects to both the evaluation board and the data capture card (see Figure 2-1). If the user chooses to connect a different external power supply, a minimum output of 750 mA is required.

The power domains on the evaluation board may be merged, if needed. Specifically, the 1.2VA and 1.2VD power domains may be merged by populating a ferrite bead on FB10. Similarly, the 1.8VA and 1.8VD power domains may be merged by populating a ferrite bead on FB11. In both cases, the digital regulators should be disabled using the appropriate switches.

The evaluation board's power supplies can also be supplied by external voltages directly to the power domains. This can be done by disabling the desired regulator using the dip switch SW2 - Regulator Enable on the silkscreen. Then, a DC voltage can be driven directly into the appropriate pin of header J16. Note that the even pins of the J16 header are GND.

WARNING

Avoid applying an input signal while the board is powered off. This may damage the input baluns! If a high second harmonic is seen, it is likely that these baluns need to be replaced.

Ferrite Locations
FB10 and FB11

Power
Header

External
Clock
Input

Unpopulated
Crystal Oscillator
Trace (Y1/Y2)

Power
Supply
Input (5V)

Xilinx
FPGA

Regulator
Switches

Data Capture
Card Reset
Switch

Analog
Inputs

USB
Input

Power
Supply
Input (5V)

ADC Device
Under Test (bottom side of board)

Eval. Board
Reset Switch

Board
Connectors

**MCP37X3X-200 16-Bit 200 Msps ADC
VTLA Evaluation Board (ADM00505)**

**MCP37XXX High-Speed Pipeline
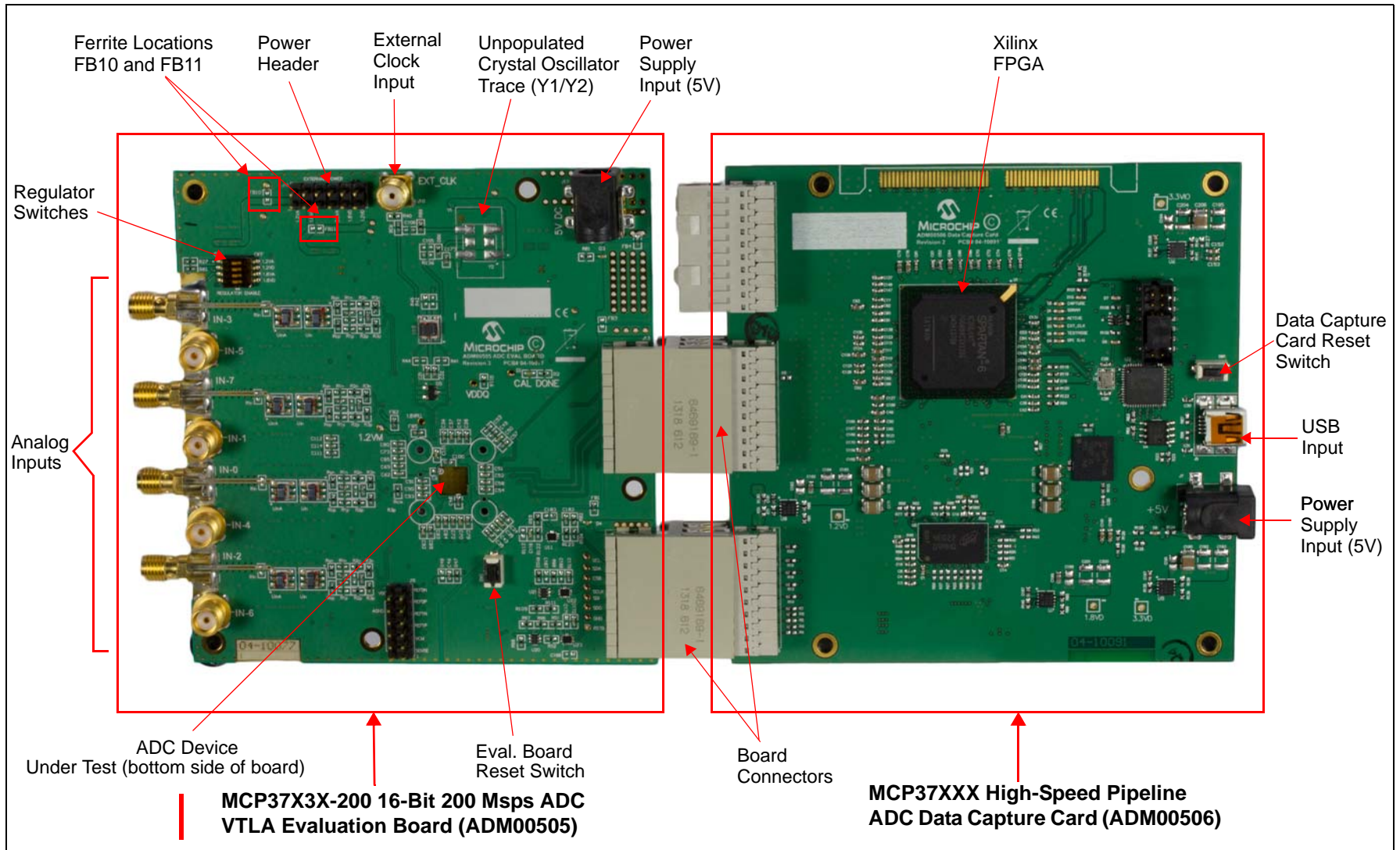ADC Data Capture Card (ADM00506)**

FIGURE 2-1: *Evaluation Kit - The evaluation board connected to the data capture card.*

2.2.4   Input/Output Optimization

2.2.4.1   OPTIMIZING THE CLOCK CONNECTION

The evaluation board does not come with a clock source for the device. The user must provide a clock source in order to run this evaluation board. The user has two options:

- A clean and low-jitter clock signal can be connected to the external clock SMA input (see Figure 2-1). For the signal generation, Rohde & Schwarz® SMA or Agilent Technologies HP8644B signal generators can be used. To ensure that a clean and low-jitter clock signal is provided, the user should interpose a band-pass filter to reduce the phase noise coming from the signal source. A TTE® Inc. KC4T-200M-10P-50-3A filter can be used with a 200 MHz clock signal.
- Alternatively, an unpopulated trace is available on the board, where the user can place a crystal oscillator. For a single-ended clock, refer to the instructions in Example 1. For a differential clock, refer to the instructions in Example 2. The user may also choose to place another type of clock as well. Note that the component values given below are for example only and may vary depending on the exact clock source used.

Example 1

A clock oscillator with a single ended output may be populated on Y1. The use of the Crystek CCHD-950 series is recommended, such as CCHD-950-50-100.000 for a 100 MHz clock. In this case, proceed to the following modifications:

1. Remove R36
2. Populate R89 with a 50Ω resistor
3. Populate C106 with a 22 pF capacitor.

Example 2

A clock oscillator with a differential LVPECL output may be populated on Y2. The use of the Crystek CCPD-034 series is recommended, such as the CCPD-034-50-200.000 for a 200 MHz clock. In this case, proceed to the following modifications:

1. Remove R36 and R136
2. Populate R89 and R90 with a 210Ω resistor
3. Populate C105 and C106 with a 22 pF capacitor.

2.2.4.2   OPTIMIZING THE ANALOG INPUT CONNECTION

The best way to evaluate the MCP37X3X-200 device is to use a clean analog input signal with as little noise as possible. The signal can be provided using a signal generator, such as Rohde & Schwarz SMA or Agilent Technologies HP8644B, and can be used along with a bandpass filter. The filter will help with filtering the harmonic tones of the fundamental, as well as with reducing the phase noise of the source. Two such filters that can be used are:

- TTE Inc. KC4T-70M-15P-50-69A for 70 MHz input
- TTE inc. KC4T-20M-15P-50-69A for 20 MHz input

2.2.4.3   OUTPUT DATA CONFIGURATION

The MCP37X3X-200 device is able to provide both LVDS and CMOS outputs. However, this evaluation kit is designed for LVDS outputs only. The user can modify various other output data properties using the GUI, with the exception of the LVDS/CMOS output mode.

Appendix A. Schematics and Layouts

NOTICE TO CUSTOMERS

Refer to the board's web page for the complete Schematics and Layouts.

NOTES:

Appendix B. Bill of Materials (BOM)

NOTICE TO CUSTOMERS

Refer to the board's web page for the complete Bill of Materials.

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Fax: 82-53-744-4302

Korea - Seoul
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Fax: 82-2-558-5932 or
82-2-558-5934

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Malaysia - Penang
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Fax: 60-4-227-4068

Philippines - Manila
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Fax: 63-2-634-9069

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Taiwan - Hsin Chu
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03/25/14