

# UMX5101

#### ULTRA LOW MAGNETIC MOMENT PIN DIODE FOR MRI APPLICATIONS

#### DESCRIPTION

The UMX5101 PIN diode series was designed to provide ultra low magnetic PIN diodes for in bore surface coil applications associated with higher field strength (3T and greater) MR scanners. These PIN diodes produce the minimum artifacts (magnetic field distortions) available in the industry, today. The diodes have been tested in magnetic fields of  $\pm$ 7 Tesla.

## The UMX5101 PIN diodes have a magnetic moment at 7 T of 2E-8 (J/T).

The diodes are offered in a surface mount package. The SM package utilizes a square end cap to mark the cathode. The anode is round. The fully SOGO passivated PIN diode chip is full face metallurgically bonded to high conductive pins for lower thermal and electrical resistances. The PIN diodes feature low forward bias resistance and high zero bias impedance. The UMX5101 PIN diodes are characterized at 64, 128, and 300 MHz. The UMX5101 meets RoHS requirements per EU Directive 2002/95/EC.

#### ABSOLUTE MAXIMUM RATINGS AT 25° C (UNLESS OTHERWISE SPECIFIED)

Rating	Symbol	Value	Unit					
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	100 100 100	V					
RMS Reverse Voltage	V <sub>R(RMS)</sub>	75	V					
Storage Temperature	T stg	-65 to +175	°C					
Operating Temperature Non-Repetitive Peak	Т ор	-65 to +150	°C					

#### THERMAL CHARACTERISTICS AT 25° C (UNLESS OTHERWISE SPECIFIED) Thermal Resistance

θ

20

°C/Watt

IMPORTANT: For the most current data, consult our website: <u>www.MICROSEMI.com</u>

UMX5101SM



#### RoHS COMPLIANT



Ultra low magnetic construction

**KEY FEATURES** 

- SOGO passivated chip
- Thermally matched configuration
- RoHS compliant<sup>1</sup>
- Low capacitance at 0 V bias
- Low conductance at 0 V bias
- Metallurgical bond
- Fused-in-glass construction
- Non cavity design
- Available in surface mount package.
- Compatible with automatic insertion equipment

1- These devices are supplied with Silver terminations. Other terminal finishes may be available on request. Consult factory for details.

#### APPLICATIONS/BENEFITS

- High B Field (3T+) in bore APPLICATIONS:
- Active or semi-active (not passive)
- MR blocking circuits
- MR detuning circuits
- MR disable circuits
- MR receiver protector circuits

Copyright © 2006 Rev: 2009-01-19



# UMX5101

## ULTRA LOW MAGNETIC MOMENT PIN DIODE FOR MRI APPLICATIONS

**RoHS COMPLIANT** 

R	oH	s	

(e4)

ELECTRICAL PARAMETERS @ 25°C (UNLESS OTHERWISE SPECIFIED)								
Parameter	Symbol	Conditions	Min	Тур.	Max	Units		
Forward Voltage (Note 1)	V <sub>F</sub>	I <sub>F</sub> = 100 mA		0.82	1.0	V		
Reverse Break Down Voltage	V <sub>BR</sub>	I <sub>R</sub> = 10 uA	100			V		
Reverse Current	I <sub>R</sub>	V <sub>R</sub> = 100 V			10	uA		
Inductance	Ls			800		pН		
Magnetic moment		@ 7T		2E-8		J/T		
	m	@ 1T		5E-8				
Mass Susceptibility	χρ	@ 7T		-2.7E-11		m³/kg		
		@ 1T		1.2E-9				
Volume Susceptibility	χ	>1T to 7T		-2.4E-7		SI		
		<1 T		1.0E-5		31		
Capacitance	Ст	V <sub>R</sub> = 0 V, F = 1 MHz		2.5	3.0	pF		
		V <sub>R</sub> = 100 V, F = 1 MHz		1.0	1.2			
Parallel Resistance	R <sub>P</sub>	V <sub>R</sub> = 0 V, F = 64 MHz	10	30		kΩ		
	IVP	V <sub>R</sub> = 30 V, F = 64 MHz	800	1000				
Series Resistance	Rs	lf = 100 mA F = 64 MHz		0.8	1.0	Ω		
Lifetime	τ	lf = 10 mA	2	2.5		us		



# UMX5101

## ULTRA LOW MAGNETIC MOMENT PIN DIODE FOR MRI APPLICATIONS

C-V CURVES

**RoHS COMPLIANT** 

T e4 RoHS





RP - PARALLEL RESISTANCE UMXS101 TYPICAL (NUTO) (NUTO)



# **Sicrosemi**

VF VS IF

# UMX5101

## ULTRA LOW MAGNETIC MOMENT PIN DIODE FOR MRI APPLICATIONS

## RoHS COMPLIANT

## SM STYLE SOLDER FOOTPRINT



**GRAPHS/MECHANICAL** 

www.MICROSEMI.com