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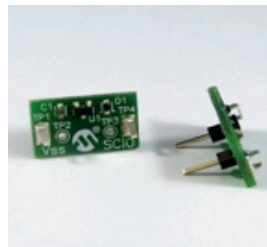
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### Part Number : AC243004 - UNI/O Bus Parasitic Power Demo

The UNI/O Bus Parasitic Power Demo Board is designed to illustrate how a standard half -wave rectifier and capacitor circuit can be used to parasitically extract power for a UNI/O device from the SCIO signal as described in application note, AN1213 Powering a UNI/O Bus Device Through SCIO. This reduces the number of connections necessary for adding a UNI/O device to your application down to two: SCIO and Vss.

The board is designed to interface with MPLAB Serial Memory Starter Kit, but the included test points allow you to interface the board with any application through the use of test leads (not included).


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