Operating Temperature Range: -25 to +125°C

lo.	Item		Specifications	Test Method				
1	Appearance ar	nd Dimensions	No visible defect, and dimensions are within specified range.	The capacitor should be visually inspected for evidence of defect. Dimensions should be measured with slide calipers.				
2	Marking		To be easily legible	The capacitor should be visually inspected.				
3	Capacitance		Within specified tolerance					
4	Dissipation Fac	ctor (D.F.)	Char. Specifications B, E D.F.≤2.5% F D.F.≤5.0% SL Q≥400+20C*¹(C<30pF)	The capacitance, dissipation factor and Q should be measure at 20°C with 1±0.1kHz (char. SL: 1±0.1MHz) and AC5V(r.m.s max.				
5	Insulation Resi	istance (I.R.)	10000M Ω min.	The insulation resistance should be measured with DC500 \pm 50V within 60 \pm 5 sec. of charging. The voltage should be applied to the capacitor through a resistor of 1M Ω .				
6	Dielectric Strength	Between Lead Wires	No failure	The capacitor should not be damaged when the test voltages from Table 1 are applied between the lead wires for 60 sec. Table 1				
		Body Insulation	No failure	First, the terminals of the capacitor should be connected together. Then, as shown in the figure at right, a metal foil should be closely wrapped around the body of the capacitor to the distance of about 3 to 6mm from each terminal. Then, the capacitor should be inserted into a container filled with metal balls of about 1mm diameter. Finally, AC voltage from Table 2 is applied for 60 sec. between the capacitor lead wires and metal balls. <table 2=""></table>				
				Type Test Voltage KY AC2600V(r.m.s.) KH AC2600V(r.m.s.) KX AC4000V(r.m.s.)				
7	Temperature Characteristics		Char. Capacitance Change B Within ±10% E Within ±25% F Within ±35% (Temp. range: -25 to +85°C) Char. Temperature Coefficient SL +350 to -1000ppm/°C (Temp. range: +20 to +85°C)	The capacitance measurement should be made at each step specified in Table 3.				
3	Solderability o	f Leads	Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.	The lead wire of a capacitor should be dipped into molten solder for 2±0.5 sec. The depth of immersion is up to about 1.5 to 2.0mm from the root of lead wires. Temp. of solder: Lead Free Solder (Sn-3Ag-0.5Cu) 245±5°C H63 Eutectic Solder 235±5°C				

 $^{^{\}star 1}$ "C" expresses nominal capacitance value (pF).

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No.	Item		Specifications	Test Method			
9	Soldering Effect (Non-Preheat)	Appearance Capacitance	No marked defect	As shown in the figure, the lead wires should be immersed in Thermal Capacitor			
		Change	Within ±10%	solder of 350±10°C or 260±5°C up Screen to 1.5 to 2.0mm from the root of			
		I.R.	1000MΩ min.	terminal for 3.5±0.5 sec. (10±1			
		Dielectric Strength	Per Item 6	sec. for 260±5°C). Pre-treatment: Capacitor should be stored at 85±2°C for 1 hr., then placed a room condition*² for 24±2 hrs. before initial measurements. Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.*¹			
		Appearance	No marked defect	First the capacitor should be			
		Capacitance Change	Within ±10%	stored at 120+0/-5°C for 60+0/-5 sec. Then, as in the figure, the lead			
	Soldering	I.R.	1000MΩ min.	wires should be immersed in solder of 260+0/-5°C up to 1.5 to			
10	_	Dielectric Strength	Per Item 6	2.0mm from the root of terminal for 7.5+0/-1 sec. Pre-treatment: Capacitor should be stored at 85±2°C for 1 hr., then placed at room condition*² for 24±2 hrs. before initial measurements. Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.*²			
		Appearance	No marked defect				
		Capacitance	Within the specified tolerance	The capacitor should be firmly soldered to the supporting lead wire and vibrated at a frequency range of 10 to 55Hz, 1.5mm in			
11	Vibration Resistance	D.F. Q	Char. Specifications B, E D.F.≦2.5% F D.F.≦5.0% SL Q≥400+20C*¹(C<30pF)	total amplitude, with about a 1-minute rate of vibration change from 10Hz to 55Hz and back to 10Hz. Apply for a total of 6 hrs., 2 hrs. each in 3 mutually perpendicular directions.			
		Appearance	No marked defect				
	Humidity (Under Steady State)	Capacitance Change	Char. Capacitance Change B Within ±10% E, F Within ±15% SL Within ± 5%				
12		D.F. Q	Char. Specifications B, E D.F.≤5.0% F D.F.≤7.5% SL Q≥275+5/2C*¹(C<30pF)	Set the capacitor for 500±12 hrs. at 40±2°C in 90 to 95% relative humidity. Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.*2			
		I.R.	3000M $Ω$ min.				
		Dielectric Strength	Per Item 6				
	Humidity Loading	Appearance	No marked defect				
		Capacitance Change	Char. Capacitance Change B Within ±10% E, F Within ±15% SL Within ± 5%				
13		D.F. Q	$\begin{tabular}{c c c} \hline \textbf{Char.} & \textbf{Specifications} \\ \hline B, E & D.F. \le 5.0\% \\ \hline F & D.F. \le 7.5\% \\ \hline SL & Q \ge 275 + 5/2C^{*}(C < 30pF) \\ \hline Q \ge 350 & (C \ge 30pF) \\ \hline \end{tabular}$	Apply the rated voltage for 500±12 hrs. at 40±2°C in 90 to 95% relative humidity. Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.*2			
		I.R.	3000MΩ min.				
		Dielectric Strength	Per Item 6				

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^{*1 &}quot;C" expresses nominal capacitance value (pF).
*2 "Room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa

Continued from the preceding page.

No.	Item		Specifications	Test Method				
		Appearance	No marked defect	Impulse Voltage				
14		Capacitance Change	Within ±20%	Each individual capacitor should be subjected to a 5kV (Type KX: 8kV) impulses for three times. Then the capacitors are applied to life test.				
		I.R.	3000MΩ min.	100 (%)				
	Life	Dielectric Strength	Per Item 6	Front time (T1) =1.2µs=1.67T Time to half-value (T2) =50µs 30 1 T T Apply a voltage from Table 4 for 1000 hrs. at 125+2/-0°C, and relative humidity of 50% max.				
				<table 4=""></table>				
				Applied Voltage				
				170% of Rated Voltage except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1 sec.				
				Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.*2				
15	Robustness of Terminations	Tensile	Lead wire should not be cut off. Capacitor should not be broken.	As shown in the figure at right, fix the body of the capacitor and apply a tensile weight gradually to each lead wire in the radial direction of the capacitor up to 10N and keep it for 10±1 sec.				
		Bending	not be bloken.	Each lead wire should be subjected to 5N of weight and bent 90° at the point of egress, in one direction, then returned to its original position and bent 90° in the opposite direction at the rate of one bend in 2 to 3 sec.				
	Active Flammability			The capacitor should be individually wrapped in at least one bu not more than two complete layers of cheesecloth. The capacitor should be subjected to 20 discharges. The interval between successive discharges should be 5 sec. The UAC should be maintained for 2 min. after the last discharge.				
				C1.2 : 1μF±10% C3 : 0.033μF±5% 10kV L1 to 4 : 1.5mH±20% 16A Rod core choke Ct : 3μF±5% 10kV R : 100Ω±2% Cx : Capacitor under test UAC : UR±5% F : Fuse, Rated 10A UR : Rated Voltage Ut : Voltage applied to C				
16			The cheesecloth should not be on fire.					

 $^{^{\}star 2}$ "Room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa

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No.	Item			Specifications			Test Method			
17	Item Passive Flammability			me should not exceed 30 sec. per should not ignite.	pc or	The capacitor under test should be held in the flame in the position that best promotes burning. Each specimen should only be exposed once to the flame. Time of exposure to flame 30 sec. Length of flame: 12±1mm Gas burner: Length 35mm min. Inside Dia. 0.5±0.1mm Outside Dia. 0.9mm max. Gas: Butane gas Purity 95% min. Test Specimen Tissue About 10mm Thick Board				
		Appearance	No marked de	fect		The capacitor should be subjected to 5 temperature cycles,			perature cycles,	
	Temperature and Immersion Cycle	Capacitance Change	Char. B E, F SL	Capacitance Change Within ±10% Within ±20% Within ± 5%	tn	then consecutively to 2 immersion cycles. <temperature cycle=""> Step Temperature (°C) Time (mir 1 -25+0/-3 30</temperature>				
		D.F. Q				2	Room ten 125+3/-		3 30	
				Specifications		4		Room temp.		
18			B, E F SL	D.F.≤5.0% D.F.≤7.5% Q≥275+5/2C*1(C<30pF) Q≥350 (C≥30pF)		Cycle time: 5				
		I.R.	3000M $Ω$ min.			Step	Temperature (°C)	Time (min)	Immersion Water	
		Dielectric Strength				1	65+5/-0	15	Clean water	
						2	0±3	15	Salt water	
			Per Item 6	Po	Cycle time: 2 cycle Pre-treatment: Capacitor should be stored at 85±2°C for 1 hr., then placed a room condition*2 for 24±2 hrs. Post-treatment: Capacitor should be stored for 24±2 hrs. at room condition.*2					

^{*1 &}quot;C" expresses nominal capacitance value (pF).
*2 "Room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa