

# **High Performance Schottky Rectifier, 100 A**

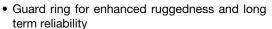


PowerTab<sup>®</sup>

PRODUCT SUMMARY				
Package	PowerTab <sup>®</sup>			
I <sub>F(AV)</sub>	100 A			
$V_{R}$	100 V			
V <sub>F</sub> at I <sub>F</sub>	0.82 V			
I <sub>RM</sub>	180 mA at 125 °C			
T <sub>J</sub> max.	175 °C			
Diode variation	Single die			
E <sub>AS</sub>	9 mJ			

#### **FEATURES**

- 175 °C max. operating junction temperature
- High frequency operation
- Low forward voltage drop
- · Continuous high current operation





ROHS COMPLIANT

- Screw mounting only
- Designed and qualified according to JEDEC®-JESD 47
- PowerTab<sup>®</sup> package
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

The VS-100BGQ100 Schottky rectifier has been optimized for low reverse leakage at high temperature.

The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
Rectangular waveform		100	А	
I <sub>F(AV)</sub>	T <sub>C</sub>	124	°C	
V <sub>RRM</sub>		100	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	6300	Α	
W	100 A <sub>pk</sub> (typical)	0.77	V	
V <sub>F</sub>	TJ	125	°C	
T <sub>J</sub>	Range	-55 to +175	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	100BGQ100	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	100	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	100	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 124 °C, rectangular waveform		100	Α
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	6300	Α
non-repetitive surge current	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	800	ζ
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25 ^{\circ}\text{C},  I_{AS} = 2  \text{A},  L = 4.5  \text{mH}$		mJ	
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		А	

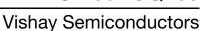


ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES		UNITS
PANAMETER	STWIBOL			TYP.	MAX.	UNITS
	V <sub>FM</sub> <sup>(1)</sup>	50 A	T <sub>J</sub> = 25 °C	0.83	0.86	- v
Forward voltage drop		100 A		1.01	1.08	
Forward voltage drop		50 A	- T <sub>J</sub> = 125 °C	0.66	0.7	
		100 A		0.77	0.82	
Devices lealings summer	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	22	300	μA
Reverse leakage current I <sub>RI</sub>		T <sub>J</sub> = 125 °C	v <sub>R</sub> = nateu v <sub>R</sub>	14	18	mA
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ , (test signal range 100 kHz to 1 MHz) 25 °C		13	20	pF
Typical series inductance	L <sub>S</sub>	Measured from tab to mounting plane 3.5		.5	nΗ	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/ <sub>k</sub>			V/µs	

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and temperature range	storage	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C	
Maximum thermal resis	stance,	R <sub>thJC</sub>	DC operation	0.50	°C/W	
Typical thermal resistar case to heatsink	nce,	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.30		
Approximate weight			5	g		
			0.18	OZ.		
Mounting torque ———	minimum			1.2 (10)	N⋅m	
	maximum			2.4 (20)	(lbf $\cdot$ in)	
Marking device			Case style PowerTab®	100BGQ100		





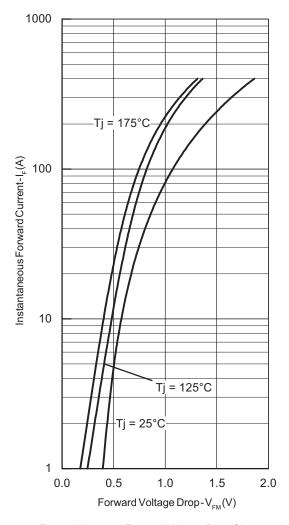


Fig. 1 - Maximum Forward Voltage Drop Characteristics

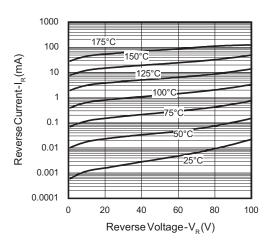


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

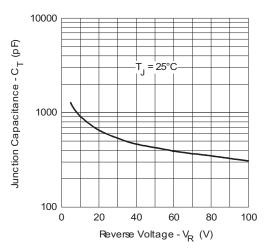


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

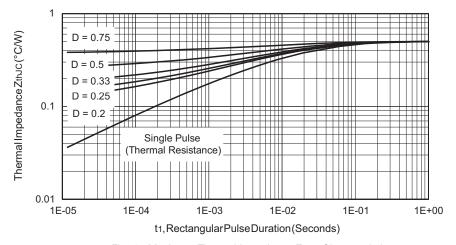


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

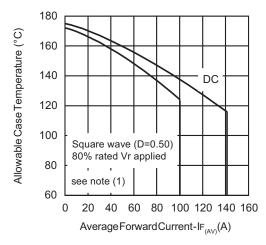


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

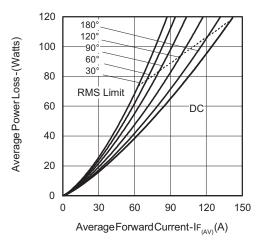


Fig. 6 - Forward Power Loss Characteristics

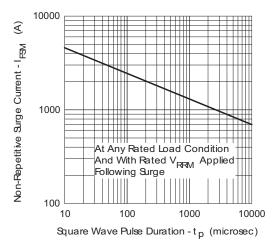


Fig. 7 - Maximum Non-Repetitive Surge Current

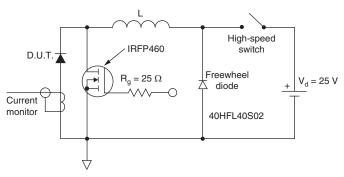


Fig. 8 - Unclamped Inductive Test Circuit

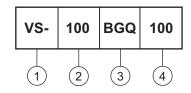
#### Note

 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>th,JC</sub>; Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>



### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Current rating

Essential part number

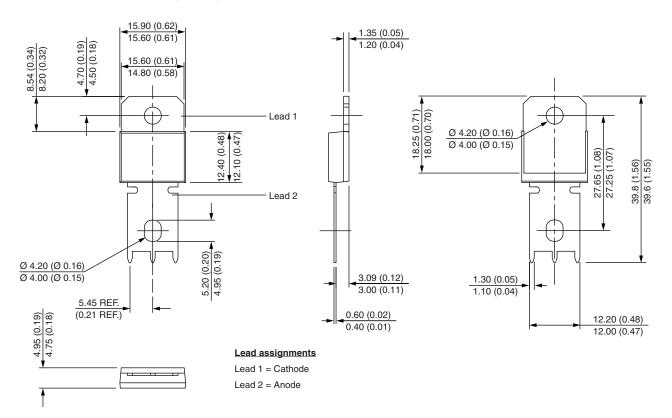
Voltage code = V<sub>RRM</sub>

LINKS TO RELATED DOCUMENTS			
Dimensions <u>www.vishay.com/doc?95240</u>			
Part marking information	www.vishay.com/doc?95370		
Application note	www.vishay.com/doc?95179		



## PowerTab<sup>®</sup>

### **DIMENSIONS** in millimeters (inches)





## **Legal Disclaimer Notice**

Vishay

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