

30 V, 2 A low VF dual MEGA Schottky barrier rectifier

20 January 2015

Product data sheet

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier in common cathode configuration with an integrated guard ring for stress protection, encapsulated in an ultra thin DFN2020D-3 (SOT1061D) leadless small Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

2. Features and benefits

- Average forward current I_{F(AV)} ≤ 2 A
- Reverse voltage V_R ≤ 30 V
- Low forward voltage $V_F \leq 440 \text{ mV}$
- Low reverse current
- Reduced Printed-Circuit-Board (PCB) area requirements
- Exposed heat sink (cathode pad) for excellent thermal and electrical conductivity
- Leadless small SMD plastic package with visible and solderable side pads
- Suitable for Automatic Optical Inspection (AOI) of solder joints
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Free-wheeling application
- Reverse polarity protection
- Low power consumption application
- Battery chargers for mobile equipment
- LED backlight for mobile application

4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per diode							
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; T _{amb} ≤ 75 °C; square wave	[1]	-	-	2	A
		δ = 0.5; f = 20 kHz; T _{sp} ≤ 135 °C; square wave		-	-	2	A



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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _R	reverse voltage	T _j = 25 °C	-	-	30	V
Per diode						,
V _F	forward voltage	I _F = 2 A; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C; pulsed	-	410	440	mV
I _R	reverse current	V_R = 30 V; $t_p \le$ 300 µs; $\delta \le$ 0.02; T _j = 25 °C; pulsed	-	485	2000	μA

[1] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	anode diode 1	A1	3	3
2	anode diode 2	A2		* *
3	common cathode	К	Transparent top view DFN2020D-3 (SOT1061D)	1 2 006aaa438

6. Ordering information

Table 3. Ordering in	formation		
Type number	Package		
	Name	Description	Version
PMEG3020CPAS	DFN2020D-3	DFN2020D-3: plastic thermal enhanced ultra thin small outline package; no leads; 3 terminals; body $2 \times 2 \times 0.65$ mm	SOT1061D

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG3020CPAS	СТ

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per diode		·				
V _R	reverse voltage	T _j = 25 °C		-	30	V
l _F	forward current	T _{sp} ≤ 130 °C; δ = 1		-	2.8	А
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; T _{amb} ≤ 75 °C; square wave	[1]	-	2	A
		δ = 0.5; f = 20 kHz; T _{sp} ≤ 135 °C; square wave		-	2	A
I _{FRM}	repetitive peak forward current	t _p ≤ 1 ms; δ ≤ 0.25		-	7	А
I _{FSM}	non-repetitive peak forward current	t_p = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	9	A
Per device;	; one diode loaded	·				
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[2]	-	500	mW
			[3]	-	960	mW
			[1]	-	1800	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. The	rmal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per device; or	e diode loaded						
R _{th(j-a)} thermal resistan from junction to ambient	thermal resistance	[1	[1][2]	-	-	250	K/W
	-		[1][3]	-	-	130	K/W
			[1][4]	-	-	70	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[5]	-	-	12	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [4] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.
- [5] Soldering point of cathode tab.

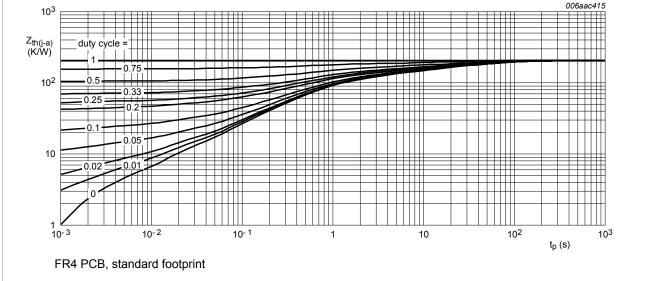
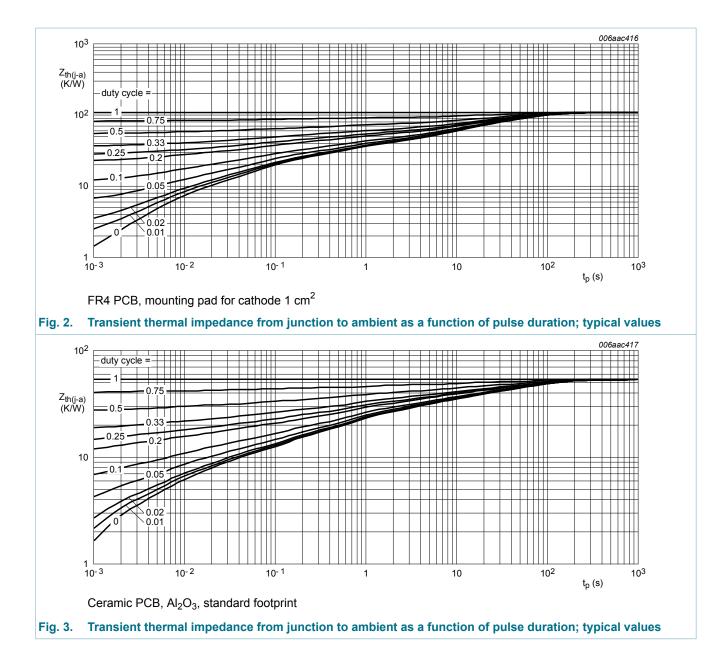


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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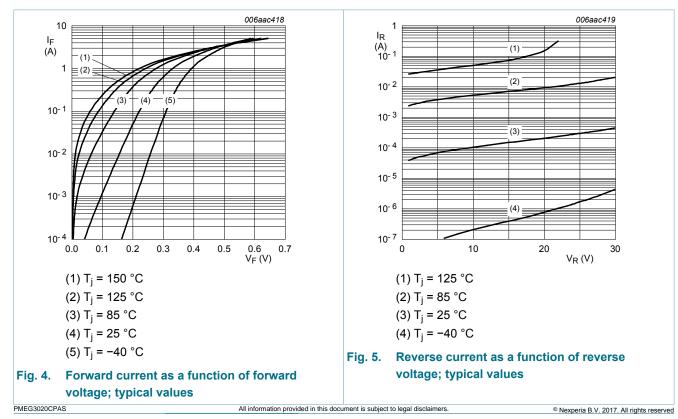
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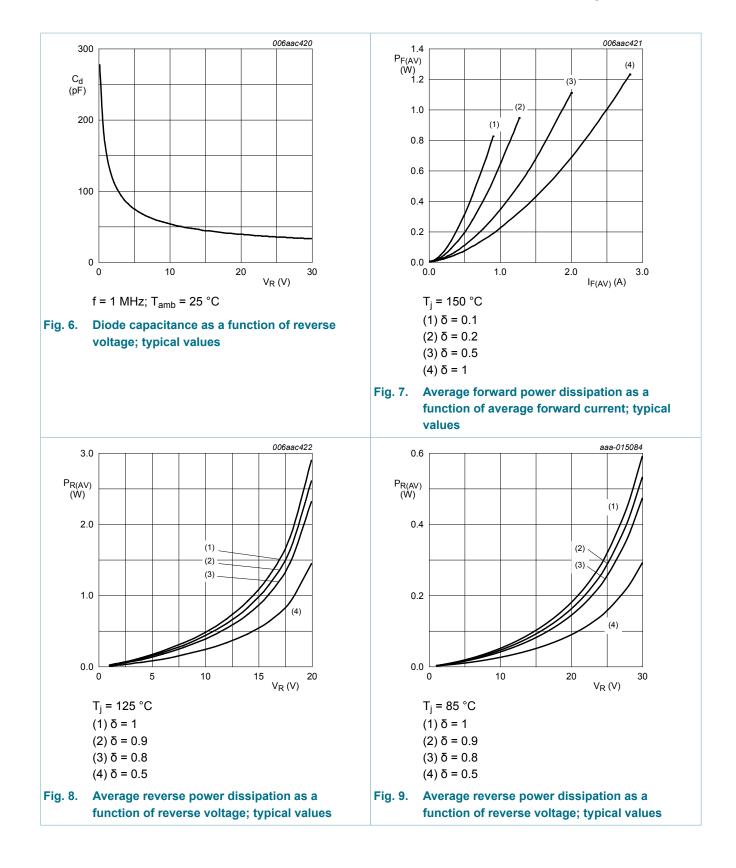


10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Symbol	Farameter	conditions		Ч	IVIAA	Unit
Per diode						
V _{(BR)R}	reverse breakdown voltage	I_{R} = 5 mA; T _j = 25 °C; t _p = 300 µs; δ = 0.02; pulsed	30	-	-	V
V _F fc	forward voltage	$\begin{split} I_F &= 100 \text{ mA; } t_p \leq 300 \mu\text{s}; \delta \leq 0.02; \\ T_j &= 25 ^\circ\text{C}; \text{ pulsed} \end{split}$	-	220	-	mV
		$I_F = 1 \text{ A}; t_p \le 300 \mu\text{s}; \delta \le 0.02;$ $T_j = 25 \text{ °C}; \text{ pulsed}$	-	335	370	mV
		$\begin{split} I_F &= 2 \text{ A}; \ t_p \leq 300 \ \mu\text{s}; \ \delta \leq 0.02; \\ T_j &= 25 \ ^\circ\text{C}; \ \text{pulsed} \end{split}$	-	410	440	mV
I _R	reverse current	$\label{eq:VR} \begin{split} V_{R} &= 10 \text{ V}; \ t_{p} \leq 300 \ \mu\text{s}; \ \delta \leq 0.02; \\ T_{j} &= 25 \ ^{\circ}\text{C}; \ \text{pulsed} \end{split}$	-	120	-	μA
		V_R = 30 V; $t_p \le$ 300 µs; $\delta \le$ 0.02; T_j = 25 °C; pulsed	-	485	2000	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	170	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C	-	60	-	pF
trr	reverse recovery time	I _F = 0.5 A; I _R = 1 A; I _{R(meas)} = 0.25 A; T _i = 25 °C	-	4	-	ns

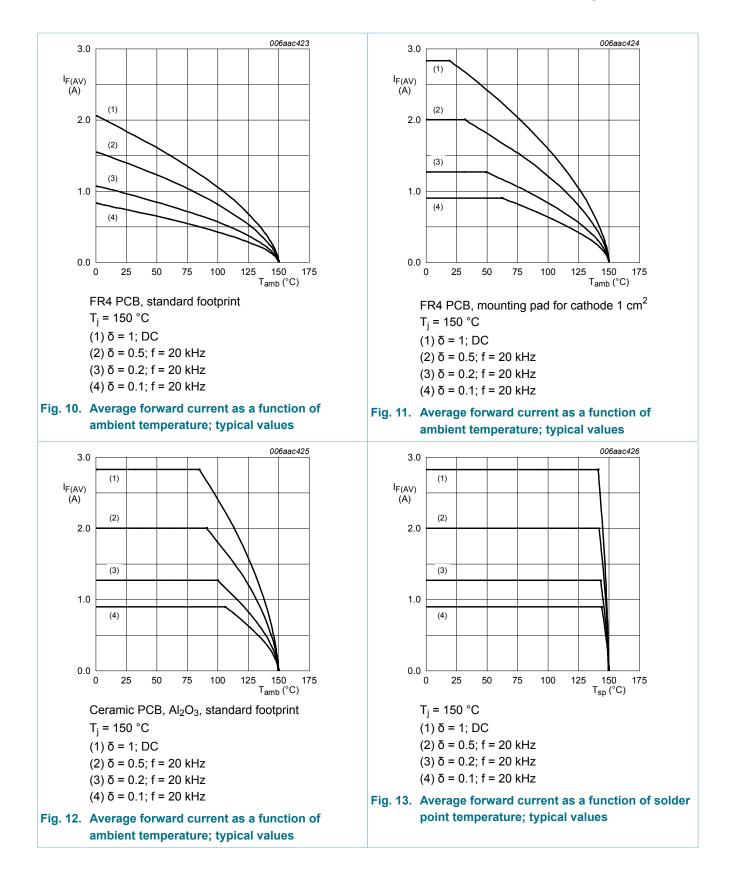


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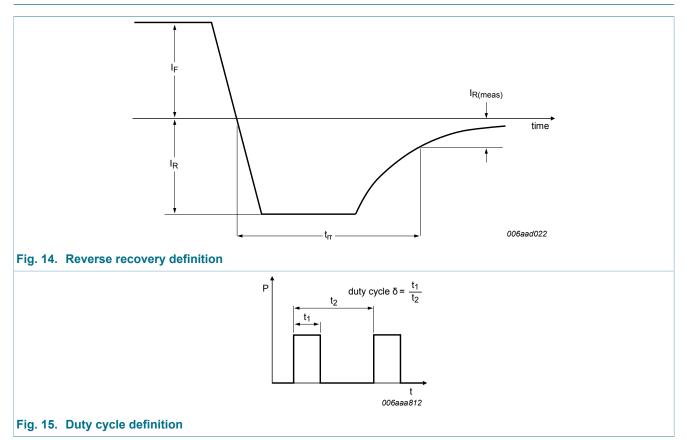
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11. Test information



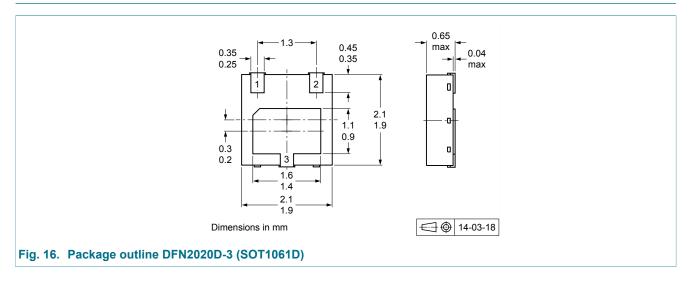
The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

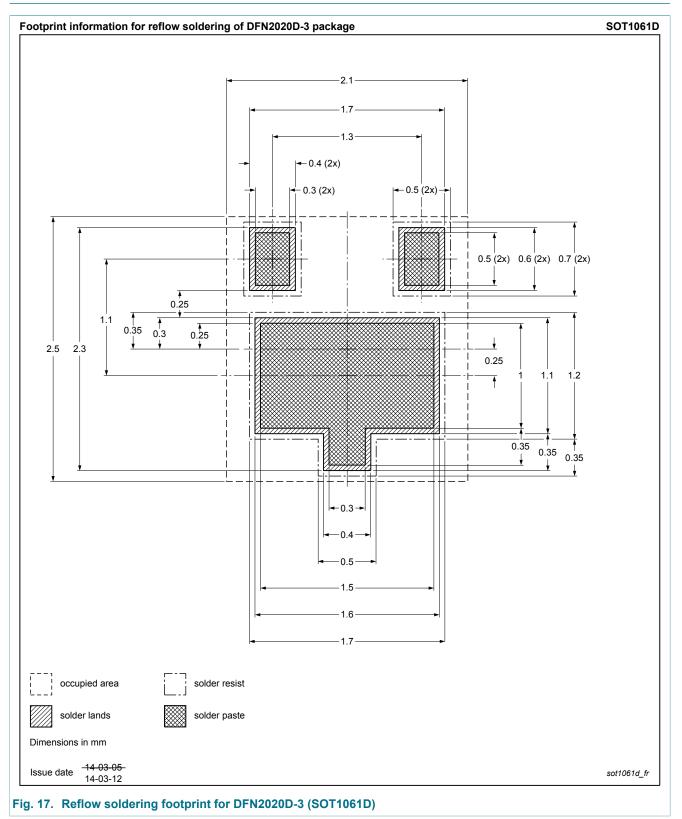
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12. Package outline



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13. Soldering



14. Revision history

Table 8. Revision his	story			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG3020CPAS v.2	20150120	Product data sheet	-	PMEG3020CPAS v.1
Modifications:	Changed data sheet status			
PMEG3020CPAS v.1	20141210	Preliminary data sheet	-	-

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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