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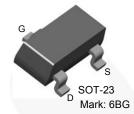
June 2015



## MMBF4416A N-Channel RF Amplifier

## Features

- This device is designed for RF amplifiers.
- Sourced from process 50.



#### **Ordering Information**

Part Number	Top Mark	Package	Packing Method
MMBF4416A	6BG	SOT-23 3L	Tape and Reel

#### Absolute Maximum Ratings<sup>(1),(2)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>DG</sub>	Drain-Gate Voltage	35	V
V <sub>GS</sub>	Gate-Source Voltage	-35	V
I <sub>GF</sub>	Forward Gate Current	10	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to 150	°C

#### Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

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## Thermal Characteristics<sup>(3)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Max.	Unit
P <sub>D</sub>	Total Device Dissipation	225	mW
	Derate Above 25°C	1.8	mW/°C
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	556	°C/W

Note:

3. Device mounted on FR-4 PCB 1.6" × 1.6" × 0.06".

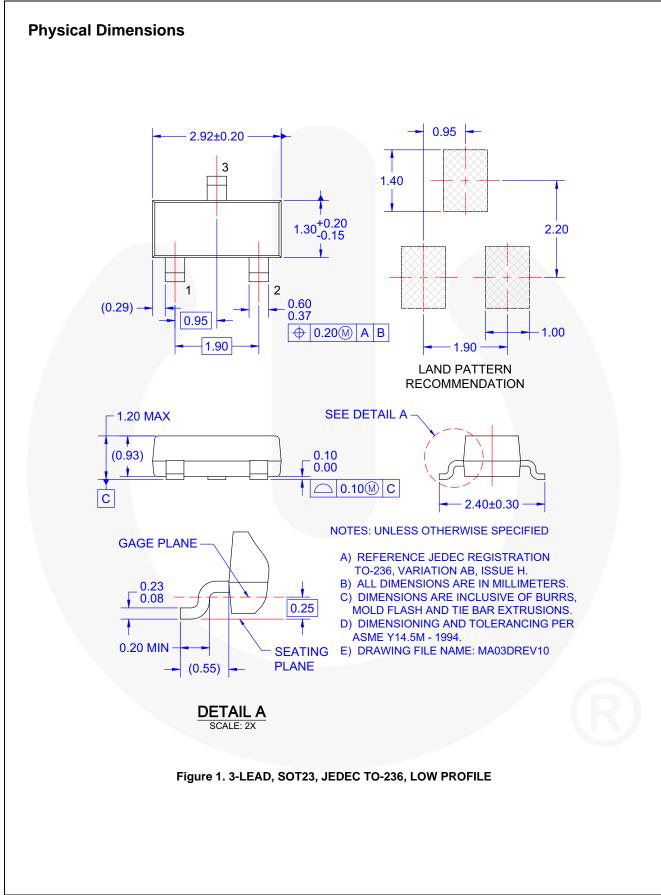
## **Electrical Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
Off Charact	eristics				
V <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	V <sub>DS</sub> = 0, I <sub>G</sub> = 1.0 μA	-35		V
I <sub>GSS</sub>	Gate Reverse Current	$V_{GS} = -20 V, V_{DS} = 0$		-100	pА
V <sub>GS</sub> (off)	Gate-Source Cut-Off Voltage	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 1.0 nA	-2.5	-6.0	V
V <sub>GS</sub>	Gate-Source Voltage	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 500 μA	-1.0	-5.5	V
On Charact	eristics	·		•	
I <sub>DSS</sub>	Zero-Gate Voltage Drain Current	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0	5	15	mA
V <sub>GS</sub> (f)	Gate-Source Forward Voltage	V <sub>DS</sub> = 0, I <sub>G</sub> = 1.0 mA		1	V
Small Signa	al Characteristics	·			
9 <sub>fs</sub>	Forward Transfer Conductance <sup>(4)</sup>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 kHz	4500	7500	μmhos
9 <sub>os</sub>	Output Conductance <sup>(4)</sup>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 kHz		50	μmhos
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 MHz		4.0	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 MHz		0.8	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 MHz		2.0	pF
NF	Noise Figure	$V_{DS}$ = 15 V, $V_{GS}$ = 0, I <sub>D</sub> = 5 mA, R <sub>g</sub> = 1 kΩ, f = 400 MHz		4.0	dB

Note:

4. Pulse test: pulse width  $\leq$  300 ms, duty cycle  $\leq$  2%



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Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.
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