



Series 537 'Live Bug' Type Universal PLCC ZIF Test Socket

FEATURES

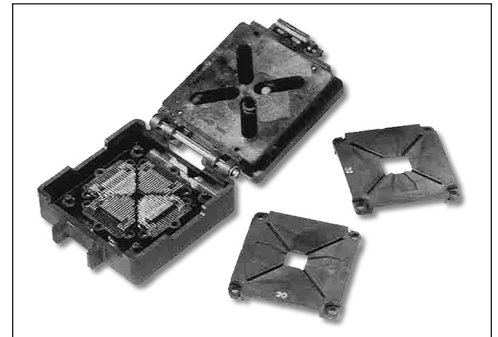
- This Universal PLCC ZIF Test Socket will take seven sizes of PLCC footprints with Aries Insert Plate, P/N XX-537-20, see **Data Sheet 10012** for additional Insert Plates
- The PLCC is inserted 'live bug' (pins down) into the position over the bed; the top half of the clamshell is closed for testing, programming, etc. The bottom solder tail pins are the ends of the contacts or "bed," thus permitting mounting to a PC board.
- Accepts JEDEC MO-047AA-AH PLCC's and JEDEC MO-044AA-AB ceramics with 20, 28, 44, 52, 68, or 84 square leads, as well as JEDEC MS-016 PLCC's with 32 rectangular leads.

GENERAL SPECIFICATIONS

- PLASTIC: black UL 94V-0 glass-filled Polyphenylene Sulfide (PPS)
- CONTACT PIN: BeCu 172 heat-treated
- CONTACT PLATING: 12 μ [0.31 μ] min. Au per MIL-G-45204 over 100 μ [2.54 μ] min. Ni per SAE AMS-QQ-N-290 over 20 μ [0.51 μ] Cu per MIL-C-14550
- HARDWARE: Stainless Steel
- OPERATING TEMPERATURE: 85°F to 257°F [-65°C to 125°C]
- LIFE CYCLE: 10,000 insertions max.

MOUNTING CONSIDERATIONS

- SUGGESTED PCB HOLE SIZE: 0.032 $\pm$ 0.002 [0.81 $\pm$ 0.05] dia.



CUSTOMIZATION: In addition to the standard products shown on this page, Aries specializes in custom design and production. Special materials, platings, sizes, and configurations can be furnished, depending on the quantity. **NOTE:** Aries reserves the right to change product general specifications without notice.

ORDERING INFORMATION

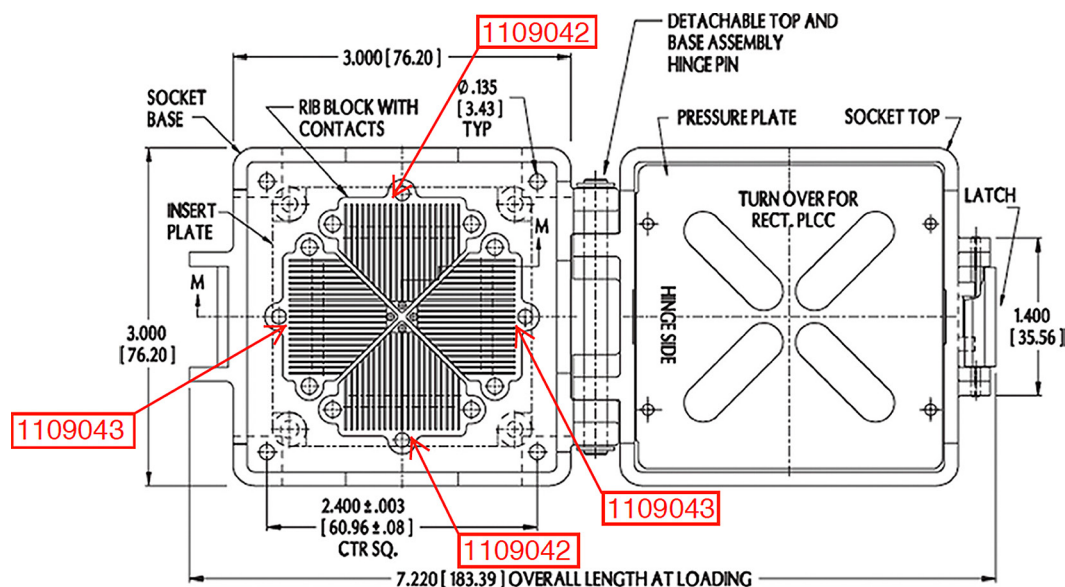
P/N 84-537-21 Socket Only, No Insert Plates

P/N 84-537-21-7 for Socket & All 7 Insert Plates

ALL DIMENSIONS: INCHES [MILLIMETERS]

ALL TOLERANCES: $\pm$ 0.005 [0.13] UNLESS OTHERWISE SPECIFIED

CONSULT FACTORY FOR OTHER SIZES AND CONFIGURATIONS



ARIES
ELECTRONICS, INC.

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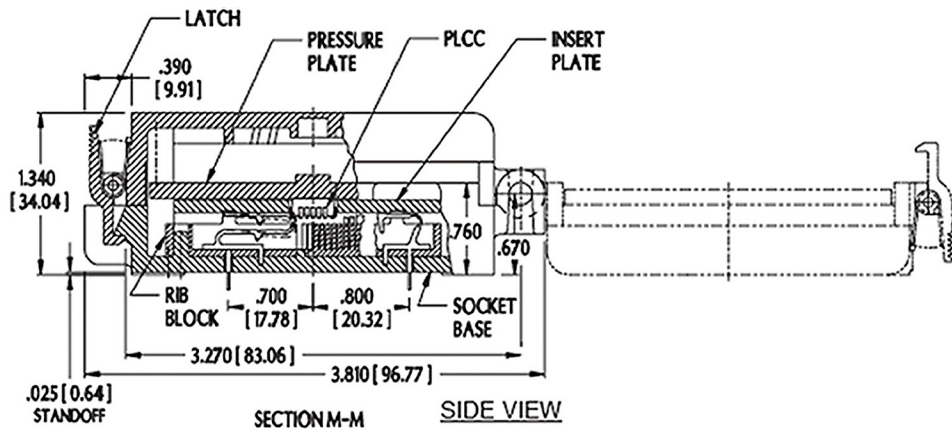


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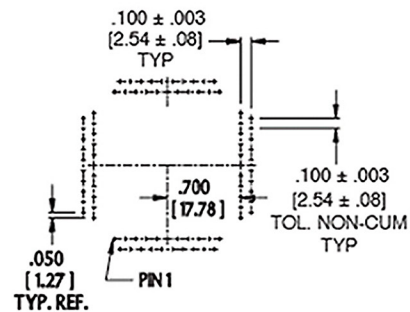
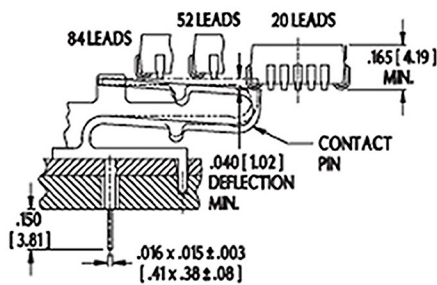
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1 of 2
Rev. AA



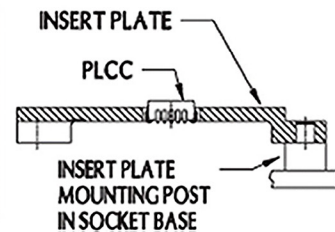
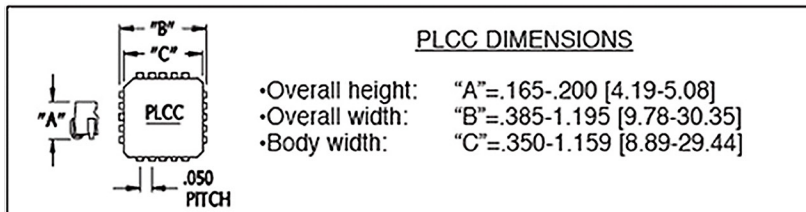
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CONTACT DETAIL



Top View Footprint
84 Pin Terminals



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Rev. AA