



# SMT GaAs HBT MMIC x4 ACTIVE FREQUENCY MULTIPLIER 11.4 - 13.2 GHz OUTPUT

# **Typical Applications**

The HMC695LP4(E) is ideal for:

- Fiber Optic Applications
- Point-to-Point Radios
- Military Radar

# **Functional Diagram**



### Features

Output Power: +7 dBm Sub-Harmonic Suppression: >25 dBc SSB Phase Noise: -140 dBc/Hz Single Supply: +5V @ 60 mA 24 Lead 4x4 mm SMT Package: 16 mm<sup>2</sup>

## **General Description**

The HMC695LP4(E) are active miniature x4 frequency multipliers utilizing InGaP GaAs HBT technology in 4x4 mm leadless surface mount packages. Power output is +7 dBm typical from a +5V supply voltage and varies little vs. input power, temperature and supply voltage. Suppression of undesired fundamental and sub-harmonics is >25 dBc typical with respect to output signal level. The low additive SSB phase noise of -140 dBc/Hz at 100 kHz offset helps the user maintain good system noise performance. The HMC695LP4(E) are ideal for use in LO multiplier chains allowing reduced parts count vs. traditional approaches.

# Electrical Specifications, $T_A = +25^{\circ} C$ , Vcc= 5V

| Parameter                          | Mi       | in.         | Тур.       | Max. | Units  |
|------------------------------------|----------|-------------|------------|------|--------|
| Frequency Range, Input             |          |             | 2.85 - 3.3 |      | GHz    |
| Frequency Range, Output            |          | 11.4 - 13.2 |            | GHz  |        |
| Input Power Range                  | -1       | 5           |            | 5    | dBm    |
| Output Power                       | 2        | 2           | 7          |      | dBm    |
| Sub-Harmonic Suppression           |          |             | 25         |      | dBc    |
| Input Return Loss                  |          |             | 15         |      | dB     |
| Output Return Loss                 |          |             | 8          |      | dB     |
| SSB Phase Noise (100 kHz Offset) P | n= 0 dBm |             | -140       |      | dBc/Hz |
| Supply Current (Icc)               |          |             | 60         | 75   | mA     |

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Output Power vs.



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Output Power vs. Supply Voltage @ -10 dBm Drive Level



**Output Return Loss vs. Temperature** 



**Output Power vs. Drive Level** 



Input Return Loss vs. Temperature



#### Harmonics @ -10 dBm Drive Level



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SSB Phase Noise @ Pin = 0 dBm @ 12.5 GHz



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# Absolute Maximum Ratings

| RF Input (Vcc= +5V)  | +20 dBm        |
|--|----------------|
| Vcc  | +5.5V          |
| Channel Temperature  | 135 °C         |
| Continuous Pdiss (T=85 °C)<br>(derate 10.8 mW/°C above 85 °C)        | 538 mW         |
| Thermal Resistance (R <sub>th</sub> )<br>(junction to ground paddle) | 93 °C/W        |
| Storage Temperature  | -65 to +150 °C |
| Operating Temperature  | -40 to +85 °C  |
| ESD Sensitivity (HBM)  | Class 1B       |

SSB Phase Noise @ Pin = -10 dBm @ 12.5 GHz -20 SSB PHASE NOISE (dBc/Hz) -40 -60 -80 -100 -120 -140 -160 10<sup>3</sup> 10<sup>5</sup> 10<sup>6</sup> 10<sup>7</sup> 10<sup>2</sup> 10<sup>4</sup> OFFSET FREQUENCY (Hz)

# Typical Supply Current vs. Vcc

| Vcc (V) | Icc (mA) |
|---------|----------|
| 4.75    | 59       |
| 5.00    | 60       |
| 5.25    | 61       |

Note: Multiplier will operate over full voltage range shown above.



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

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## **Outline Drawing**



# 7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED LAND PATTERN.

#### Package Information

| Part Number | Package Body Material                              | Lead Finish   | MSL Rating          | Package Marking <sup>[3]</sup> |
|-------------|--|---------------|---------------------|--------------------------------|
| HMC695LP4   | Low Stress Injection Molded Plastic                | Sn/Pb Solder  | MSL1 <sup>[1]</sup> | H695<br>XXXX                   |
| HMC695LP4E  | RoHS-compliant Low Stress Injection Molded Plastic | 100% matte Sn | MSL1 <sup>[2]</sup> | <u>H695</u><br>XXXX            |

[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

[3] 4-Digit lot number XXXX

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#### **Pin Description**

| Pin Number                       | Function | Description  | Interface Schematic    |
|----------------------------------|----------|--|------------------------|
| 1, 2, 5 - 14,<br>17, 18, 20 - 24 | N/C      | The pins are not connected internally; however, all<br>data shown herein was measured with these pins<br>connected to RF/DC ground externally. |                        |
| 3                                | RFIN     | RF input needs to be DC blocked only if there is an external DC voltage applied to RFIN.   | RFIN ○    <br>500<br>= |
| 4, 15                            | GND      | All ground leads and ground paddle<br>must be soldered to PCB RF/DC ground.  |                        |
| 16                               | RFOUT    | Multiplied Output. AC coupled.<br>No external DC blocks necessary.   |                        |
| 19                               | Vcc      | Supply voltage 5V  |                        |

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## **Evaluation PCB**



# List of Materials for Evaluation PCB 106137 [1]

| Item    | Description                       |
|---------|-----------------------------------|
| J1 - J3 | PCB Mount SMA Connector           |
| C1      | 1,000 pF Capacitor, 0603 Pkg.     |
| U1      | HMC695LP4(E) x4 Active Multiplier |
| PCB [2] | 104610 Eval Board                 |

Reference this number when ordering complete evaluation PCB
Circuit Board Material: Arlon 25FR or Rogers 4350

#### The circuit board used in the application should be generated with proper RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. The evaluation circuit board shown is available from Hittite upon request.

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