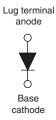


High Performance Schottky Rectifier, 180 A





HAI F.	DΔK	(D-67)
HALF.	TAN	(D-0 <i>1)</i>

PRODUCT SUMMARY			
I _{F(AV)}	180 A		
V _R	100 V		
Package	HALF-PAK (D-67)		
Circuit	Single diode		

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation



- Guard ring for enhanced ruggedness and long term reliability
- · Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-183NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	180	А	
V_{RRM}		100	V	
I _{FSM}	t _p = 5 μs sine	22 000	А	
V _F	180 A _{pk} , T _J = 125 °C	0.73	V	
TJ	Range	-55 to +175	°C	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-183NQ100PbF	UNITS		
Maximum DC reverse voltage	V_{R}	100	V		
Maximum working peak reverse voltage	V_{RWM}	100	V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 128 °C	, rectangular waveform	240	
Maximum peak one cycle non-repetitive surge current	1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	22 000	Α
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	2500	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 5.5 \text{A}, L = 1 \text{mH}$		15	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	А



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	. TEST CONDITIONS VALUES		VALUES	UNITS
		180 A	- T _J = 25 °C - T _J = 125 °C	0.91	V
Maximum forward voltage drop	V _{FM} ⁽¹⁾	360 A		1.23	
See fig. 1	V _{FM} ···	180 A		0.73	
		360 A		0.9	
Maximum reverse leakage current	1 (4)	T _J = 25 °C	V _R = Rated V _R	4.5	mA
See fig. 2	I _{RM} (1)	T _J = 125 °C		60	MA
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		4150	pF
Typical series inductance	L _S	From top of terminal hole to mounting plane		6.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/µ		V/µs	

Note

⁽¹⁾ Pulse width = $500 \mu s$

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 175	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	0.28	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.05		
Approximate weight				30	g	
Approximate weight				1.06	OZ.	
Mounting torque	minimum			3 (26.5)		
Mounting torque	maximum		Non-lubricated threads	4 (35.4)	N⋅m	
Torminal torque	minimum			3.4 (30)	(lbf · in)	
Terminal torque	maximum			5 (44.2)		
Case style			HALF-PAK modu		K module	

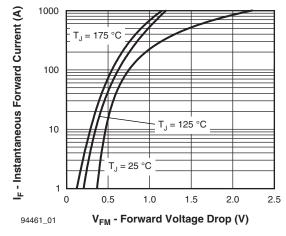


Fig. 1 - Maximum Forward Voltage Drop Characteristics

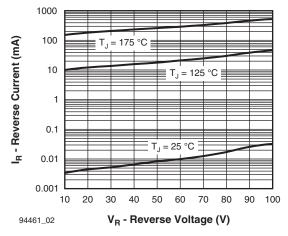


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

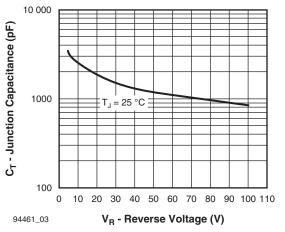


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

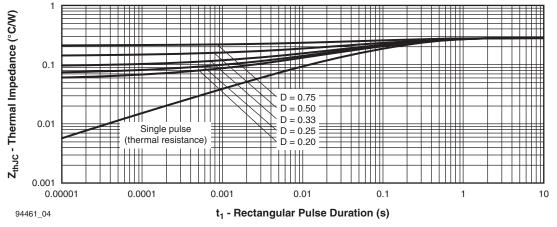


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

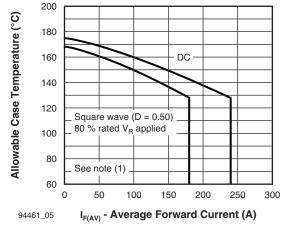


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

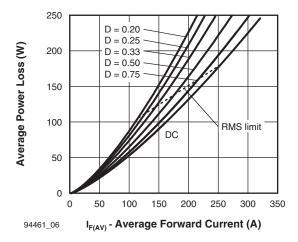


Fig. 6 - Forward Power Loss Characteristics

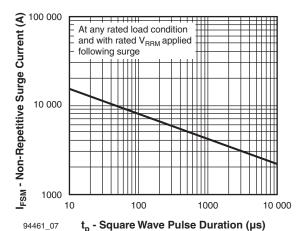


Fig. 7 - Maximum Non-Repetitive Surge Current

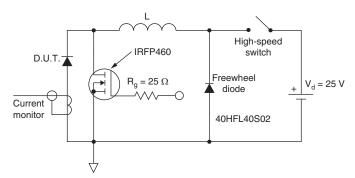


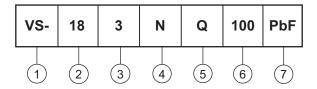
Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$

ORDERING INFORMATION TABLE

Device code



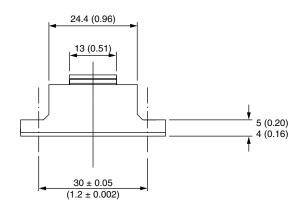
- Vishay Semiconductors product
- 2 Average current rating (x 10)
- 3 Product silicon identification
- 4 N = Not isolated
- 5 Q = Schottky rectifier diode
- 6 Voltage rating (100 = 100 V)
- 7 Lead (Pb)-free

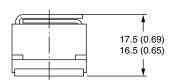
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95020		

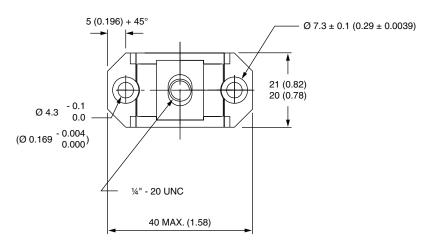


D-67 HALF-PAK

DIMENSIONS in millimeters (inches)









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