

Vishay Semiconductors

### Dome Lens SMD LED



#### **DESCRIPTION**

The dome lens SMD LED series has been designed in a small untinted and clear molded package with lens for surface mounting as gullwing or reverse gullwing version. The VLD.1235... series is using recent ultrabright AllnGaP / Si chip technology with high luminous flux and large chip size allowing a high DC forward current up to 70 mA.

#### PRODUCT GROUP AND PACKAGE DATA

• Product group: LED · Product series: power · Package: SMD dome lens Angle of half intensity: ± 11°

#### **FEATURES**

- Utilizing latest advanced AllnGaP technology
- · Package type: surface mount
- Package form: gullwing, reverse gullwing
- Dimensions (L x W x H in mm): 2.3 x 2.3 x 2.8
- · High luminous flux and luminous intensity
- · Luminous intensity and color categorized per packing unit
- · Luminous intensity ratio per packing unit  $I_{Vmax}/I_{Vmin.} \le 1.6$
- ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- Preconditioning according to JEDEC® level 2a
- Suitable for reflow soldering according to J-STD-020
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

- Traffic signals and signs
- Interior and exterior lighting
- Indicator and backlighting purposes for audio, video, LCDs switches, symbols, illuminated advertising etc.

PARTS TA	ABLE													
PART COLOR		_	UMINOL NTENSIT (mcd)	-	I <sub>F</sub> (nn		(nm)		at I <sub>F</sub>	FORWARD VOLTAGE (V)		at I <sub>F</sub>	TECHNOLOGY	
		MIN.	TYP.	MAX.	(mA)	MIN.	TYP.	MAX.	(mA)	MIN.	TYP.	MAX.	(mA)	
VLDS1235G	Super red	5600	11 000	22 400	50	626	630	637	50	1.9	2.2	2.7	50	AllnGaP on Si
VLDS1235R	Super red	5600	11 000	22 400	50	626	630	637	50	1.9	2.2	2.7	50	AllnGaP on Si
VLDR1235G	Red	9000	14 500	35 500	50	619	624	631	50	1.9	2.2	2.7	50	AllnGaP on Si
VLDR1235R	Red	9000	14 500	35 500	50	619	624	631	50	1.9	2.2	2.7	50	AllnGaP on Si
VLDK1235G	Amber	9000	18 000	35 500	50	611	616	621	50	1.9	2.25	2.7	50	AllnGaP on Si
VLDK1235R	Amber	9000	18 000	35 500	50	611	616	621	50	1.9	2.25	2.7	50	AllnGaP on Si
VLDY1235G	Yellow	9000	18 000	35 500	50	583	589	595	50	1.9	2.3	2.7	50	AllnGaP on Si
VLDY1235R	Yellow	9000	18 000	35 500	50	583	589	595	50	1.9	2.3	2.7	50	AllnGaP on Si

	TINGS (T <sub>amb</sub> = 25 °C, unless otherwi	ise specified)		
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage (1)	Short term application only	V <sub>R</sub>	5	V
DC Forward current	T <sub>amb</sub> ≤ 60 °C	I <sub>F</sub>	70	mA
Power dissipation		P <sub>V</sub>	200	mW
Junction temperature		Tj	125	°C
Operating temperature range		T <sub>amb</sub>	-40 to +100	°C
Storage temperature range		T <sub>stg</sub>	-40 to +100	°C
Thermal resistance junction/ambient	Mounted on PC board (pad size > 16 mm <sup>2</sup> )	R <sub>thJA</sub>	325	K/W

(1) Driving the LED in reverse direction is suitable for a short term application only

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OPTICAL AND ELECTRICAL C VLDS1235G, VLDS1235R, SU		<sub>b</sub> = 25 °C, ur	less othe	rwise spe	ecified)	
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity (1)	I <sub>F</sub> = 50 mA	I <sub>V</sub>	5600	11 000	22 400	mcd
Luminous flux/luminous intensity		φ <sub>V</sub> /I <sub>V</sub>	-	0.5	-	mlm/mcd
Dominant wavelength (1)	I <sub>F</sub> = 50 mA	λ <sub>d</sub>	626	630	637	nm
Peak wavelength	I <sub>F</sub> = 50 mA	λρ	-	639	-	nm
Spectral bandwidth at 50 % I <sub>rel max</sub> .	I <sub>F</sub> = 50 mA	Δλ	-	18	-	nm
Angle of half intensity	I <sub>F</sub> = 50 mA	φ	-	± 11	-	deg
Forward voltage (1)	I <sub>F</sub> = 50 mA	V <sub>F</sub>	1.9	2.2	2.7	V
Reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	-	0.01	10	μΑ

#### Note

 $<sup>^{(1)}</sup>$  Tolerances:  $\pm$  15 % for  $I_V,$   $\pm$  0.1 V for  $V_F,$   $\pm$  1 nm for  $\lambda_d.$ 

OPTICAL AND ELECTRICAL C VLDR1235G, VLDR1235R, RE		<sub>b</sub> = 25 °C, un	less othe	rwise spe	ecified)	
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity (1)	I <sub>F</sub> = 50 mA	I <sub>V</sub>	9000	14 500	35 500	mcd
Luminous flux/luminous intensity		φ <sub>V</sub> /I <sub>V</sub>	-	0.5	-	mlm/mcd
Dominant wavelength (1)	I <sub>F</sub> = 50 mA	λ <sub>d</sub>	619	624	631	nm
Peak wavelength	I <sub>F</sub> = 50 mA	λρ	-	632	-	nm
Spectral bandwidth at 50 % I <sub>rel max.</sub>	I <sub>F</sub> = 50 mA	Δλ	-	18	-	nm
Angle of half intensity	I <sub>F</sub> = 50 mA	φ	-	± 11	-	deg
Forward voltage (1)	I <sub>F</sub> = 50 mA	V <sub>F</sub>	1.9	2.2	2.7	V
Reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	-	0.01	10	μΑ

### Note

 $<sup>^{(1)}</sup>$  Tolerances:  $\pm$  15 % for  $I_V,\,\pm$  0.1 V for  $V_F,\,\pm$  1 nm for  $\lambda_d.$ 

OPTICAL AND ELECTRICAL C VLDK1235G, VLDK1235R, AN	·	<sub>b</sub> = 25 °C, un	less othe	rwise spe	ecified)	
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity (1)	I <sub>F</sub> = 50 mA	I <sub>V</sub>	9000	18 000	35 500	mcd
Luminous flux/luminous intensity		φ <sub>V</sub> /I <sub>V</sub>	-	0.5	-	mlm/mcd
Dominant wavelength (1)	I <sub>F</sub> = 50 mA	$\lambda_d$	611	616	621	nm
Peak wavelength	I <sub>F</sub> = 50 mA	$\lambda_{p}$	-	622	-	nm
Spectral bandwidth at 50 % I <sub>rel max.</sub>	I <sub>F</sub> = 50 mA	Δλ	-	18	-	nm
Angle of half intensity	I <sub>F</sub> = 50 mA	φ	-	± 11	-	deg
Forward voltage (1)	I <sub>F</sub> = 50 mA	V <sub>F</sub>	1.9	2.25	2.7	V
Reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	-	0.01	10	μΑ

#### Note

 $<sup>^{(1)}</sup>$  Tolerances:  $\pm$  15 % for  $I_V,\,\pm$  0.1 V for  $V_F,\,\pm$  1 nm for  $\lambda_d.$ 



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OPTICAL AND ELECTRICAL C VLDY1235G, VLDY1235R, YEI		<sub>b</sub> = 25 °C, un	less othe	rwise spe	ecified)	
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity (1)	I <sub>F</sub> = 50 mA	I <sub>V</sub>	9000	18 000	35 500	mcd
Luminous flux/luminous intensity		φ <sub>V</sub> /I <sub>V</sub>	-	0.5	-	mlm/mcd
Dominant wavelength (1)	I <sub>F</sub> = 50 mA	λ <sub>d</sub>	583	589	595	nm
Peak wavelength	I <sub>F</sub> = 50 mA	λρ	-	591	-	nm
Spectral bandwidth at 50 % I <sub>rel max</sub> .	I <sub>F</sub> = 50 mA	Δλ		17		nm
Angle of half intensity	I <sub>F</sub> = 50 mA	φ		± 11		deg
Forward voltage (1)	I <sub>F</sub> = 50 mA	V <sub>F</sub>	1.9	2.3	2.7	V
Reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>		0.01	10	μΑ

#### Note

 $<sup>^{(1)}</sup>$  Tolerances:  $\pm$  15 % for  $I_V,\,\pm$  0.1 V for  $V_F,\,\pm$  1 nm for  $\lambda_d.$ 

COLOR CLASSIFIC	CLASSIFICATION					
		DOMINANT WA	VELENGTH (nm)			
GROUP	AM	BER	YEL	LOW		
	MIN.	MAX.	MIN.	MAX.		
2	611	616				
3	616	621	583	586		
4			586	589		
5			589	592		
6			592	595		

### Note

• Wavelengths are tested at a current pulse duration of 25 ms and an accuracy of  $\pm$  1 nm.

UMINOUS INTENSITY CLASSIFICATION					
GROUP	LUMINOUS IN	TENSITY (mcd)			
STANDARD	MIN.	MAX.			
DB	5600	7100			
EA	7100	9000			
EB	9000	11 200			
FA	11 200	14 000			
FB	14 000	18 000			
GA	18 000	22 400			
GB	22 400	28 000			
HA	28 000	35 500			

#### Note

Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of ± 15 %.

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel).

In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel. In order to ensure availability, single wavelength groups will not be orderable.

## Vishay Semiconductors

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

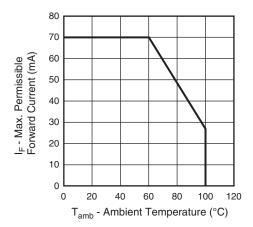


Fig. 1 - Maximum Permissible Forward Current vs.
Ambient Temperature

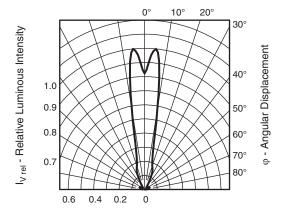


Fig. 2 - Relative Luminous Intensity vs. Angular Displacement

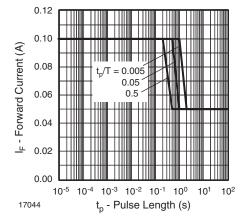


Fig. 3 - Forward Current vs. Pulse Length

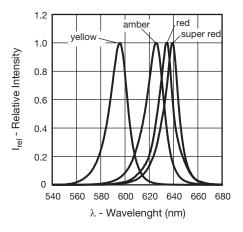


Fig. 4 - Relative Intensity vs. Wavelength

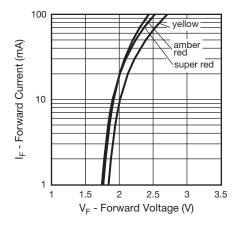


Fig. 5 - Forward Current vs. Forward Voltage

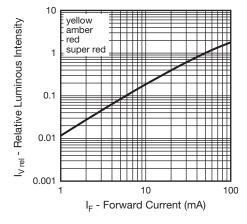


Fig. 6 - Relative Luminous Intensity vs. Forward Current

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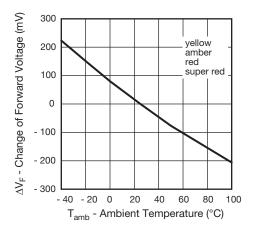


Fig. 7 - Change of Forward Voltage vs. Ambient Temperature

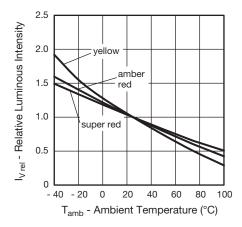


Fig. 8 - Relative Luminous Intensity vs. Ambient Temperature

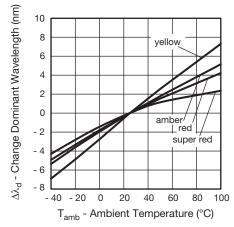
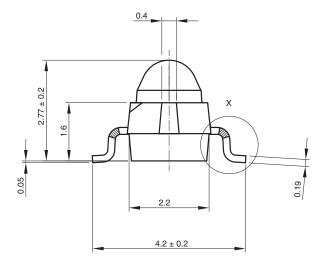
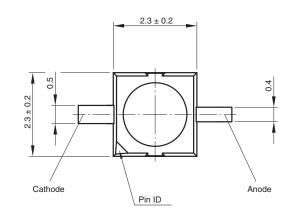


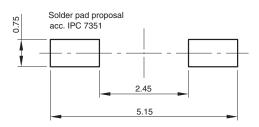
Fig. 9 - Change of Dominant Wavelength vs. Ambient Temperature

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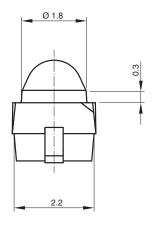
### PACKAGE DIMENSIONS in millimeters: VLD.1235G.. (gullwing)

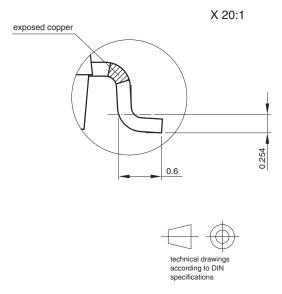


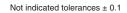


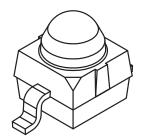


Drawing-No.: 6.544-5383.02-4 Issue: 4; 18.03.10 21488







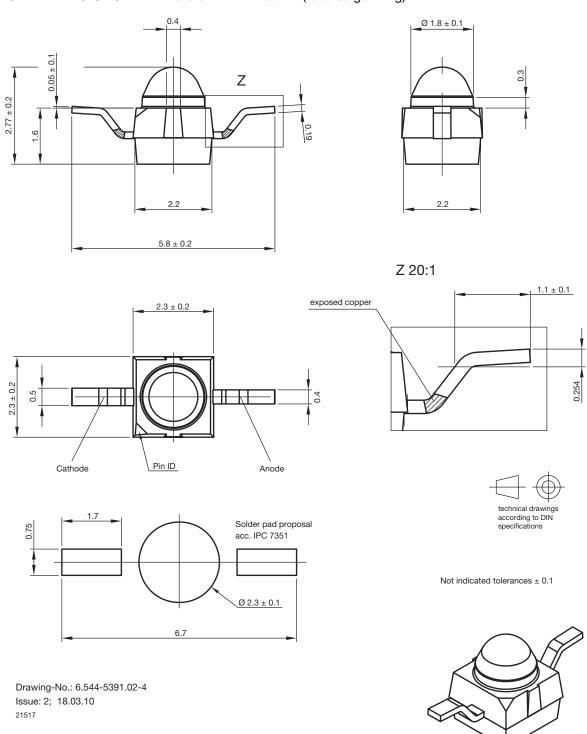




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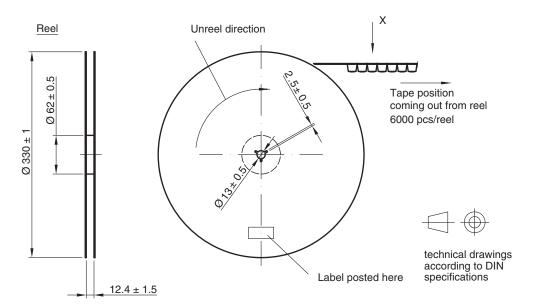
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### PACKAGE DIMENSIONS in millimeters: VLD.1235R.. (reverse gullwing)

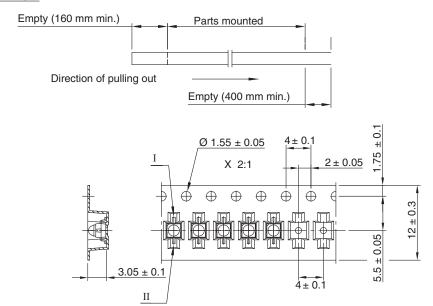


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### TAPING AND REEL DIMENSIONS in millimeters: VLD.1235G.. (gullwing)



#### Leader and trailer tape:



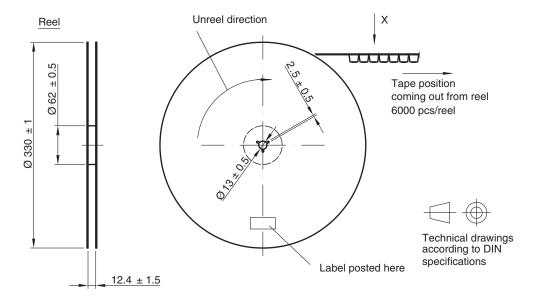
Drawing-No.: 9.800-5091.01-4

Issue: 3; 18.03.10

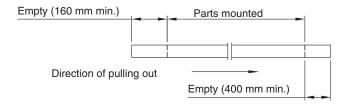


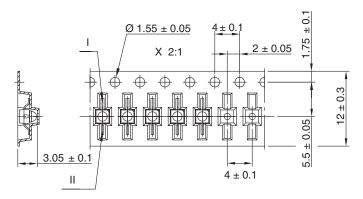
### Vishay Semiconductors

### TAPING AND REEL DIMENSIONS in millimeters: VLD.1235R.. (reverse gullwing)



Leader and trailer tape:





Drawing-No.: 9.800-5100.01-4

Issue: 2; 18.03.10

### **COVER TAPE PEEL STRENGTH**

According to DIN EN 60286-3 0.1 N to 1.3 N  $300 \pm 10$  mm/min  $165^{\circ}$  to  $180^{\circ}$  peel angle

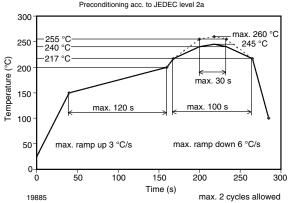
### **LABEL**

#### Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

### Vishay Semiconductors

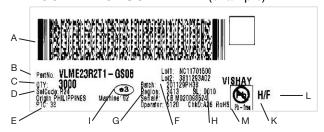
### **SOLDERING PROFILE**



IR Reflow Soldering Profile for Lead (Pb)-free Soldering

Fig. 10 - Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020)

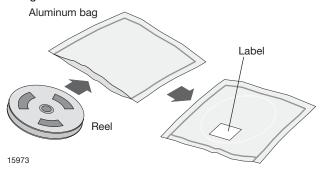
### **BAR CODE PRODUCT LABEL** (example)



- A. 2D barcode
- B. PartNo = Vishay part number
- C. QTY = Quantity
- D. SelCode = selection code (binning)
- E. PTC = Code of manufacturing plant
- F. Batch = date code: year / week / plant code
- G. Region code
- H. SL = sales location
- I. Terminations finishing
- K. Lead (Pb)-free symbol
- L. Halogen-free symbol
- M. RoHS symbol

### **DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



#### FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

#### RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

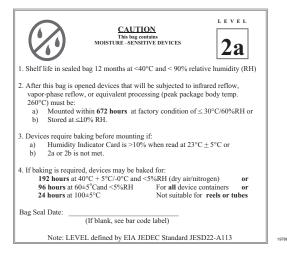
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

### **ESD PRECAUTION**

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

# VISHAY SEMICONDUCTORS STANDARD BAR CODE LABEL

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



### **Legal Disclaimer Notice**

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### **Material Category Policy**

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

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