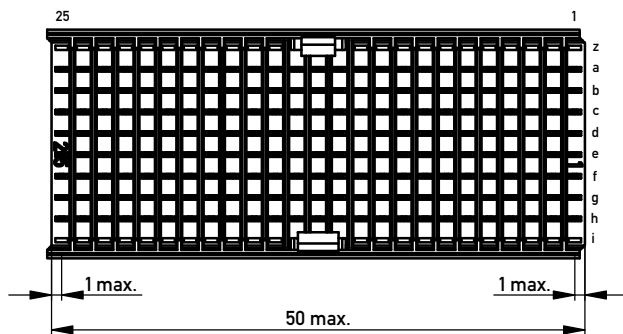
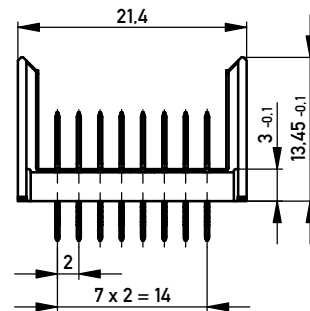
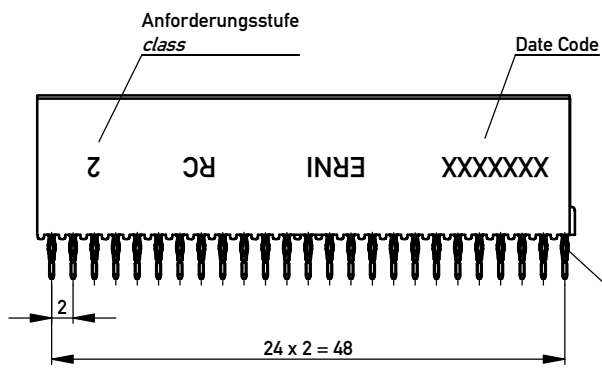
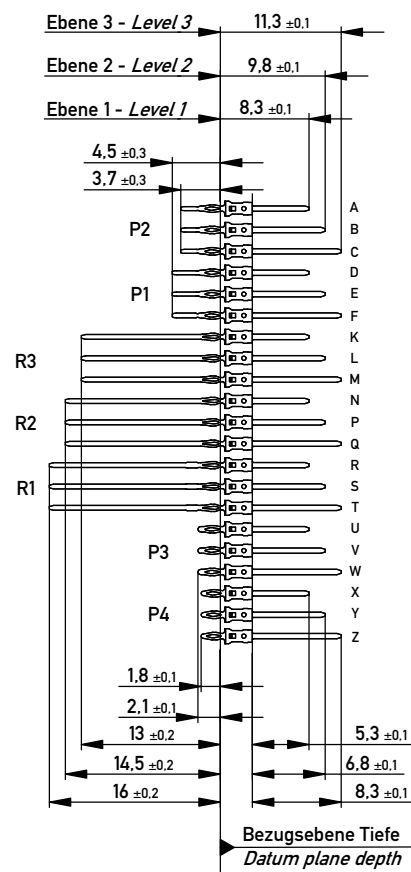
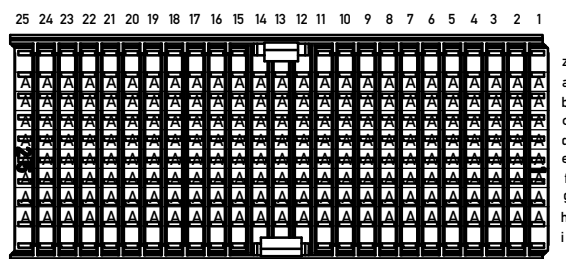


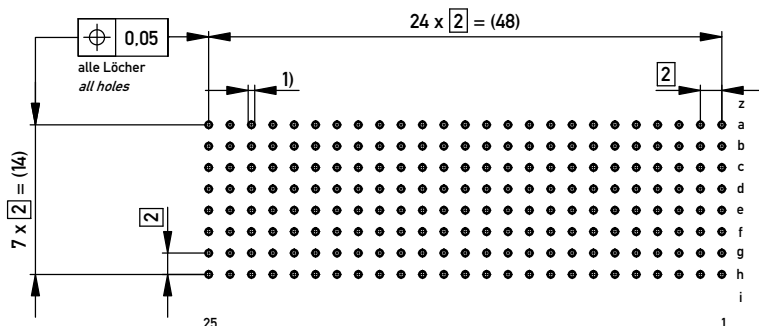


Bestückungsplan / *contact layout*



Lochbild für Leiterplatte
(Bestückungsseite)

*Board hole pattern
(Component mounting side)*



1) $\varnothing 0,6 \pm 0,05$ Durchmesser des metallisierten Loches
 $\varnothing 0,6 \pm 0,05$ Diameter of finished plated-through hole

Schichtaufbau im metallisierten Loch siehe Zeichnung 164062 Nr.1
Metal plating of plated-through hole see drawing 164062 No.1

Fehlende Maße und Angaben nach IEC 61076-4-101
Missing information and dimensions per IEC 61076-4-101

Au-plating!

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Information:
P2 Level 1
siehe Bestückungsplan / *see contact layout*
Anforderungsstufe 2 / *class 2*

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28.04.2015

Date

Tolerances



Scale 2:1

Subject to modification without
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Drawing will not be updated.

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