

74AUP2GU04

Low-power dual unbuffered inverter

Rev. 6 — 28 January 2019

Product data sheet

1. General description

The 74AUP2GU04 provides two unbuffered inverting gates.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- ESD protection:
 - HBM JESD22-A114F Class 3A exceeds 5000 V
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption; $I_{CC} = 0.9 \mu A$ (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74AUP2GU04GW	-40 °C to +125 °C	SC-88	plastic surface-mounted package; 6 leads	SOT363
74AUP2GU04GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm	SOT886
74AUP2GU04GF	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1 x 0.5 mm	SOT891
74AUP2GU04GN	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm	SOT1115
74AUP2GU04GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm	SOT1202

4. Marking

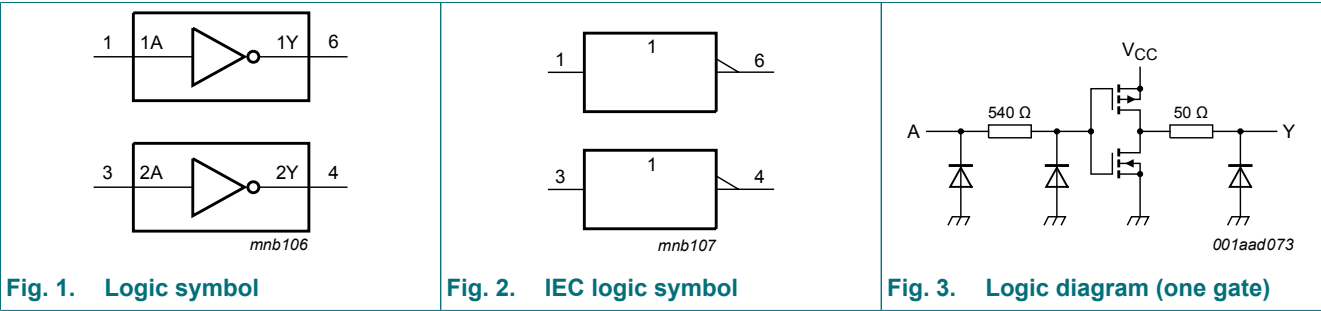
Table 2. Marking

Type number	Marking code ^[1]
74AUP2GU04GW	aD
74AUP2GU04GM	aD
74AUP2GU04GF	aD

Type number	Marking code ^[1]
74AUP2GU04GN	aD
74AUP2GU04GS	aD

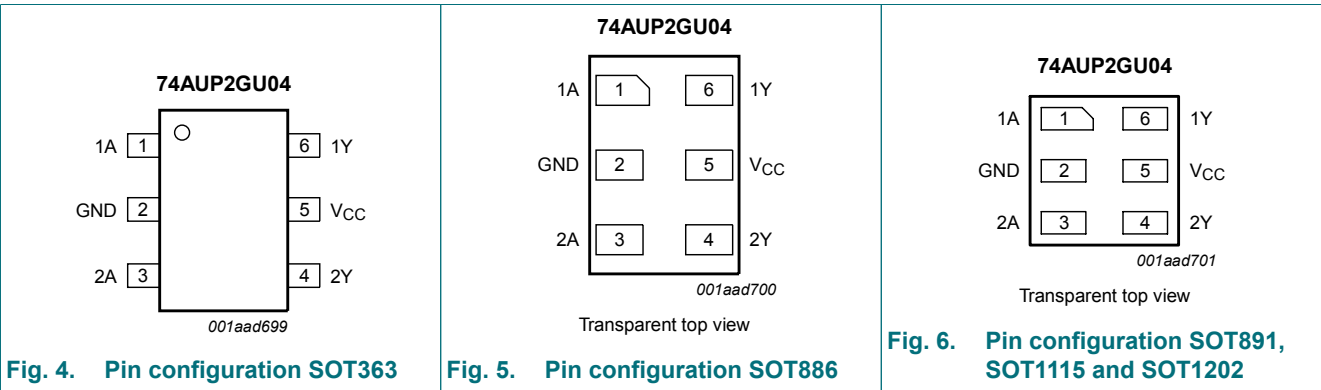
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram



6. Pinning information

6.1. Pinning



6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
1A	1	data input
GND	2	ground (0 V)
2A	3	data input
2Y	4	data output
V _{CC}	5	supply voltage
1Y	6	data output

7. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level.

Input	Output
nA	nY
L	H
H	L

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+4.6	V
I_{IK}	input clamping current	$V_I < 0$ V	-50	-	mA
V_I	input voltage	[1]	-0.5	+4.6	V
I_{OK}	output clamping current	$V_O < 0$ V	-50	-	mA
V_O	output voltage	[2]	-0.5	$V_{CC} + 0.5$	V
I_O	output current	$V_O = 0$ V to V_{CC}	-	± 20	mA
I_{CC}	supply current		-	50	mA
I_{GND}	ground current		-50	-	mA
T_{stg}	storage temperature		-65	+150	°C
P_{tot}	total power dissipation	$T_{amb} = -40$ °C to +125 °C [3]	-	250	mW

[1] The minimum input voltage ratings may be exceeded if the input current ratings are observed.

[2] The output voltage ratings may be exceeded if the output current ratings are observed.

[3] For SC-88 packages: above 87.5 °C the value of P_{tot} derates linearly with 4.0 mW/K.

For XSON6 packages: above 118 °C the value of P_{tot} derates linearly with 7.8 mW/K.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		0.8	3.6	V
V_I	input voltage		0	3.6	V
V_O	output voltage		0	V_{CC}	V
T_{amb}	ambient temperature		-40	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 0.8$ V to 3.6 V	0	200	ns/V

10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
T_{amb} = 25 °C						
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V to 3.6 V	0.75 × V _{CC}	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V to 3.6 V	-	-	0.25 × V _{CC}	V
V _{OH}	HIGH-level output voltage	V _I = GND or V _{CC}				
		I _O = -20 µA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.72	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.6	-	-	V
V _{OL}	LOW-level output voltage	V _I = GND or V _{CC}				
		I _O = 20 µA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V
I _I	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.1	µA
I _{CC}	supply current	V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 0.8 V to 3.6 V	-	-	0.5	µA
C _I	input capacitance	V _{CC} = 0 V to 3.6 V; V _I = GND or V _{CC}	-	1.5	-	pF
C _O	output capacitance	V _O = GND; V _{CC} = 0 V	-	1.8	-	pF
T_{amb} = -40 °C to +85 °C						
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V to 3.6 V	0.75 × V _{CC}	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V to 3.6 V	-	-	0.25 × V _{CC}	V
V _{OH}	HIGH-level output voltage	V _I = GND or V _{CC}				
		I _O = -20 µA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.7 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.03	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.97	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.85	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.67	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.55	-	-	V

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{OL}	LOW-level output voltage	V _I = GND or V _{CC}				
		I _O = 20 µA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.45	V
I _I	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.5	µA
I _{CC}	supply current	V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 0.8 V to 3.6 V	-	-	0.9	µA
T_{amb} = -40 °C to +125 °C						
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V to 3.6 V	0.75 × V _{CC}	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V to 3.6 V	-	-	0.25 × V _{CC}	V
V _{OH}	HIGH-level output voltage	V _I = GND or V _{CC}				
		I _O = -20 µA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.6 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.40	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.30	-	-	V
V _{OL}	LOW-level output voltage	V _I = GND or V _{CC}				
		I _O = 20 µA; V _{CC} = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	V
I _I	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.75	µA
I _{CC}	supply current	V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 0.8 V to 3.6 V	-	-	1.4	µA

11. Dynamic characteristics

Table 8. Dynamic characteristics

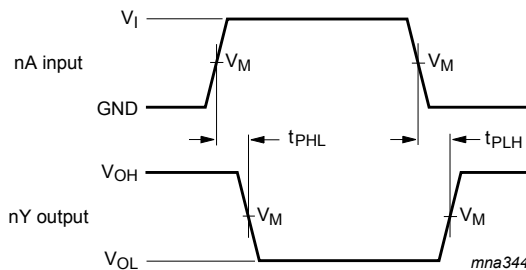
Voltages are referenced to GND (ground = 0 V); for test circuit see [Fig. 8](#).

Symbol	Parameter	Conditions	25 °C			-40 °C to +125 °C			Unit
			Min	Typ[1]	Max	Min	Max (85 °C)	Max (125 °C)	
C _L = 5 pF									
t _{pd}	propagation delay	nA to nY; see Fig. 7 [2]							
		V _{CC} = 0.8 V	-	6.2	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	0.9	2.3	4.4	0.9	4.8	5.3	ns
		V _{CC} = 1.4 V to 1.6 V	0.7	1.7	3.1	0.6	3.4	3.8	ns
		V _{CC} = 1.65 V to 1.95 V	0.5	1.4	2.6	0.5	2.9	3.2	ns
		V _{CC} = 2.3 V to 2.7 V	0.4	1.1	2.0	0.4	2.3	2.6	ns
		V _{CC} = 3.0 V to 3.6 V	0.3	1.0	1.8	0.3	2.1	2.4	ns
C _L = 10 pF									
t _{pd}	propagation delay	nA to nY; see Fig. 7 [2]							
		V _{CC} = 0.8 V	-	9.6	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	1.2	3.1	6.1	1.2	6.8	7.5	ns
		V _{CC} = 1.4 V to 1.6 V	1.0	2.3	4.0	0.9	4.6	5.1	ns
		V _{CC} = 1.65 V to 1.95 V	0.8	1.9	3.3	0.7	3.8	4.2	ns
		V _{CC} = 2.3 V to 2.7 V	0.6	1.5	2.7	0.6	3.1	3.5	ns
		V _{CC} = 3.0 V to 3.6 V	0.5	1.3	2.4	0.5	2.7	3.0	ns
C _L = 15 pF									
t _{pd}	propagation delay	nA to nY; see Fig. 7 [2]							
		V _{CC} = 0.8 V	-	13.0	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	1.6	3.8	7.9	1.4	8.8	9.7	ns
		V _{CC} = 1.4 V to 1.6 V	1.3	2.8	4.9	1.1	5.7	6.3	ns
		V _{CC} = 1.65 V to 1.95 V	1.0	2.3	4.0	0.9	4.7	5.2	ns
		V _{CC} = 2.3 V to 2.7 V	0.8	1.9	3.2	0.8	3.7	4.1	ns
		V _{CC} = 3.0 V to 3.6 V	0.7	1.6	2.9	0.7	3.3	3.7	ns
C _L = 30 pF									
t _{pd}	propagation delay	nA to nY; see Fig. 7 [2]							
		V _{CC} = 0.8 V	-	23.2	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.4	6.0	13.1	2.2	14.8	16.3	ns
		V _{CC} = 1.4 V to 1.6 V	2.0	4.2	7.6	1.8	9.0	9.9	ns
		V _{CC} = 1.65 V to 1.95 V	1.7	3.6	6.1	1.5	7.2	8.0	ns
		V _{CC} = 2.3 V to 2.7 V	1.4	2.9	4.8	1.3	5.7	6.3	ns
		V _{CC} = 3.0 V to 3.6 V	1.2	2.5	4.3	1.1	5.1	5.7	ns

Symbol	Parameter	Conditions	25 °C			-40 °C to +125 °C			Unit
			Min	Typ[1]	Max	Min	Max (85 °C)	Max (125 °C)	
C _L = 5 pF, 10 pF, 15 pF and 30 pF									
C _{PD}	power dissipation capacitance	f _i = 1 MHz; V _I = GND to V _{CC} [3][4]							
		V _{CC} = 0.8 V	-	1.1	-	-	-	-	pF
		V _{CC} = 1.1 V to 1.3 V	-	1.1	-	-	-	-	pF
		V _{CC} = 1.4 V to 1.6 V	-	1.3	-	-	-	-	pF
		V _{CC} = 1.65 V to 1.95 V	-	1.5	-	-	-	-	pF
		V _{CC} = 2.3 V to 2.7 V	-	3.0	-	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	4.5	-	-	-	-	pF

- [1] All typical values are measured at nominal V_{CC}.
 [2] t_{pd} is the same as t_{PLH} and t_{PHL}.
 [3] All specified values are the average typical values over all stated loads.
 [4] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).
 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$ where:
 f_i = input frequency in MHz;
 f_o = output frequency in MHz;
 C_L = load capacitance in pF;
 V_{CC} = supply voltage in V;
 N = number of inputs switching;
 $\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

11.1. Waveforms and test circuit



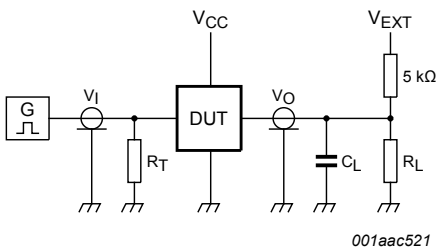
Measurement points are given in [Table 9](#).

Logic levels: V_{OL} and V_{OH} are typical output voltage drops that occur with the output load.

Fig. 7. The data input (nA) to output (nY) propagation delays

Table 9. Measurement points

Supply voltage	Output	Input		
V _{CC}	V _M	V _M	V _I	t _r = t _f
0.8 V to 3.6 V	0.5 × V _{CC}	0.5 × V _{CC}	V _{CC}	≤ 3.0 ns



Test data is given in [Table 10](#).
Definitions for test circuit:
 R_L = Load resistance.
 C_L = Load capacitance including jig and probe capacitance.
 R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator.
 V_{EXT} = External voltage for measuring switching times.

Fig. 8. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Load		V_{EXT}		
V_{CC}	C_L	R_L [1]	t_{PLH} , t_{PHL}	t_{PZH} , t_{PHZ}	t_{PZL} , t_{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	$2 \times V_{CC}$

- [1] For measuring enable and disable times $R_L = 5 \text{ k}\Omega$.
For measuring propagation delays, set-up and hold times and pulse width $R_L = 1 \text{ M}\Omega$.

12. Additional characteristics

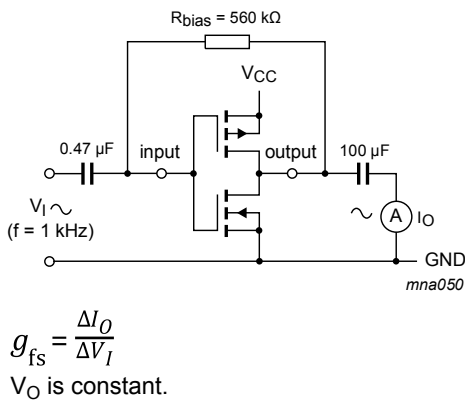


Fig. 9. Test set-up for measuring forward transconductance

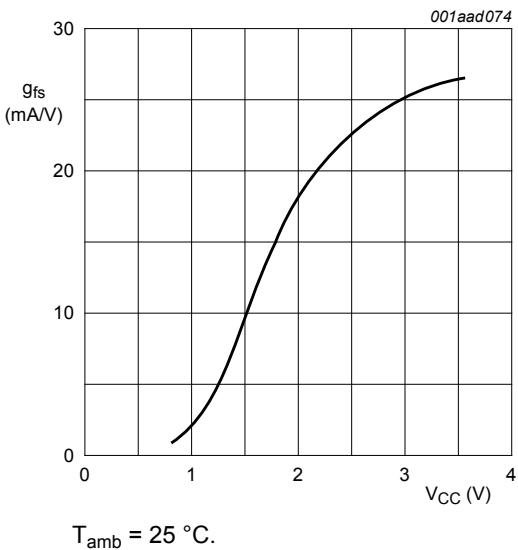


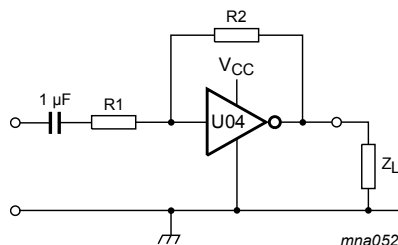
Fig. 10. Typical forward transconductance as a function of supply voltage

13. Application information

Some applications for the 74AUP2GU04 are:

- Linear amplifier (see [Fig. 11](#))
- Crystal oscillator (see [Fig. 12](#))

Remark: All values given are typical values unless otherwise specified.



$$Z_L > 10 \text{ k}\Omega.$$

$$R1 \geq 3 \text{ k}\Omega.$$

$$R2 \leq 1 \text{ M}\Omega.$$

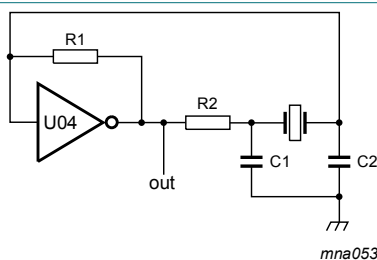
$$\text{Open loop amplification: } A_{OL} = 20.$$

$$\text{Voltage amplification: } A_V = -\frac{A_{OL}}{1 + \frac{R1}{R2}(1 + A_{OL})}.$$

$$V_{O(p-p)} = V_{CC} - 1.5 \text{ V centered at } 0.5 \times V_{CC}.$$

$$\text{Unity gain bandwidth product is } 5 \text{ MHz}.$$

Fig. 11. Linear amplifier application



$$C1 = 47 \text{ pF}.$$

$$C2 = 22 \text{ pF}.$$

$$R1 = 1 \text{ M}\Omega \text{ to } 10 \text{ M}\Omega.$$

R2 optimum value depends on the frequency and required stability against changes in V_{CC} or average minimum I_{CC} ($I_{CC} = 2 \text{ mA}$ at $V_{CC} = 3.3 \text{ V}$ and $f = 10 \text{ MHz}$).

Fig. 12. Crystal oscillator application

14. Package outline

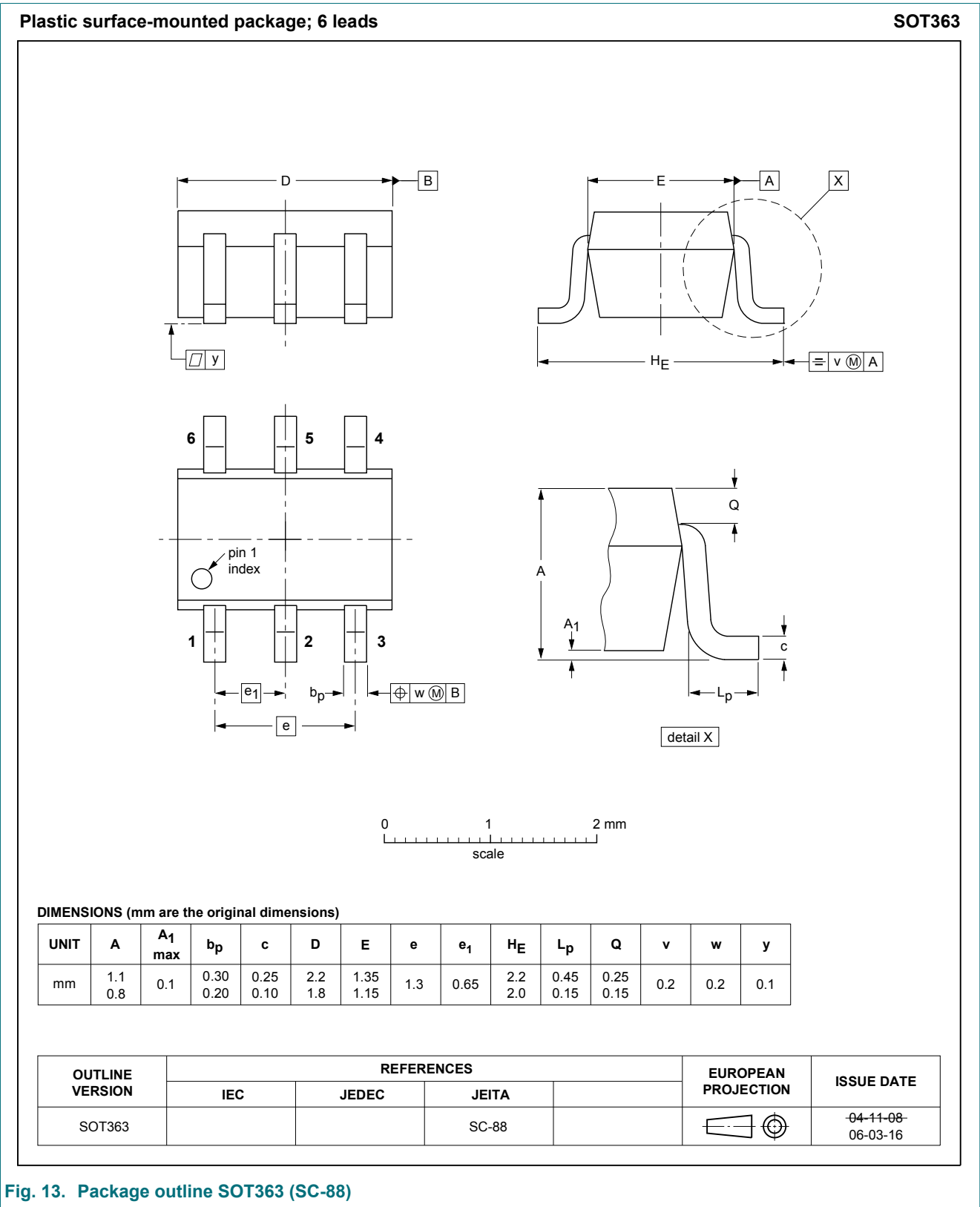
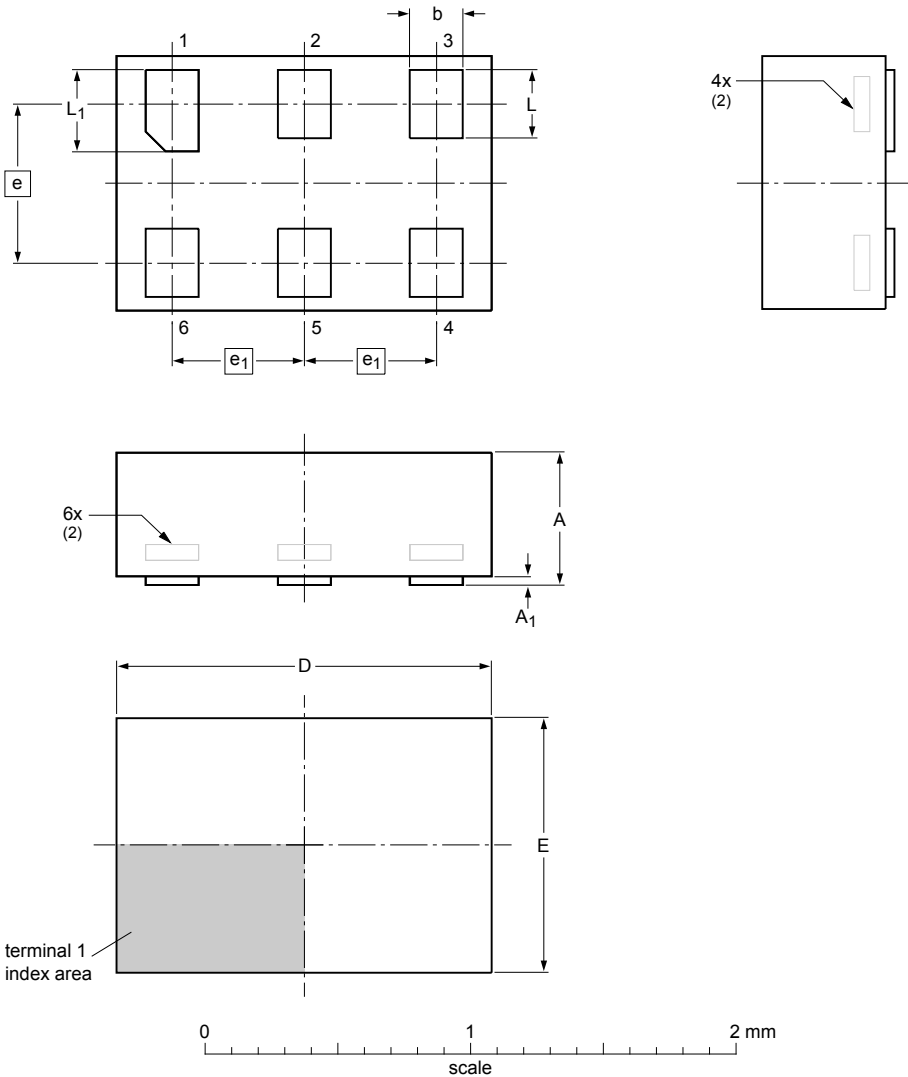


Fig. 13. Package outline SOT363 (SC-88)

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm

SOT886



Dimensions (mm are the original dimensions)

Unit	A ⁽¹⁾	A ₁	b	D	E	e	e ₁	L	L ₁
mm	max	0.5	0.04	0.25	1.50	1.05		0.35	0.40
	nom			0.20	1.45	1.00	0.6	0.5	0.35
	min			0.17	1.40	0.95		0.27	0.32

- Notes
- 1. Including plating thickness.
 - 2. Can be visible in some manufacturing processes.

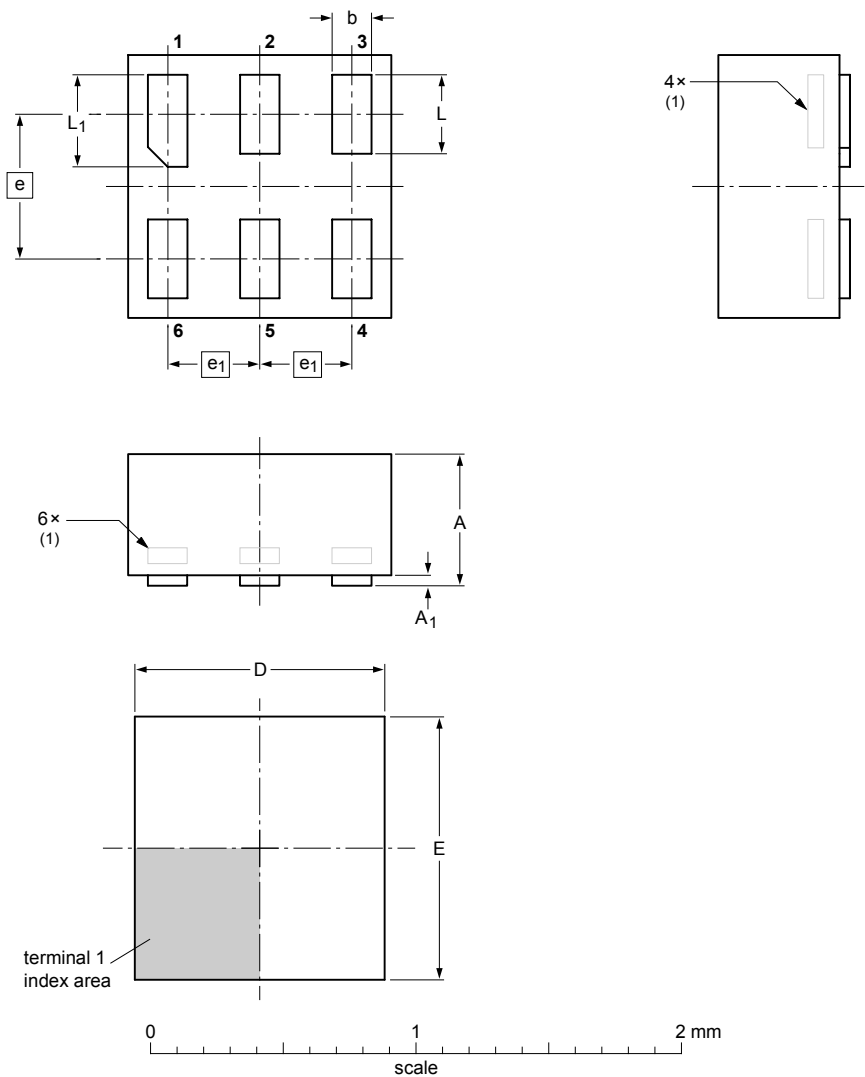
sot886_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT886		MO-252				-04-07-22- 12-01-05

Fig. 14. Package outline SOT886 (XSON6)

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1 x 0.5 mm

SOT891



DIMENSIONS (mm are the original dimensions)

UNIT	A _{max}	A _{1max}	b	D	E	e	e ₁	L	L ₁
mm	0.5	0.04	0.20 0.12	1.05 0.95	1.05 0.95	0.55	0.35	0.35 0.27	0.40 0.32

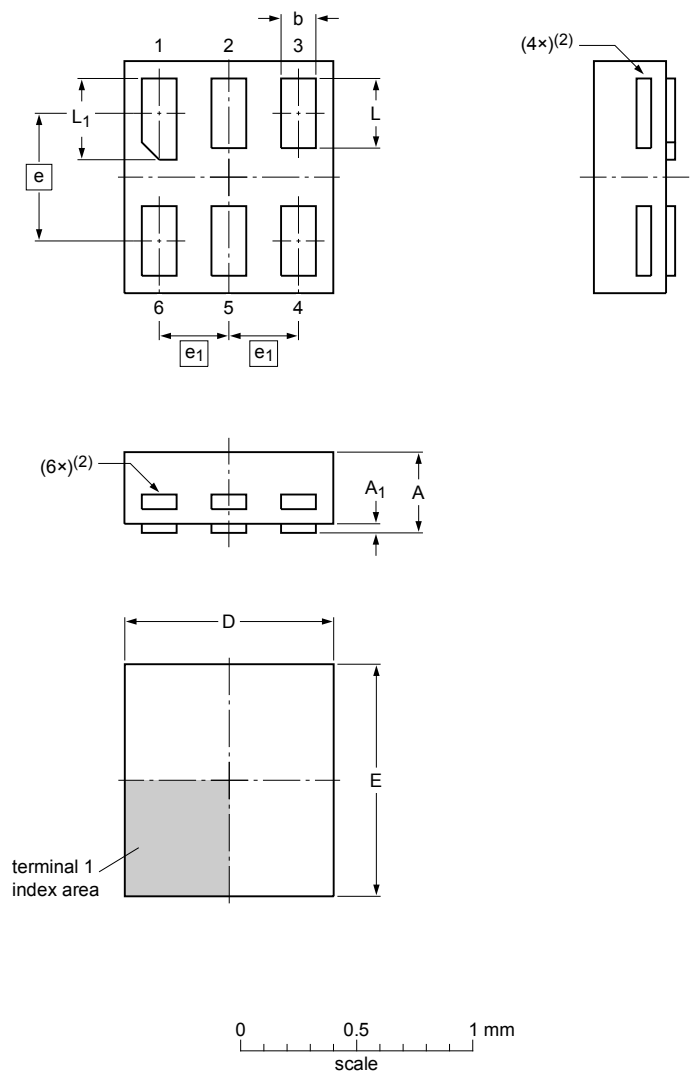
Note
1. Can be visible in some manufacturing processes.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT891						-05-04-06 07-05-15

Fig. 15. Package outline SOT891 (XSON6)

XSON6: extremely thin small outline package; no leads;
6 terminals; body 0.9 x 1.0 x 0.35 mm

SOT1115



Dimensions

Unit	A ⁽¹⁾	A ₁	b	D	E	e	e ₁	L	L ₁
mm	max	0.35	0.04	0.20	0.95	1.05		0.35	0.40
	nom			0.15	0.90	1.00	0.55	0.30	0.35
	min			0.12	0.85	0.95		0.27	0.32

Note

- 1. Including plating thickness.
- 2. Visible depending upon used manufacturing technology.

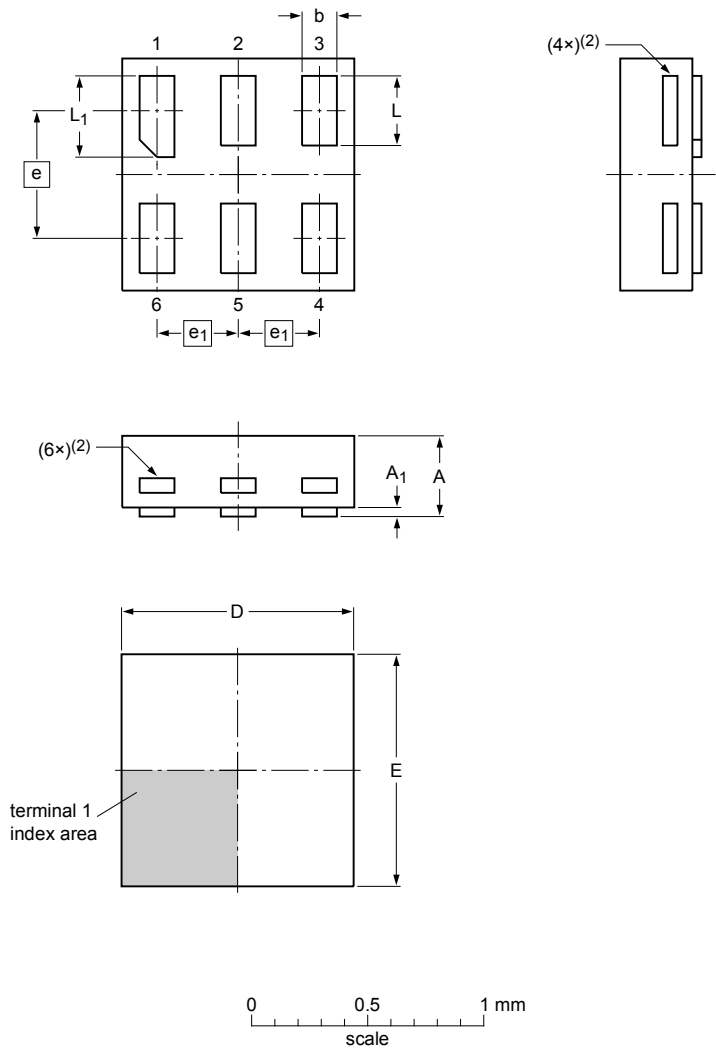
sot1115_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT1115						-10-04-02- 10-04-07

Fig. 16. Package outline SOT1115 (XSON6)

XSON6: extremely thin small outline package; no leads;
6 terminals; body 1.0 x 1.0 x 0.35 mm

SOT1202



Dimensions

Unit	A ⁽¹⁾	A ₁	b	D	E	e	e ₁	L	L ₁
mm	max	0.35	0.04	0.20	1.05	1.05		0.35	0.40
	nom			0.15	1.00	1.00	0.55	0.30	0.35
	min			0.12	0.95	0.95		0.27	0.32

Note

- 1. Including plating thickness.
- 2. Visible depending upon used manufacturing technology.

sot1202_po

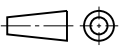
Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT1202						10-04-02 10-04-06

Fig. 17. Package outline SOT1202 (XSON6)

15. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model

16. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP2GU04 v.6	20190128	Product data sheet	-	74AUP2GU04 v.5
Modifications:	- The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. - Legal texts have been adapted to the new company name where appropriate.			
74AUP2GU04 v.5	20131011	Product data sheet	-	74AUP2GU04 v.4
Modifications:	Package outline drawing of SOT886 (Fig. 14) modified.			
74AUP2GU04 v.4	20111207	Product data sheet	-	74AUP2GU04 v.3
Modifications:	Legal pages updated.			
74AUP2GU04 v.3	20101110	Product data sheet	-	74AUP2GU04 v.2
74AUP2GU04 v.2	20090703	Product data sheet	-	74AUP2GU04 v.1
74AUP2GU04 v.1	20061215	Product data sheet	-	-

17. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal

injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nexperia.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Contents

1. General description.....	1
2. Features and benefits.....	1
3. Ordering information.....	1
4. Marking.....	1
5. Functional diagram.....	2
6. Pinning information.....	2
6.1. Pinning.....	2
6.2. Pin description.....	2
7. Functional description.....	3
8. Limiting values.....	3
9. Recommended operating conditions.....	3
10. Static characteristics.....	4
11. Dynamic characteristics.....	6
11.1. Waveforms and test circuit.....	7
12. Additional characteristics.....	8
13. Application information.....	9
14. Package outline.....	10
15. Abbreviations.....	15
16. Revision history.....	15
17. Legal information.....	16

© Nexperia B.V. 2019. All rights reserved

For more information, please visit: <http://www.nexperia.com>

For sales office addresses, please send an email to: salesaddresses@nexperia.com

Date of release: 28 January 2019