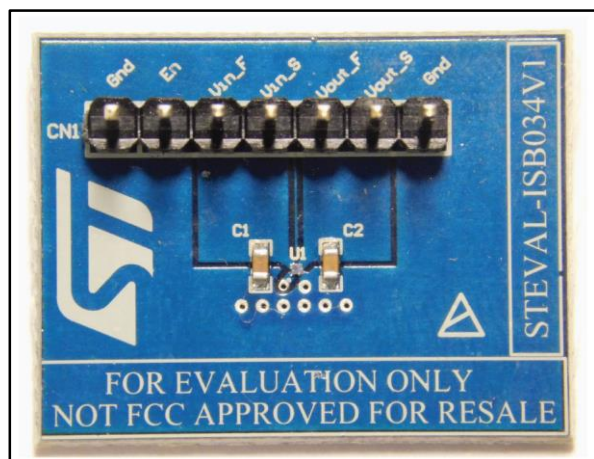


Evaluation board for the LDBL20 high PSRR, miniaturized 200 mA LDO

Data brief



Features

- Output current: 200 mA
- Logic controlled electronic shutdown
- High PSRR
 - 80 dB @ 1 kHz
 - 50 dB @ 100 kHz
- Quiescent current:
 - 20 μ A typ. at no load
 - 0.03 μ A typ. in off mode
- 2.5 V $\pm$ 2% output voltage
- Other voltage options available on request
- ST Stamp™ 0.47 x 0.47 mm² bumpless CSP
- RoHS compliant

Description

The STEVAL-ISB034V1 board features the LDBL20 high performance linear voltage regulator, configured to convert a 2.85 V to 5.5 V DC input voltage into a precise and stable 2.5 V output voltage.

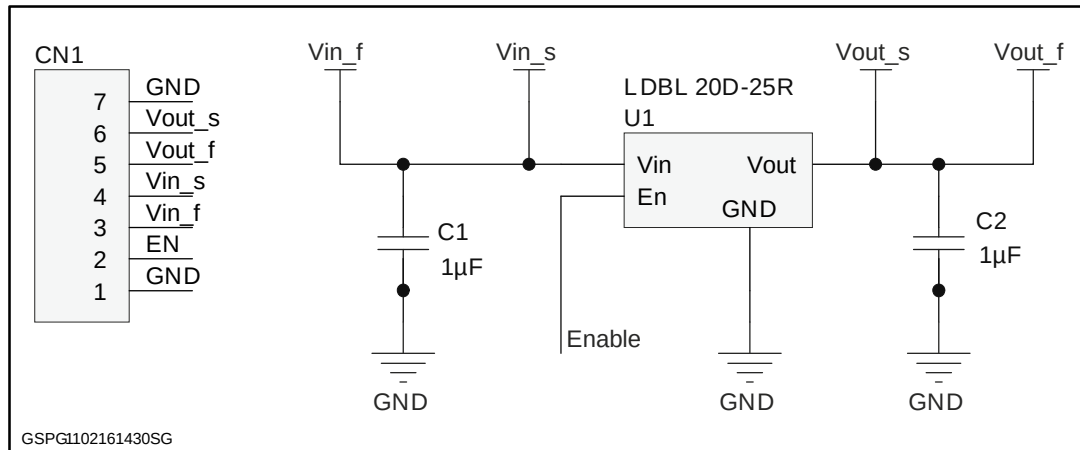
Only two small ceramic capacitors are needed to implement the linear regulator solution.

The very low dropout LDBL20 voltage regulator features high PSRR, low quiescent current and the minute ST Stamp™ chip-scale package, with a footprint of only 0.47 x 0.47 mm².

It is designed for low power battery-operated equipment like smartphones, tablets and wearable devices.

1 Schematic diagram

Figure 1: STEVAL-ISB034V1 circuit schematic



2 Revision history

Table 1: Document revision history

Date	Version	Changes
17-Feb-2016	1	Initial release.

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