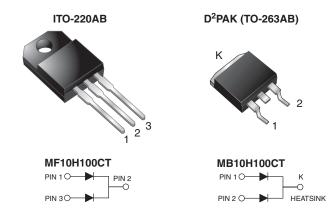


Vishay General Semiconductor

## **Dual Common Cathode High Voltage Schottky Rectifier**

High Barrier Technology for Improved High Temperature Performance



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 5 A			
V <sub>RRM</sub>	100 V			
I <sub>FSM</sub>	150 A			
V <sub>F</sub>	0.61 V			
I <sub>R</sub>	3.5 µA			
T <sub>J</sub> max.	175 °C			
Package	ITO-220AB, D <sup>2</sup> PAK (TO-263AB)			
Circuit configuration	Common cathode			

#### **FEATURES**

- Power pack
- · Guardring for overvoltage protection



- Low forward voltage drop
- · Low leakage current
- · High forward surge capability
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C (for D<sup>2</sup>PAK (TO-263AB) package)
- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106 (for ITO-220AB package)
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### TYPICAL APPLICATIONS

For use in high frequency rectifier of switching mode power supplies, freewheeling diodes, DC/DC converters, and polarity protection application.

### **MECHANICAL DATA**

Case: ITO-220AB, D2PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating Base P/NHE3\_X - RoHS-compliant, AEC-Q101 qualified ("\_X" denotes revision code e.g. A, B, .....)

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

HE3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T <sub>C</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	MB10H100CT	UNIT			
Maximum repetitive peak reverse voltage	$V_{RRM}$	100				
Working peak reverse voltage	V <sub>RWM</sub>	100	V			
Maximum DC blocking voltage		$V_{DC}$	100	1		
Maximum average femiliard restified assured at T 105 °C	total device		10			
Maximum average forward rectified current at T <sub>C</sub> = 105 °C	per diode	I <sub>F(AV)</sub>	5.0			
Peak forward surge current 8.3 ms single half sine-wave superimp per diode	I <sub>FSM</sub>	150	A			
Peak repetitive reverse current per diode at $t_p$ = 2.0 $\mu$ s, 1 kHz	I <sub>RRM</sub>	0.5				
Voltage rate of change (rated V <sub>R</sub> )	dV/dt	10 000	V/µs			
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +175	°C			
Isolation voltage (ITO-220AB only) from terminal to heatsink t = 1 min		V <sub>AC</sub>	1500	V		

# **MB10H100CT, MF10H100CT**

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>C</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUE	UNIT
Maximum instantaneous forward voltage per diode	V <sub>F</sub> <sup>(1)</sup>	I <sub>F</sub> = 5 A	T <sub>J</sub> = 25 °C	0.76	V
		I <sub>F</sub> = 5 A	T <sub>J</sub> = 125 °C	0.61	
		I <sub>F</sub> = 10 A	T <sub>J</sub> = 25 °C	0.85	
		I <sub>F</sub> = 10 A	T <sub>J</sub> = 125 °C	0.71	
Maximum reverse current per diode	I <sub>R</sub> <sup>(1)</sup>	I <sub>R</sub> <sup>(1)</sup> Rated V <sub>R</sub> -	T <sub>J</sub> = 25 °C	3.5	μΑ
			T <sub>J</sub> = 100 °C	4.5	mA

#### **Notes**

 $^{(1)}$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 40 ms

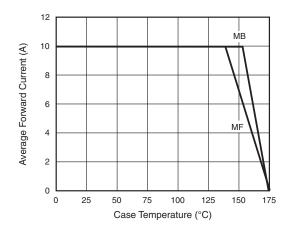
THERMAL CHARACTERISTICS (T <sub>C</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	МВ	MF	UNIT		
Typical thermal resistance per diode	$R_{ heta JC}$	2.2	5.2	°C/W		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
ITO-220AB	MF10H100CTHE3_B/P (1)	1.79	Р	50/tube	Tube	
TO-263AB	MB10H100CTHE3_B/P (1)	1.35	Р	50/tube	Tube	
TO-263AB	MB10H100CTHE3_B/I (1)	1.35	1	800/reel	Tape and reel	

### Note

(1) AEC-Q101 qualified

### RATINGS AND CHARACTERISTICS CURVES (T<sub>C</sub> = 25 °C unless otherwise noted)





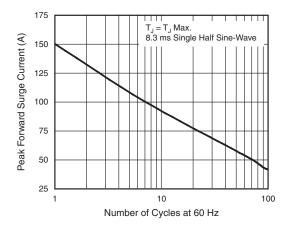


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Diode



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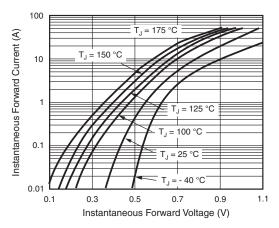


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

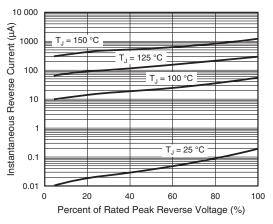


Fig. 4 - Typical Reverse Characteristics Per Diode

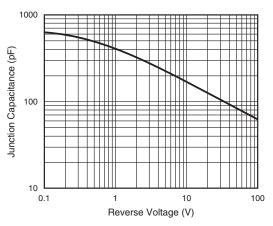


Fig. 5 - Typical Junction Capacitance Per Diode

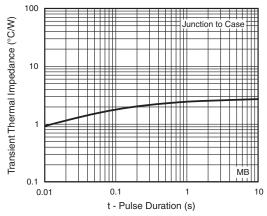


Fig. 6 - Typical Transient Thermal Impedance Per Diode

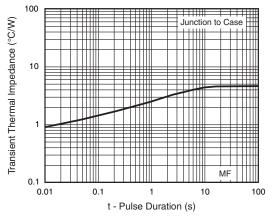
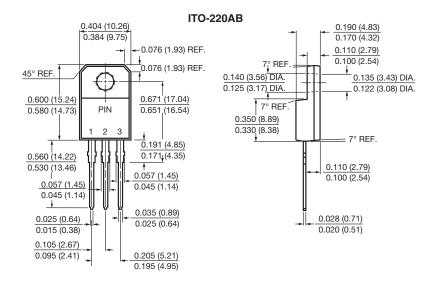


Fig. 7 - Typical Transient Thermal Impedance Per Diode

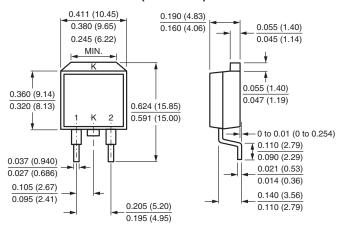
# **MB10H100CT, MF10H100CT**

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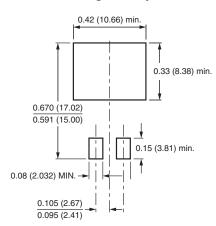
### PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



### D<sup>2</sup>PAK (TO-263AB)



### **Mounting Pad Layout**





### **Legal Disclaimer Notice**

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