

# **High Performance Schottky Rectifier, 220 A**



PRODUCT SUMMARY			
I <sub>F(AV)</sub>	220 A		
$V_{R}$	30 V		
Package	TO-244		
Circuit	Two diodes common cathodes		

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- · Center tap module
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

### **DESCRIPTION**

The VS-220CNQ.. center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature.

The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES U				
I <sub>F(AV)</sub>	Rectangular waveform	220	Α			
V <sub>RRM</sub>		30	V			
I <sub>FSM</sub>	$t_p = 5 \mu s sine$	18 000	Α			
V <sub>F</sub>	110 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.41	V			
TJ	Range	-55 to 150	°C			

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-220CNQ030PbF	UNITS
Maximum DC reverse voltage	$V_{R}$	30	V
Maximum working peak reverse voltage	$V_{RWM}$	30	V

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average forward current	per leg		50 % duty cycle at T <sub>C</sub> = 122 °C, rectangular waveform		50 0/ district outle at T 100 °C restance les verses en		110	
See fig. 5	per device	I <sub>F(AV)</sub>			Α			
Maximum peak one cycle surge current per leg	non-repetitive		I <sub>FSM</sub> 5 μs sine or 3 μs rect. pulse  Following any rated load condition and with rated V <sub>RRM</sub> applied		18 000	A		
See fig. 7		FSM			1950			
Non-repetitive avalanche	energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 15 A, L = 1 mH		99	mJ		
Repetitive avalanche curre	ent per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		А			

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST COI	VALUES	UNITS	
	V <sub>FM</sub> <sup>(1)</sup>	110 A	T 05 00	0.49	V
Maximum forwardvoltage drop per leg		220 A	T <sub>J</sub> = 25 °C	0.59	
See fig. 1		110 A	T 105 °C	0.41	
		220 A	T <sub>J</sub> = 125 °C	0.55	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm R}$ = Rated $V_{\rm R}$	10	mA
See fig. 2		T <sub>J</sub> = 125 °C	v <sub>R</sub> = nateu v <sub>R</sub>	650	
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz) 25 °C		7400	pF
Typical series inductance per leg	L <sub>S</sub>	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temper	rature range	T <sub>J</sub> , T <sub>Stg</sub>	-55	-	150	°C
Thermal resistance, junction to case	per leg	D	-	-	0.38	°C/W
merma resistance, junction to case	per module	$R_{thJC}$	-	-	0.19	
Thermal resistance, case to heatsink		R <sub>thCS</sub>	-	0.10	-	
Weight				68		g
weignt			_	2.4	_	OZ.
Mounting torque			35.4 (4)	-	53.1 (6)	
Mounting torque center hole			30 (3.4)	-	40 (4.6)	lbf ⋅ in (N ⋅ m)
Terminal torque			30 (3.4)	-	44.2 (5)	
Vertical pull			-	-	80	- lbf ⋅ in
2" lever pull			-	-	35	ווויוטו

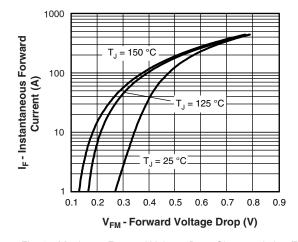


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

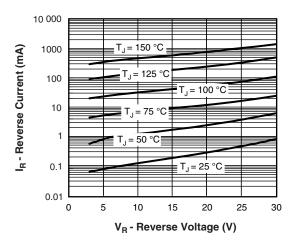


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)



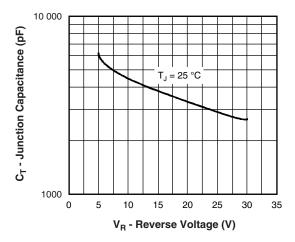


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

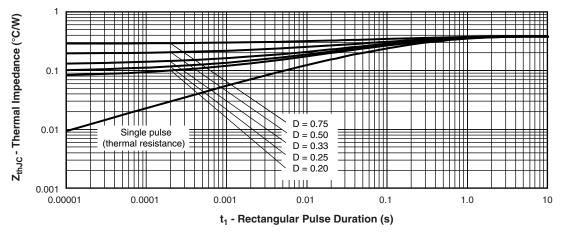


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

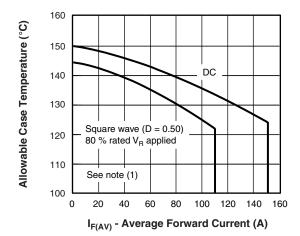


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

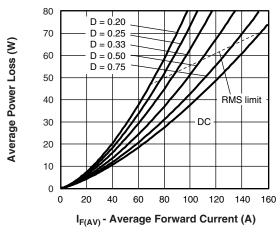


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

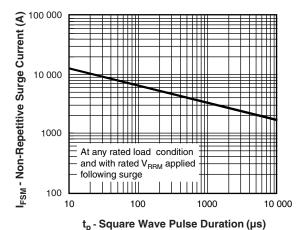


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

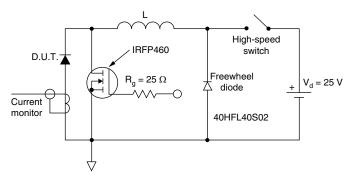


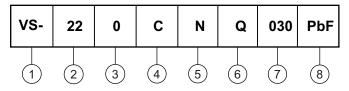
Fig. 8 - Unclamped Inductive Test Circuit

### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{thJC}}; \\ \text{Pd} = \text{Forward power loss} = I_{\text{F(AV)}} \times \text{V}_{\text{FM}} \text{ at } (I_{\text{F(AV)}}/D) \text{ (see fig. 6)}; \\ \text{Pd}_{\text{REV}} = \text{Inverse power loss} = \text{V}_{\text{R1}} \times \text{I}_{\text{R}} \text{ (1 - D)}; I_{\text{R}} \text{ at } \text{V}_{\text{R1}} = 80 \text{ \% rated V}_{\text{R}} \\ \end{array}$ 

### **ORDERING INFORMATION TABLE**

### Device code



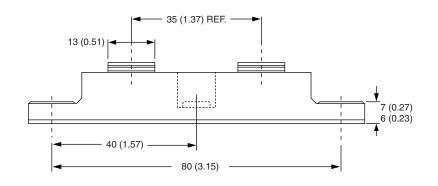
- Vishay Semiconductors product
- Average current rating (x 10)
- Product silicon identification
- C = Circuit configuration
- 5 N = Not isolated
- Q = Schottky rectifier diode
- 7 Voltage rating (30 V)
- 8 Lead (Pb)-free

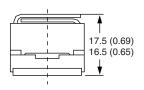
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			

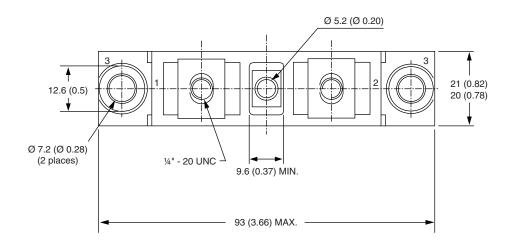


## **TO-244**

## **DIMENSIONS** in millimeters (inches)









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