

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
20V	24mΩ @ V _{GS} = 4.5V	6.8A
	32mΩ @ V _{GS} = 2.5V	5.9A

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power Management Functions
- Backlighting

Features and Benefits

- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

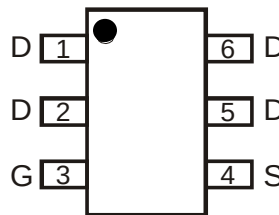
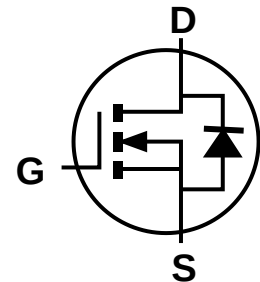
Mechanical Data

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.013 grams (Approximate)

TSOT26



Top View


 Top View
Pin Configuration


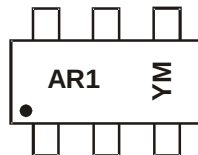
Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2029UVT-7	TSOT26	3,000/Tape & Reel
DMN2029UVT-13	TSOT26	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



AR1 = Product Type Marking Code
 YM = Date Code Marking
 Y or Y = Year (ex: F = 2018)
 M = Month (ex: 9 = September)

Date Code Key

Date Code Key

Year	2018	2019	2020	2021	2022	2023	2024	2025	2026
Code	F	G	H	I	J	K	L	M	N

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±10	V
Continuous Drain Current (Note 6) V _{GS} = 4.5V	Steady State	T _A = +25°C	I _D	6.8	A
		T _A = +70°C		5.5	A
Maximum Body Diode Forward Current (Note 6)			I _S	2	A
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)			I _{DM}	40	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	P _D	1.7	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{θJA}	109	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	P _D	0.7	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	74	°C/W
Thermal Resistance, Junction to Case (Note 6)		R _{θJC}	15	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	—	V	V _{GS} = 0V, I _D = 250µA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	µA	V _{DS} = 16V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	µA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.4	0.7	1.5	V	V _{DS} = V _{GS} , I _D = 250µA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	18	24	mΩ	V _{GS} = 4.5V, I _D = 6.2A
		—	21	32		V _{GS} = 2.5V, I _D = 5.2A
Diode Forward Voltage	V _{SD}	—	0.65	1.2	V	V _{GS} = 0V, I _S = 1.3A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{ISS}	—	646	—	pF	V _{DS} = 10V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{OSS}	—	78	—		
Reverse Transfer Capacitance	C _{rSS}	—	38	—		
Gate Resistance	R _g	—	628	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	—	7.1	—	nC	V _{DS} = 10V, I _D = 6.2A, V _{GS} = 4.5 V
Gate-Source Charge	Q _{gs}	—	0.9	—		
Gate-Drain Charge	Q _{gd}	—	0.7	—		
Turn-On Delay Time	t _{D(ON)}	—	98	—	ns	V _{DD} = 10V, V _{GS} = 4.5V, I _D = 1A, R _g = 6Ω
Turn-On Rise Time	t _r	—	139	—		
Turn-Off Delay Time	t _{D(OFF)}	—	1023	—		
Turn-Off Fall Time	t _f	—	433	—		
Reverse Recovery Time	t _{RR}	—	245	—	ns	I _F = 1.0A, di/dt = 100A/µs
Reverse Recovery Charge	Q _{RR}	—	148	—	nC	I _F = 1.0A, di/dt = 100A/µs

- Notes:
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to production testing.

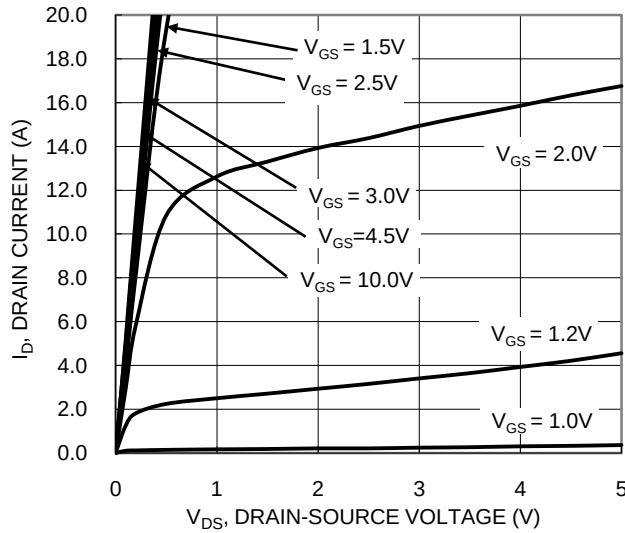


Figure 1. Typical Output Characteristic

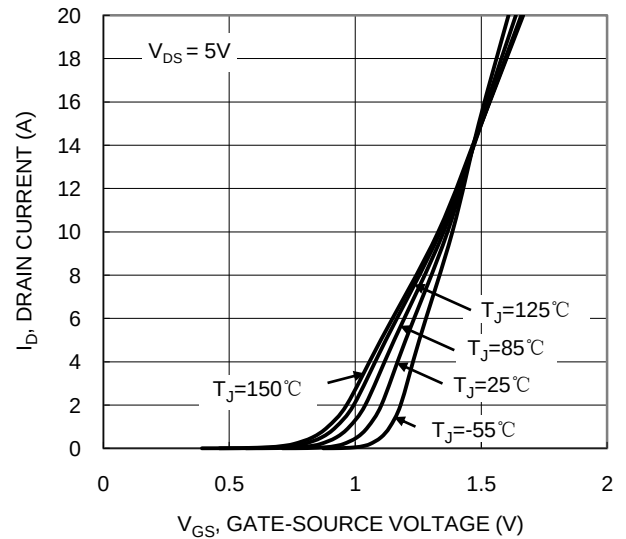


Figure 2. Typical Transfer Characteristic

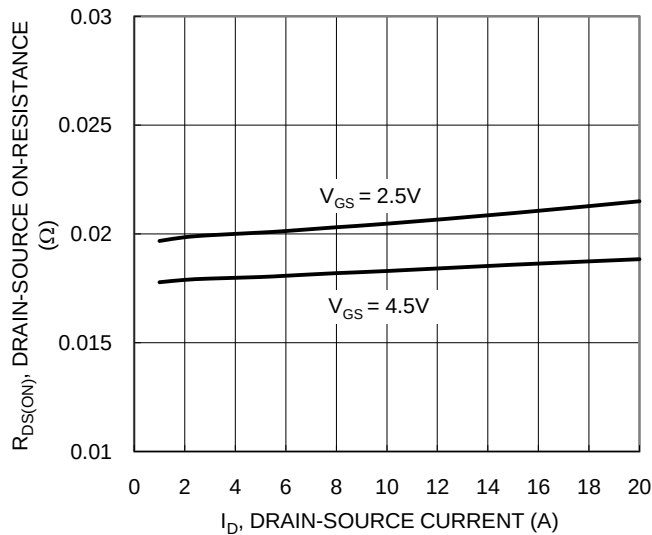


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

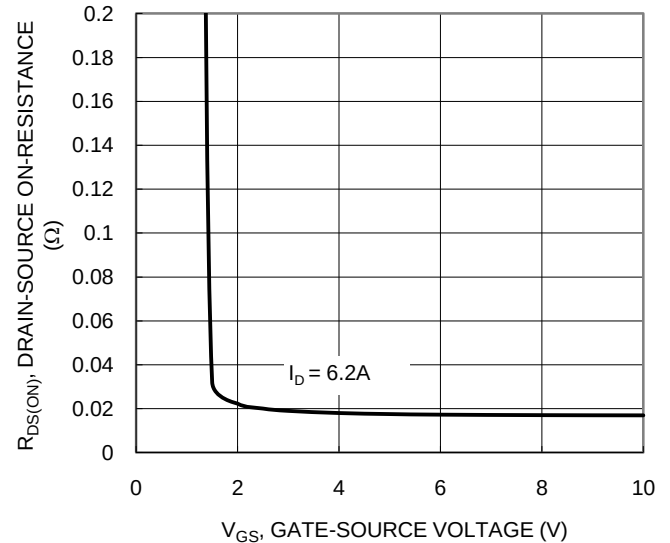


Figure 4. Typical Transfer Characteristic

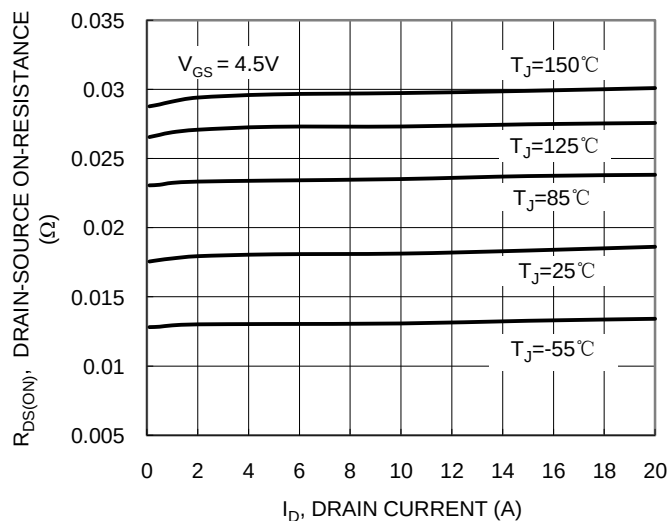


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

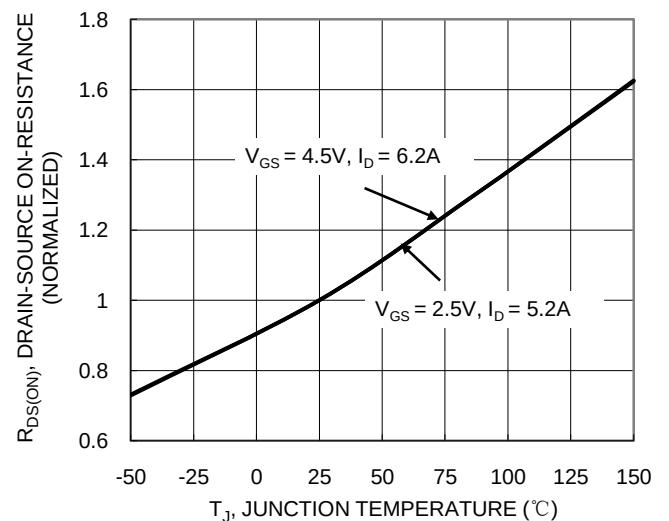
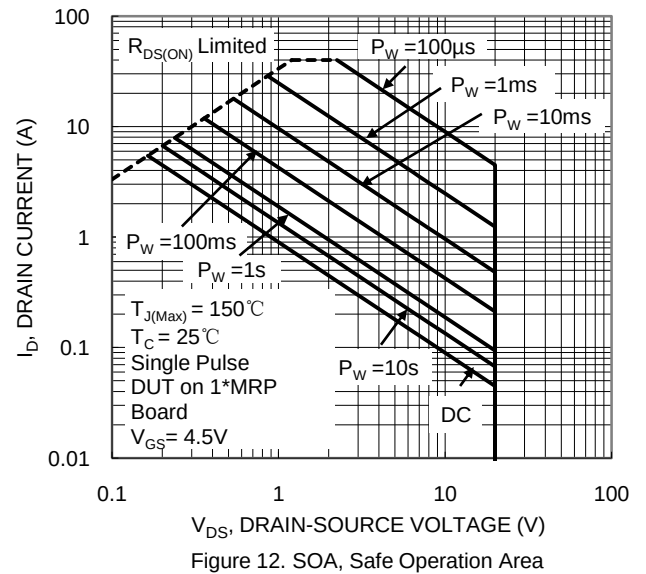
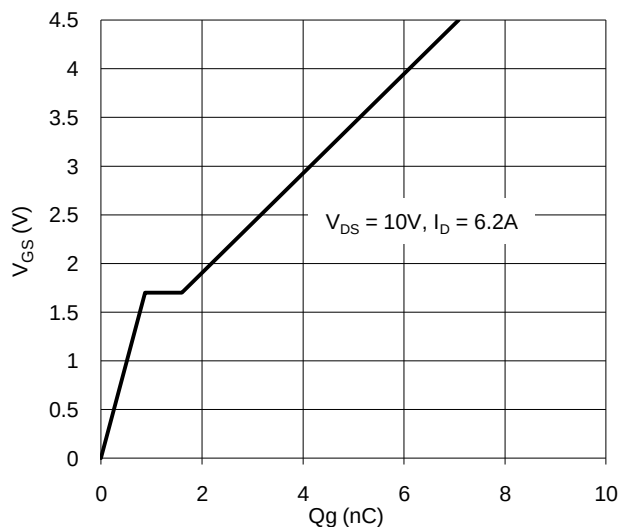
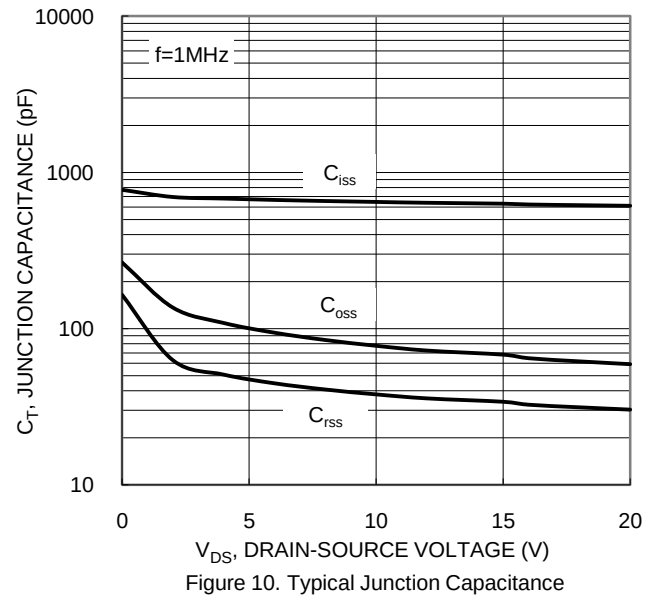
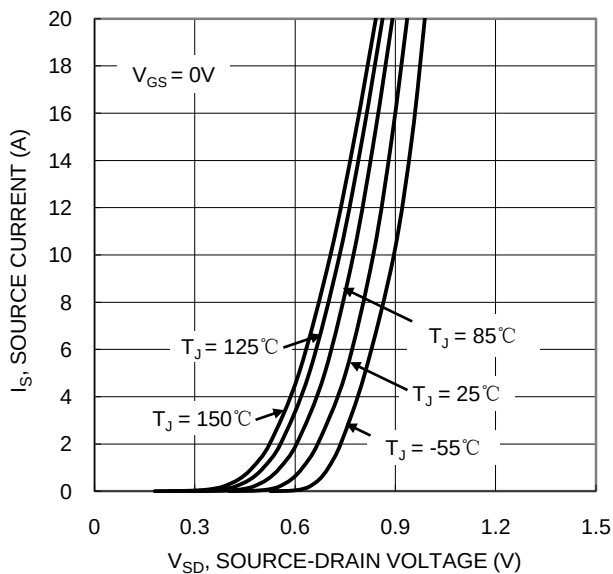
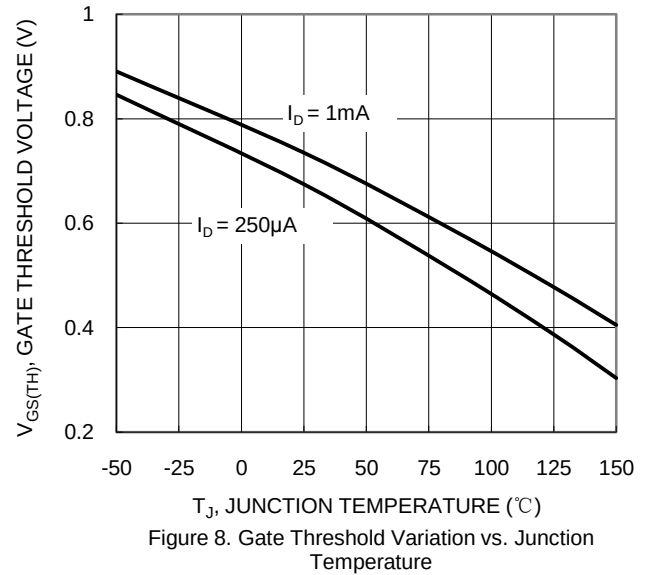
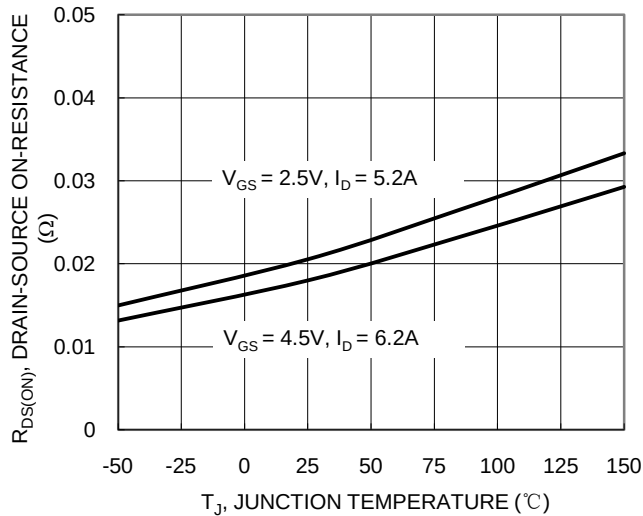


Figure 6. On-Resistance Variation with Junction Temperature



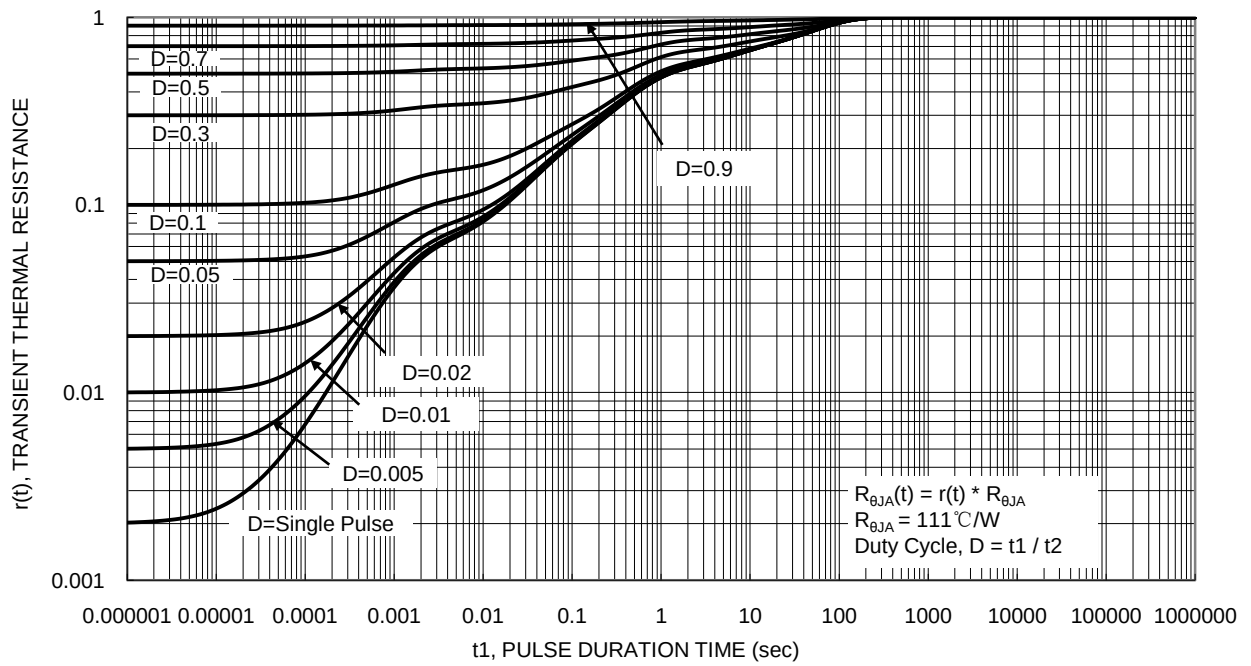
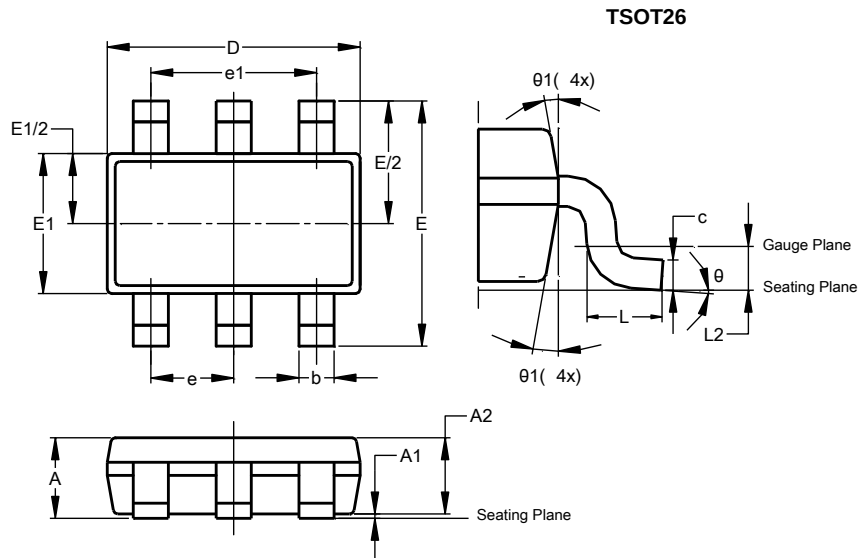


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

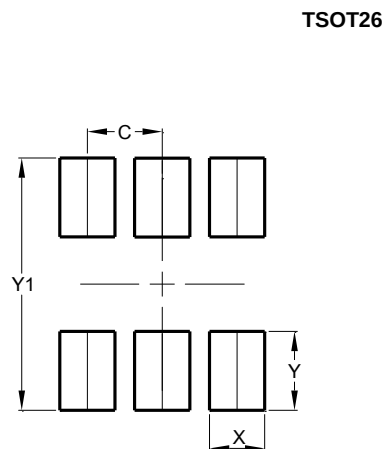
Please see <http://www.diodes.com/package-outlines.html> for the latest version.



TSOT26			
Dim	Min	Max	Typ
A	—	1.00	—
A1	0.010	0.100	—
A2	0.840	0.900	—
D	2.800	3.000	2.900
E	2.800 BSC		
E1	1.500	1.700	1.600
b	0.300	0.450	—
c	0.120	0.200	—
e	0.950 BSC		
e1	1.900 BSC		
L	0.30	0.50	—
L2	0.250 BSC		
θ	0°	8°	4°
θ1	4°	12°	—
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Dimensions	Value (in mm)
C	0.950
X	0.700
Y	1.000
Y1	3.199

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