RoHS

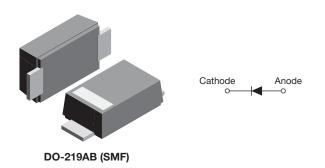
COMPLIANT HALOGEN

FREE



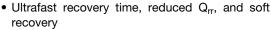
Vishay Semiconductors

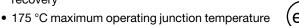
Ultrafast Rectifier, 1 A FRED Pt®

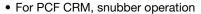


PRODUCT SUMMARY				
Package	DO-219AB (SMF)			
I _{F(AV)}	1 A			
V_{R}	600 V			
V _F at I _F	0.83 V			
t _{rr}	55 ns			
T _J max.	175 °C			
Diode variation	Single die			

FEATURES







· Low forward voltage drop

· Low leakage current

 Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

Meets JESD 201 class 2 whisker test

 Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop, ultrafast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in PFC, boost, lighting, in the AC/DC section of SMPS, freewheeling and clamp diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _C = 158 °C ⁽¹⁾	1	۸
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C, 6 ms square pulse	30	A
Operating junction and storage temperature range	T _J , T _{Stg}		-55 to +175	°C

Note

(1) Device on PCB with 8 mm x 16 mm soldering lands

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	600	-	-	
Forward voltage V _F		I _F = 1 A	-	1.0	1.2	V
	I _F = 1 A, T _J = 150 °C	-	0.83	1		
Reverse leakage current I _R	1	V _R = V _R rated	-	-	3	
	^I R	T _J = 150 °C, V _R = V _R rated	-	20	100	μΑ
Junction capacitance	C _T	V _R = 600 V	-	5	-	pF



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 A, dI_F/dt = 50 A$	/μs, V _R = 30 V	-	42	ı	
Reverse recovery time		I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	55	
neverse recovery time	t _{rr}	T _J = 25 °C	I _F = 1 A dI _F /dt = 500 A/μs V _R = 400 V	-	32	-	ns
		T _J = 125 °C		-	47	-	
Dook receivent ourrent	1	T _J = 25 °C		-	4.8	-	^
Peak recovery current	I _{RRM}	T _J = 125 °C		-	6.8	-	A
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	77	-	nC
	Q _{rr}	T _J = 125 °C		-	160	-	110

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	+175	°C
Thermal resistance, junction to case	R_{thJC}	Device mounted on PCB with 8 mm x 16 mm soldering lands	-	-	15	°C/W
Thermal resistance, junction to ambient	R _{thJA}	Device mounted on PCB with 2 mm x 3.5 mm soldering lands	-	-	130	°C/W
Approximate weight				0.015		g
Approximate weight				0.0005		oz.
Marking device		Case style DO-219AB (SMF)		MI	NU	

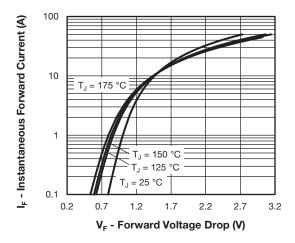


Fig. 1 - Typical Forward Voltage Drop Characteristics

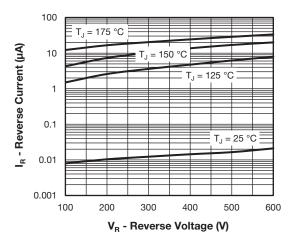


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

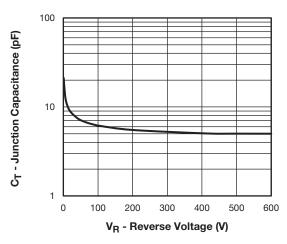


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

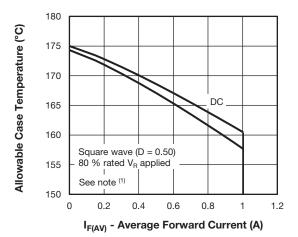


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

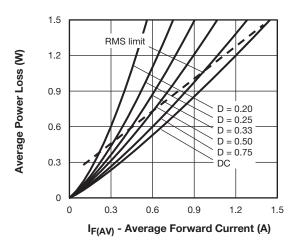


Fig. 5 - Forward Power Loss Characteristics

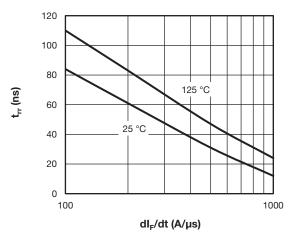


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

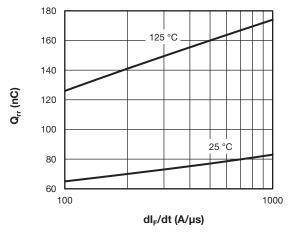
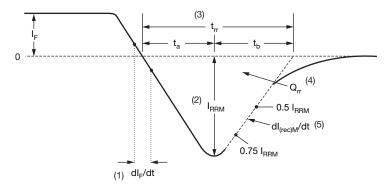


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 5)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D)}; I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

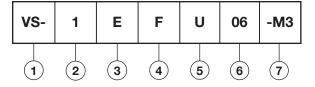
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) dl_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product
- Current rating (1 = 1 A)
- 3 Circuit configuration:

E = single diode

- 4 F = SMF package
- 5 Process type,

U = ultrafast recovery

Voltage code (06 = 600 V)

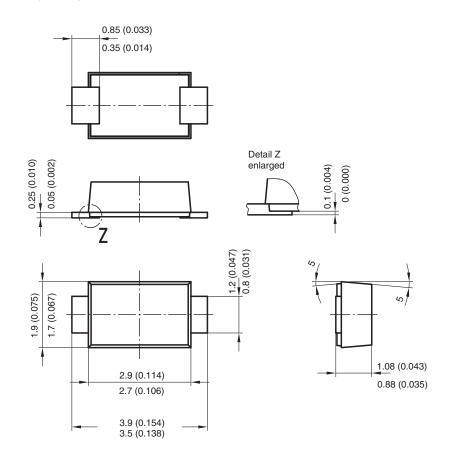
7 - -M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-1EFU06-M3/I	10 000	10 000	13"diameter plastic tape and reel			

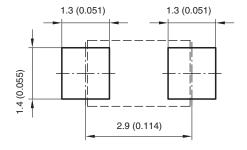
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95572			
Part marking information	www.vishay.com/doc?95618			
Packaging information	www.vishay.com/doc?95577			

DO-219AB (SMF)

DIMENSIONS in millimeters (inches)



Foot print recommendation:



Created - Date: 15. February 2005 Rev. 3 - Date: 13. March 2007 Document no.:S8-V-3915.01-001 (4)

17247



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Vishay

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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Revision: 02-Oct-12 Document Number: 91000