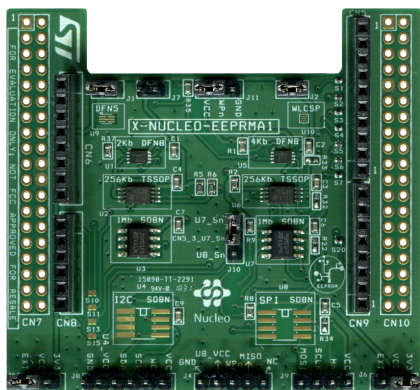


Standard I²C and SPI EEPROM memory expansion board based on M24xx and M95xx series for STM32 Nucleo



Features

- Easy portability across different MCU families
- Equipped with Arduino™ UNO R3 connector
- Compatible with [STM32 Nucleo](#) boards
- Free comprehensive development firmware library and sample implementation available when the [X-NUCLEO-EEPRMA1](#) expansion board is plugged on top of a [NUCLEO-F401RE](#) or [NUCLEO-L053R8](#) development board
- Developer can choose and solder an EEPROM to be tested using the evaluation software provided
- RoHS and WEEE compliant

Description

The [X-NUCLEO-EEPRMA1](#) expansion board is based on M24xx I²C and M95xx SPI EEPROM for data reading and writing.

The expansion board acts as an external storage device that can be used to store data such as manufacturing traceability, calibration, user setting, error flags, data log and monitoring data to make applications more flexible and accurate.

The X-NUCLEO-EEPRMA1 expansion board is compatible with the Arduino UNO R3 connector pin assignment and can be easily plugged to any [STM32 Nucleo](#) development board. You can mount the ST morpho connectors if required.

Product summary

Standard I ² C and SPI EEPROM memory expansion board based on M24xx and M95xx series for STM32 Nucleo	X-NUCLEO-EEPRMA1
STM32 Nucleo-64 development board with STM32F401RE MCU	NUCLEO-F401RE
STM32 Nucleo-64 development board with STM32L053R8 MCU	NUCLEO-L053R8

1 Schematic diagrams

Figure 1. X-NUCLEO-EEPRMA1 circuit schematic (1 of 4)

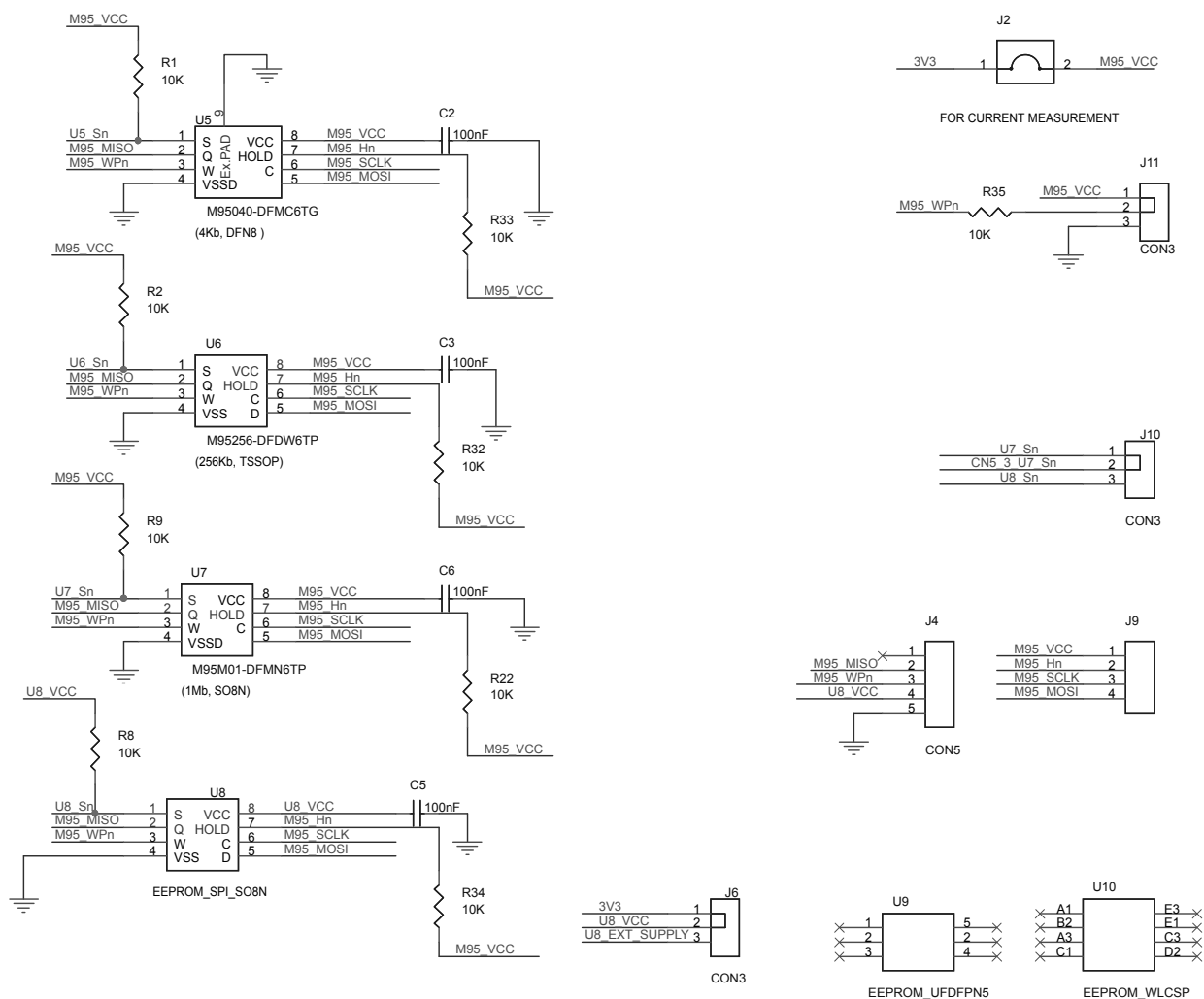


Figure 2. X-NUCLEO-EEPRMA1 circuit schematic (2 of 4)

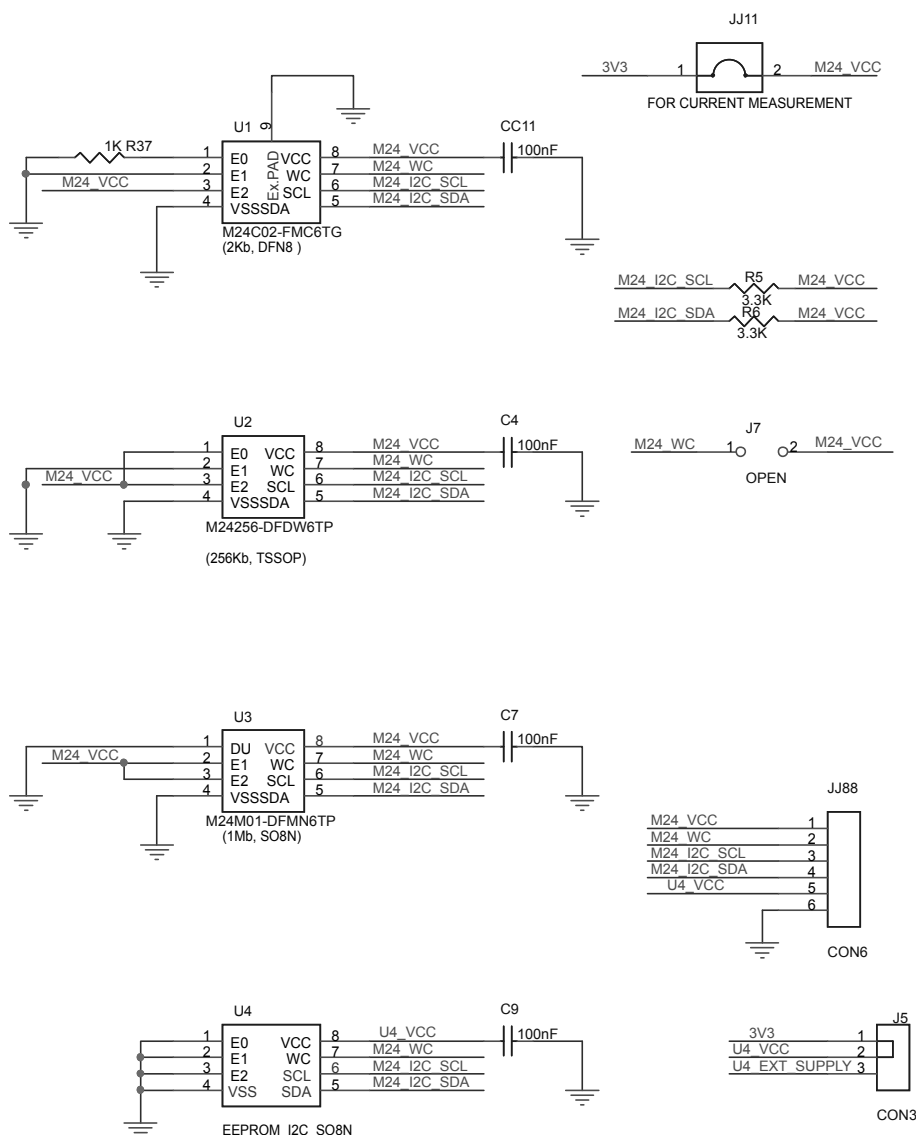
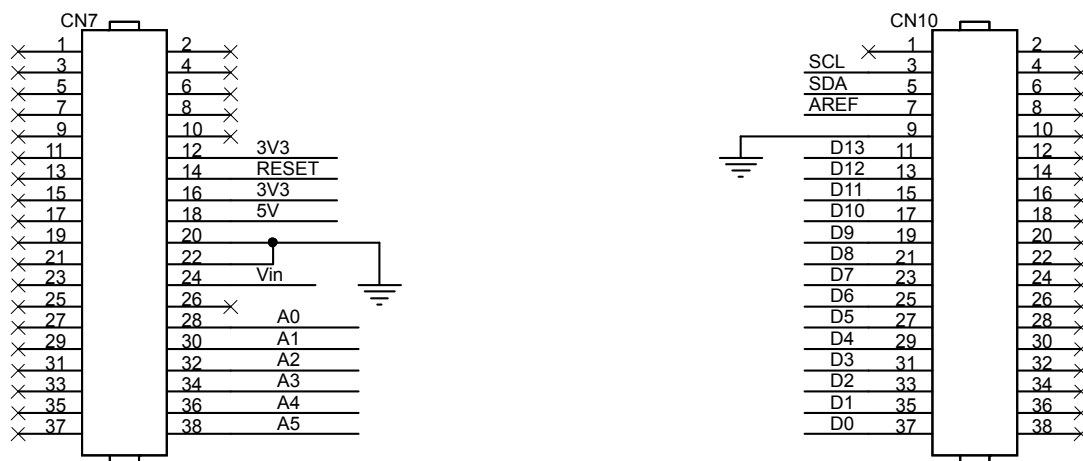


Figure 3. X-NUCLEO-EEPRMA1 circuit schematic (3 of 4)



CN6

NC	1	X
IOREF	2	X
RESET	3	RESET
3V3	4	3V3
5V	5	5V
GND	6	
GND	7	
VIN	8	Vin

CN5

M24 I2C SCL S1	CLOSED SCL 10
M24 I2C SDA S2	CLOSED SDA 9
	AREF 8
	7
M95 SCLK S3	CLOSED D13 6
M95 MISO S4	CLOSED D12 5
M95 MOSI S5	CLOSED D11 4
CN5 3 U7 Sn S6	CLOSED D10 3
U6 Sn S7	CLOSED D9 2
	D8 1

CN8

A0	1	A0 OPEN	S10 M95 WPn
A1	2	A1 OPEN	S11 M24 WC
A2	3	A2	
A3	4	A3 OPEN	S13 M95 Hn
A4	5	A4	
A5	6	A5 OPEN	S15 U8 Sn

CN9

	D7 8
	D6 7
	D5 6
	D4 5
	D3 4
U5 Sn S20	CLOSED D2 3
	D1 2
	D0 1

Revision history

Table 1. Document revision history

Date	Version	Changes
02-Oct-2018	1	Initial release.

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