

# **ADD-A-PAK Generation VII Power Modules Schottky Rectifier, 110 A**



PRODUCT SUMMARY			
I <sub>F(AV)</sub>	110 A		
$V_{R}$	30 V		
Package	ADD-A-PAK		
Circuit	Two diodes doubler circuit		

### **MECHANICAL DESCRIPTION**

The ADD-A-PAK generation VII, new generation of ADD-A-PAK module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

### **FEATURES**

- 150 °C T<sub>.1</sub> operation
- · Low forward voltage drop
- High frequency operation
- · Low thermal resistance
- UL approved file E78996
- · Designed and qualified for industrial level
- · Material categorization: For definitions of compliance please see www.vishav.com/doc?99912

### **BENEFITS**

- · Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- High surge capability
- Easy mounting on heatsink

#### **ELECTRICAL DESCRIPTION**

The VS-VSKDS220.. Schottky rectifier doubler has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature.

Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	110	А		
V <sub>RRM</sub>		30	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 µs sine	18 000	А		
V <sub>F</sub>	110 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.57	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-VSKDS220/030	UNITS	
Maximum DC reverse voltage	$V_{R}$	30	V	
Maximum working peak reverse voltage	$V_{RWM}$	] 30	V	



ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 110 °C, rectangular waveform		110	
Maximum peak one cycle	I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	18 000	Α
non-repetitive surge current		10 ms sine or 6 ms rect. pulse		2000	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 15 A, L = 1 mH		99	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		А	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V <sub>FM</sub>	110 A	T <sub>J</sub> = 25 °C	0.59	V
		220 A		0.78	
		110 A	T <sub>J</sub> = 125 °C	0.57	
		220 A		0.82	
	I <sub>RM</sub>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	10	A
Maximum reverse leakage current		T <sub>J</sub> = 125 °C		650	mA
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		7400	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs
Maximum RMS insulation voltage	V <sub>INS</sub>	50 Hz		3000 (1 min) 3600 (1 s)	V

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C
Maximum thermal resistance, junction to case per leg		R <sub>thJC</sub>	DC operation	0.52	°C/W
Typical thermal resistance, case to heatsink per module		R <sub>thCS</sub>		0.1	C/VV
Approximate weight				75	g
Approximate weight				2.7	OZ.
Mounting torque ± 10 %	to heatsink		A mounting compound is recommended and the torque should be rechecked after a period of 3 h to allow for the	4	Nm
	busbar		spread of the compound.	3	INIII
Case style			JEDEC®	TO-240AA co	mpatible

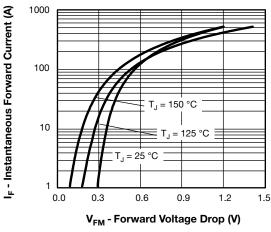


Fig. 1 - Maximum Forward Voltage Drop Characteristics

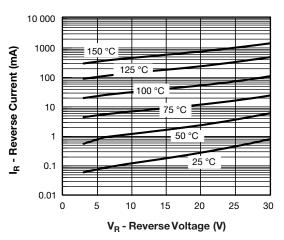


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

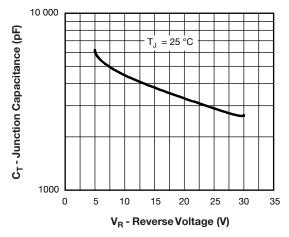


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

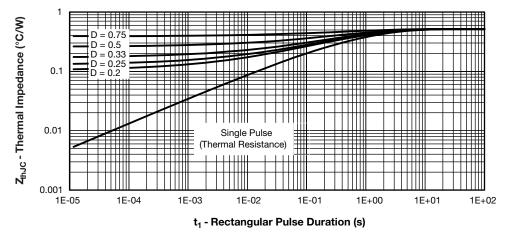


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

# www.vishay.com Vishay Semiconductors

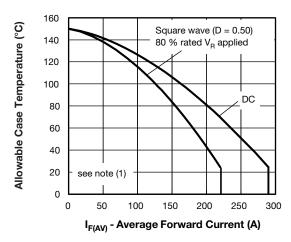


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

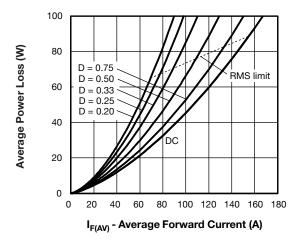


Fig. 6 - Forward Power Loss Characteristics

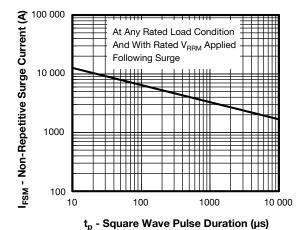


Fig. 7 - Maximum Non-Repetitive Surge Current

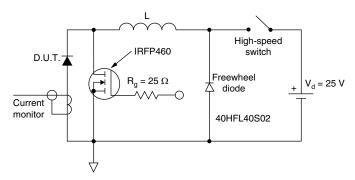
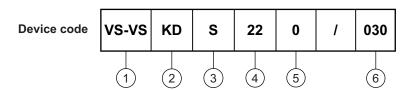


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

(1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$ 

### **ORDERING INFORMATION TABLE**



1 - VS-VS = Vishay Semiconductors product

2 - Circuit configuration:

KD = ADD-A-PAK - 2 diodes in series

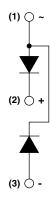
3 - S = Schottky diode

4 - Average rating (x 10)

Product silicon identification

6 - Voltage rating (030 = 30 V)

### **CIRCUIT CONFIGURATION**



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95369			



# **ADD-A-PAK Generation VII - Diode**

### **DIMENSIONS** in millimeters (inches)





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