CMOS Digital Integrated Circuits Silicon Monolithic

# 74VHC9541FT

## 1. Functional Description

- Octal Universal Schmitt Buffer with 3-State Outputs

## 2. General

The 74VHC9541FT is an ultra-high-speed octal Schmitt buffer fabricated using silicon-gate CMOS technology. The 74VHC9541FT combines low power consumption of CMOS with Schottky TTL speeds.

The outputs can be put in the high-impedance state by placing a logic HIGH on the Enable ($\overline{G}$) input. The CONT input determines the logical inversion of data. A logic LOW on the CONT input configures the 74VHC9541FT as an inverter; a logic HIGH on the CONT input configures the 74VHC9541FT as a buffer.

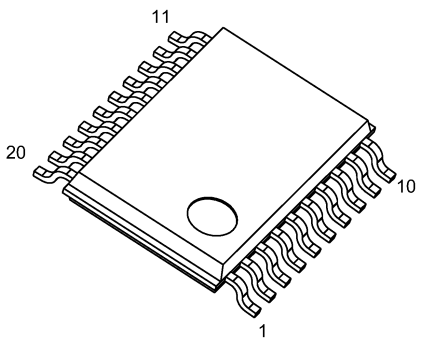
All the inputs have hysteresis between the positive-going and negative-going thresholds. Thus the 74VHC9541FT is capable of squaring up transitions of slowly changing input signals and provides an improved noise immunity. Additionally, all the inputs have a newly developed protection circuit without a diode returned to $V_{CC}$. This enables the inputs to be tolerant of up to 5 volts even when power supply is down. The input power-down protection capability makes the 74VHC9541FT ideal for a wide range of applications, such as interfacing between different voltages, voltage translation from 5 V to 3 V and battery back-up circuits.

## 3. Features

(1) AEC-Q100 (Rev. H) (Note 1)
(2) Wide operating temperature range: $T_{opr}$ = -40 to 125 °C
(3) High speed: tpd = 5.0 ns (typ.) at $V_{CC}$ = 5.0 V
(4) Low supply current: $I_{CC}$ = 4.0 μA (max) ($T_a$ = 25 °C)
(5) All inputs are provided with power-down protection.
(6) Symmetrical rise and fall delays: $t_{PLH} \approx t_{PHL}$
(7) Wide operating voltage range: $V_{CC(opr)}$ = 2.0 V to 5.5 V

Note 1: This device is compliant with the reliability requirements of AEC-Q100. For details, contact your Toshiba sales representative.

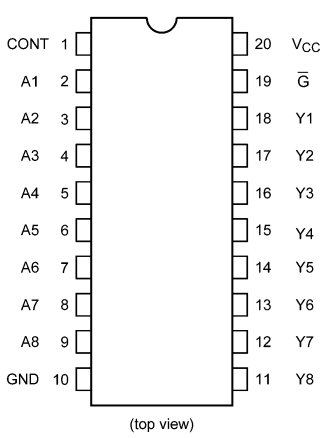
## 4. Packaging

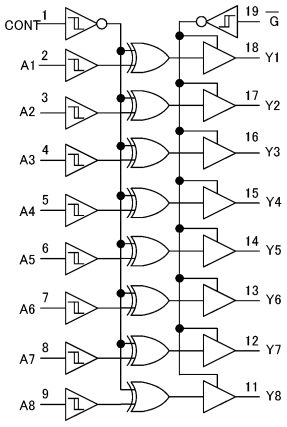



TSSOP20B

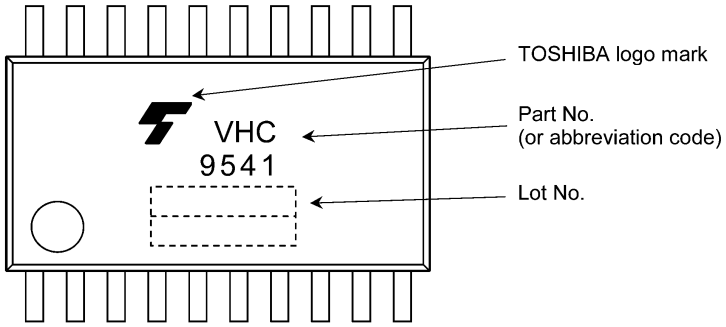
Start of commercial production
2014-04

## 5. Pin Assignment




## 6. Marking




## 7. Truth Table

| Input $\overline{G}$ | Input CONT | Input An | Output Yn |
|---|---|---|---|
| H | X | X | Z |
| L | L | L | H |
| L | L | H | L |
| L | H | L | L |
| L | H | H | H |

X: Don't care
Z: High impedance

## 8. Absolute Maximum Ratings (Note)

| Characteristics | Symbol | Note | Rating | Unit |
|---|---|---|---|---|
| Supply voltage | $V_{CC}$ | | -0.5 to 7.0 | V |
| Input voltage | $V_{IN}$ | | -0.5 to 7.0 | V |
| Output voltage | $V_{OUT}$ | | -0.5 to $V_{CC}$ + 0.5 | V |
| Input diode current | $I_{IK}$ | | -20 | mA |
| Output diode current | $I_{OK}$ | | ±20 | mA |
| Output current | $I_{OUT}$ | | ±25 | mA |
| $V_{CC}$/ground current | $I_{CC}$ | | ±75 | mA |
| Power dissipation | $P_D$ | (Note 1) | 180 | mW |
| Storage temperature | $T_{stg}$ | | -65 to 150 | °C |

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: 180 mW in the range of $T_a$ = -40 to 85 °C. From $T_a$ = 85 to 125 °C a derating factor of -3.25 mW/°C shall be applied until 50 mW.

## 9. Operating Ranges (Note)

| Characteristics | Symbol | Rating | Unit |
|---|---|---|---|
| Supply voltage | $V_{CC}$ | 2.0 to 5.5 | V |
| Input voltage | $V_{IN}$ | 0 to 5.5 | V |
| Output voltage | $V_{OUT}$ | 0 to $V_{CC}$ | V |
| Operating temperature | $T_{opr}$ | -40 to 125 | °C |

Note: The operating ranges must be maintained to ensure the normal operation of the device.
Unused inputs must be tied to either $V_{CC}$ or GND.

## 10. Electrical Characteristics

### 10.1. DC Characteristics (Unless otherwise specified, $T_a$ = 25 °C)

| Characteristics | Symbol | Test Condition | | $V_{CC}$ (V) | Min | Typ. | Max | Unit |
|---|---|---|---|---|---|---|---|---|
| Positive threshold voltage | $V_P$ | — | | 3.0 | — | — | 2.20 | V |
| | | | | 4.5 | — | — | 3.15 | |
| | | | | 5.5 | — | — | 3.85 | |
| Negative threshold voltage | $V_N$ | — | | 3.0 | 0.90 | — | — | V |
| | | | | 4.5 | 1.35 | — | — | |
| | | | | 5.5 | 1.65 | — | — | |
| Hysteresis voltage | $V_H$ | — | | 3.0 | 0.30 | — | 1.20 | V |
| | | | | 4.5 | 0.40 | — | 1.40 | |
| | | | | 5.5 | 0.50 | — | 1.60 | |
| High-level output voltage | $V_{OH}$ | $V_{IN}$ = $V_{IH}$ or $V_{IL}$ | $I_{OH}$ = -50 μA | 2.0 | 1.9 | 2.0 | — | V |
| | | | | 3.0 | 2.9 | 3.0 | — | |
| | | | | 4.5 | 4.4 | 4.5 | — | |
| | | | $I_{OH}$ = -4 mA | 3.0 | 2.58 | — | — | |
| | | | $I_{OH}$ = -8 mA | 4.5 | 3.94 | — | — | |
| Low-level output voltage | $V_{OL}$ | $V_{IN}$ = $V_{IH}$ or $V_{IL}$ | $I_{OL}$ = 50 μA | 2.0 | — | 0.0 | 0.1 | V |
| | | | | 3.0 | — | 0.0 | 0.1 | |
| | | | | 4.5 | — | 0.0 | 0.1 | |
| | | | $I_{OL}$ = 4 mA | 3.0 | — | — | 0.36 | |
| | | | $I_{OL}$ = 8 mA | 4.5 | — | — | 0.36 | |
| 3-state output OFF-state leakage current | $I_{OZ}$ | $V_{IN}$ = $V_{IH}$ or $V_{IL}$<br>$V_{OUT}$ = $V_{CC}$ or GND | | 5.5 | — | — | ±0.25 | μA |
| Input leakage current | $I_{IN}$ | $V_{IN}$ = 5.5 V or GND | | 0 to 5.5 | — | — | ±0.1 | μA |
| Quiescent supply current | $I_{CC}$ | $V_{IN}$ = $V_{CC}$ or GND | | 5.5 | — | — | 4.0 | μA |

## 10.2. DC Characteristics (Unless otherwise specified, $T_a$ = -40 to 85 °C)

| Characteristics | Symbol | Test Condition | | $V_{CC}$ (V) | Min | Max | Unit |
|---|---|---|---|---|---|---|---|
| Positive threshold voltage | $V_P$ | — | | 3.0 | — | 2.20 | V |
| | | | | 4.5 | — | 3.15 | |
| | | | | 5.5 | — | 3.85 | |
| Negative threshold voltage | $V_N$ | — | | 3.0 | 0.90 | — | V |
| | | | | 4.5 | 1.35 | — | |
| | | | | 5.5 | 1.65 | — | |
| Hysteresis voltage | $V_H$ | — | | 3.0 | 0.30 | 1.20 | V |
| | | | | 4.5 | 0.40 | 1.40 | |
| | | | | 5.5 | 0.50 | 1.60 | |
| High-level output voltage | $V_{OH}$ | $V_{IN}$ = $V_{IH}$ or $V_{IL}$ | $I_{OH}$ = -50 μA | 2.0 | 1.9 | — | V |
| | | | | 3.0 | 2.9 | — | |
| | | | | 4.5 | 4.4 | — | |
| | | | $I_{OH}$ = -4 mA | 3.0 | 2.48 | — | |
| | | | $I_{OH}$ = -8 mA | 4.5 | 3.80 | — | |
| Low-level output voltage | $V_{OL}$ | $V_{IN}$ = $V_{IH}$ or $V_{IL}$ | $I_{OL}$ = 50 μA | 2.0 | — | 0.1 | V |
| | | | | 3.0 | — | 0.1 | |
| | | | | 4.5 | — | 0.1 | |
| | | | $I_{OL}$ = 4 mA | 3.0 | — | 0.44 | |
| | | | $I_{OL}$ = 8 mA | 4.5 | — | 0.44 | |
| 3-state output OFF-state leakage current | $I_{OZ}$ | $V_{IN}$ = $V_{IH}$ or $V_{IL}$<br>$V_{OUT}$ = $V_{CC}$ or GND | | 5.5 | — | ±2.50 | μA |
| Input leakage current | $I_{IN}$ | $V_{IN}$ = 5.5 V or GND | | 0 to 5.5 | — | ±1.0 | μA |
| Quiescent supply current | $I_{CC}$ | $V_{IN}$ = $V_{CC}$ or GND | | 5.5 | — | 40.0 | μA |

## 10.3. DC Characteristics (Unless otherwise specified, $T_a$ = -40 to 125 °C)

| Characteristics | Symbol | Test Condition | | $V_{CC}$ (V) | Min | Max | Unit |
|---|---|---|---|---|---|---|---|
| Positive threshold voltage | $V_P$ | — | | 3.0 | — | 2.20 | V |
| | | | | 4.5 | — | 3.15 | |
| | | | | 5.5 | — | 3.85 | |
| Negative threshold voltage | $V_N$ | — | | 3.0 | 0.90 | — | V |
| | | | | 4.5 | 1.35 | — | |
| | | | | 5.5 | 1.65 | — | |
| Hysteresis voltage | $V_H$ | — | | 3.0 | 0.30 | 1.20 | V |
| | | | | 4.5 | 0.40 | 1.40 | |
| | | | | 5.5 | 0.50 | 1.60 | |
| High-level output voltage | $V_{OH}$ | $V_{IN}$ = $V_{IH}$ or $V_{IL}$ | $I_{OH}$ = -50 μA | 2.0 | 1.9 | — | V |
| | | | | 3.0 | 2.9 | — | |
| | | | | 4.5 | 4.4 | — | |
| | | | $I_{OH}$ = -4 mA | 3.0 | 2.40 | — | |
| | | | $I_{OH}$ = -8 mA | 4.5 | 3.70 | — | |
| Low-level output voltage | $V_{OL}$ | $V_{IN}$ = $V_{IH}$ or $V_{IL}$ | $I_{OL}$ = 50 μA | 2.0 | — | 0.1 | V |
| | | | | 3.0 | — | 0.1 | |
| | | | | 4.5 | — | 0.1 | |
| | | | $I_{OL}$ = 4 mA | 3.0 | — | 0.55 | |
| | | | $I_{OL}$ = 8 mA | 4.5 | — | 0.55 | |
| 3-state output OFF-state leakage current | $I_{OZ}$ | $V_{IN}$ = $V_{IH}$ or $V_{IL}$<br>$V_{OUT}$ = $V_{CC}$ or GND | | 5.5 | — | ±10.0 | μA |
| Input leakage current | $I_{IN}$ | $V_{IN}$ = 5.5 V or GND | | 0 to 5.5 | — | ±2.0 | μA |
| Quiescent supply current | $I_{CC}$ | $V_{IN}$ = $V_{CC}$ or GND | | 5.5 | — | 80.0 | μA |

## 10.4. AC Characteristics (Unless otherwise specified, $T_a$ = 25 °C, Input: $t_r = t_f$ = 3 ns)

| Characteristics | Symbol | Note | Test Condition | $V_{CC}$ (V) | $C_L$ (pF) | Min | Typ. | Max | Unit |
|---|---|---|---|---|---|---|---|---|---|
| Propagation delay time (An - Yn) | $t_{PLH}$,$t_{PHL}$ | | — | 3.3 ± 0.3 | 15 | — | 6.0 | 8.0 | ns |
| | | | | | 50 | — | 9.0 | 12.5 | |
| | | | | 5.0 ± 0.5 | 15 | — | 5.0 | 5.5 | |
| | | | | | 50 | — | 7.0 | 8.5 | |
| Propagation delay time (CONT - Yn) | $t_{PLH}$,$t_{PHL}$ | | — | 3.3 ± 0.3 | 15 | — | 8.5 | 11.5 | ns |
| | | | | | 50 | — | 13.0 | 17.0 | |
| | | | | 5.0 ± 0.5 | 15 | — | 6.5 | 8.0 | |
| | | | | | 50 | — | 10.5 | 12.5 | |
| 3-state output enable time | $t_{PZL}$,$t_{PZH}$ | | $R_L$ = 1 kΩ | 3.3 ± 0.3 | 15 | — | 6.0 | 8.0 | ns |
| | | | | | 50 | — | 10.5 | 13.5 | |
| | | | | 5.0 ± 0.5 | 15 | — | 4.5 | 5.5 | |
| | | | | | 50 | — | 9.0 | 10.5 | |
| 3-state output disable time | $t_{PLZ}$,$t_{PHZ}$ | | $R_L$ = 1 kΩ | 3.3 ± 0.3 | 50 | — | 12.5 | 13.5 | ns |
| | | | | 5.0 ± 0.5 | 50 | — | 9.0 | 9.5 | |
| Output skew | $t_{osLH}$,$t_{osHL}$ | (Note 1) | — | 3.3 ± 0.3 | 50 | — | — | 1.5 | ns |
| | | | | 5.0 ± 0.5 | 50 | — | — | 1.0 | |
| Input capacitance | $C_{IN}$ | | — | | | — | 4 | 10 | pF |
| Output capacitance | $C_{OUT}$ | | — | | | — | 6 | — | pF |
| Power dissipation capacitance | $C_{PD}$ | (Note 2) | $f_{IN}$ = 1 MHz | | | — | 11 | — | pF |

Note 1: Parameter guaranteed by design. ($t_{osLH} = |t_{PLH}m - t_{PLH}n|$, $t_{osHL} = |t_{PHL}m - t_{PHL}n|$)

Note 2: $C_{PD}$ is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.

$I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}/8$ (per bit)

## 10.5. AC Characteristics (Unless otherwise specified, $T_a$ = -40 to 85 °C, Input: $t_r = t_f$ = 3 ns)

| Characteristics | Symbol | Note | Test Condition | $V_{CC}$ (V) | $C_L$ (pF) | Min | Max | Unit |
|---|---|---|---|---|---|---|---|---|
| Propagation delay time (An - Yn) | $t_{PLH}$,$t_{PHL}$ | | — | 3.3 ± 0.3 | 15 | 1.0 | 10.0 | ns |
| | | | | | 50 | 1.0 | 15.0 | |
| | | | | 5.0 ± 0.5 | 15 | 1.0 | 7.0 | |
| | | | | | 50 | 1.0 | 10.0 | |
| Propagation delay time (CONT - Yn) | $t_{PLH}$,$t_{PHL}$ | | — | 3.3 ± 0.3 | 15 | 1.0 | 13.5 | ns |
| | | | | | 50 | 1.0 | 20.5 | |
| | | | | 5.0 ± 0.5 | 15 | 1.0 | 9.5 | |
| | | | | | 50 | 1.0 | 15.0 | |
| 3-state output enable time | $t_{PZL}$,$t_{PZH}$ | | $R_L$ = 1 kΩ | 3.3 ± 0.3 | 15 | 1.0 | 9.5 | ns |
| | | | | | 50 | 1.0 | 16.5 | |
| | | | | 5.0 ± 0.5 | 15 | 1.0 | 6.5 | |
| | | | | | 50 | 1.0 | 12.5 | |
| 3-state output disable time | $t_{PLZ}$,$t_{PHZ}$ | | $R_L$ = 1 kΩ | 3.3 ± 0.3 | 50 | 1.0 | 16.0 | ns |
| | | | | 5.0 ± 0.5 | 50 | 1.0 | 11.0 | |
| Output skew | $t_{osLH}$,$t_{osHL}$ | (Note 1) | — | 3.3 ± 0.3 | 50 | — | 1.5 | ns |
| | | | | 5.0 ± 0.5 | 50 | — | 1.0 | |
| Input capacitance | $C_{IN}$ | | — | | | — | 10 | pF |

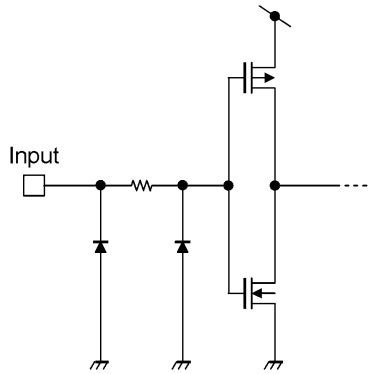
Note 1: Parameter guaranteed by design. ($t_{osLH} = |t_{PLH}m - t_{PLH}n|$, $t_{osHL} = |t_{PHL}m - t_{PHL}n|$)

### 10.6. AC Characteristics
**(Unless otherwise specified, $T_a$ = -40 to 125 °C, Input: $t_r = t_f$ = 3 ns)**

| Characteristics | Symbol | Note | Test Condition | $V_{CC}$ (V) | $C_L$ (pF) | Min | Max | Unit |
|---|---|---|---|---|---|---|---|---|
| Propagation delay time (An - Yn) | $t_{PLH}$,$t_{PHL}$ | | — | 3.3 ± 0.3 | 15 | 1.0 | 11.5 | ns |
| | | | | | 50 | 1.0 | 17.0 | |
| | | | | 5.0 ± 0.5 | 15 | 1.0 | 8.0 | |
| | | | | | 50 | 1.0 | 11.0 | |
| Propagation delay time (CONT - Yn) | $t_{PLH}$,$t_{PHL}$ | | — | 3.3 ± 0.3 | 15 | 1.0 | 15.0 | ns |
| | | | | | 50 | 1.0 | 23.0 | |
| | | | | 5.0 ± 0.5 | 15 | 1.0 | 10.5 | |
| | | | | | 50 | 1.0 | 17.0 | |
| 3-state output enable time | $t_{PZL}$,$t_{PZH}$ | | $R_L$ = 1 kΩ | 3.3 ± 0.3 | 15 | 1.0 | 10.5 | ns |
| | | | | | 50 | 1.0 | 18.5 | |
| | | | | 5.0 ± 0.5 | 15 | 1.0 | 7.5 | |
| | | | | | 50 | 1.0 | 14.0 | |
| 3-state output disable time | $t_{PLZ}$,$t_{PHZ}$ | | $R_L$ = 1 kΩ | 3.3 ± 0.3 | 50 | 1.0 | 18.0 | ns |
| | | | | 5.0 ± 0.5 | 50 | 1.0 | 12.0 | |
| Output skew | $t_{osLH}$,$t_{osHL}$ | (Note 1) | — | 3.3 ± 0.3 | 50 | — | 1.5 | ns |
| | | | | 5.0 ± 0.5 | 50 | — | 1.0 | |
| Input capacitance | $C_{IN}$ | | — | | | — | 10 | pF |

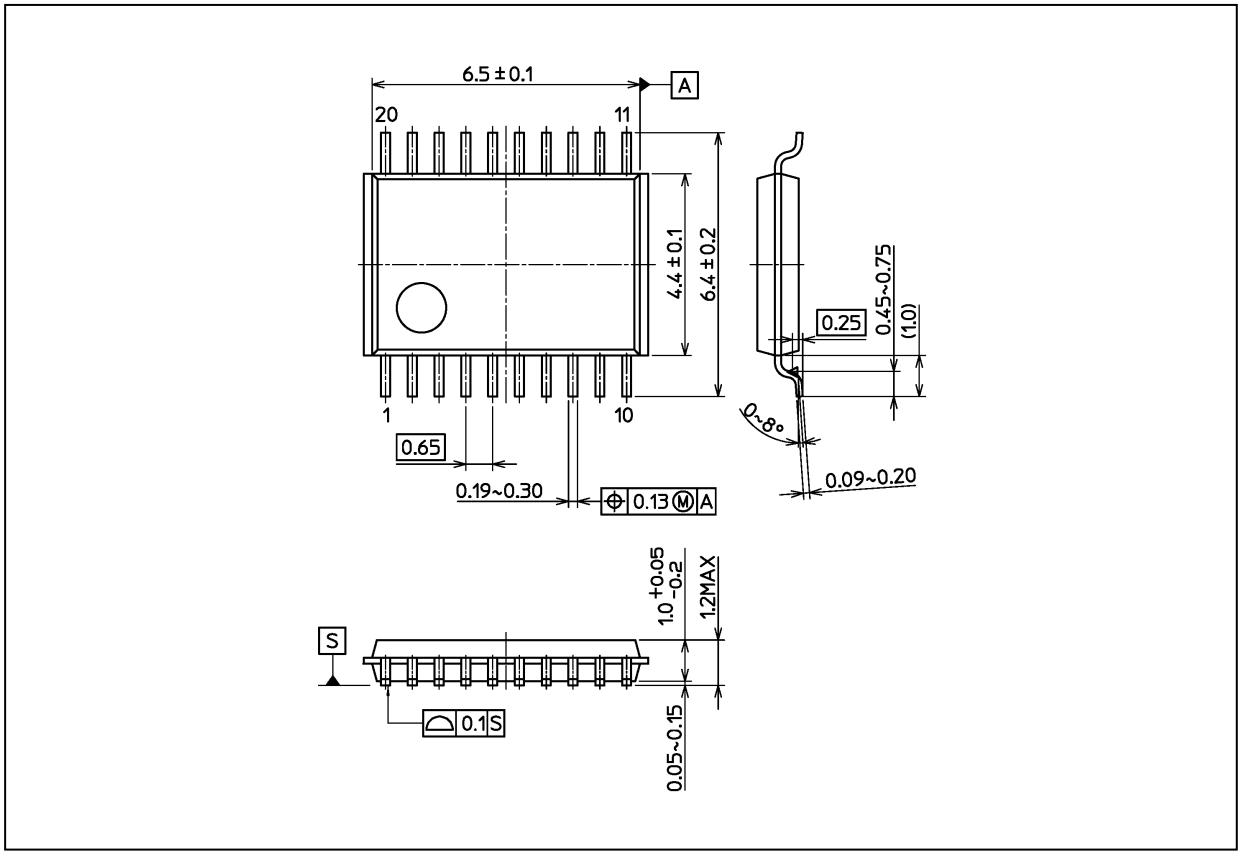
Note 1: Parameter guaranteed by design. ($t_{osLH}$ = $|t_{PLH}m-t_{PLH}n|$, $t_{osHL}$ = $|t_{PHL}m-t_{PHL}n|$)

## 11. Internal Equivalent Circuit



## Package Dimensions

Unit: mm




Weight: 0.071 g (typ.)

| Package Name(s) |
| --- |
| Nickname: TSSOP20B |

## RESTRICTIONS ON PRODUCT USE

Toshiba Corporation and its subsidiaries and affiliates are collectively referred to as "TOSHIBA".
Hardware, software and systems described in this document are collectively referred to as "Product".

- TOSHIBA reserves the right to make changes to the information in this document and related Product without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. **TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.**
- **PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH MAY CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT ("UNINTENDED USE").** Except for specific applications as expressly stated in this document, Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment, equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions, safety devices, elevators and escalators, devices related to electric power, and equipment used in finance-related fields. **IF YOU USE PRODUCT FOR UNINTENDED USE, TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT.** For details, please contact your TOSHIBA sales representative.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- **ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.**
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. **TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES OCCURRING AS A RESULT OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.**

# Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Toshiba:
74VHC9541FT