

SBR40U200CTB 40A SBR[®] SUPER BARRIER RECTIFIER

Features

- Ultra Low Forward Voltage Drop
- Low Leakage Current
- Excellent High Temperature Stability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- Lead Free Finish, RoHS Compliant (Note 1)
- Also Available in Green Molding Compound (Note 2)

Mechanical Data

- Case: D²Pak
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 1.6 grams (approximate)



Top View

Common 3 Cathode Anode Anode Package Pin Out Configuration

Ordering Information (Notes 2 & 3)

Part Number	Case	Packaging
SBR40U200CTB	D ² PAK	50 Pieces/Tube
SBR40U200CTB-G	D ² PAK	50 Pieces/Tube
SBR40U200CTB-13	D ² PAK	800/Tape & Reel
SBR40U200CTB-13-G	D ² PAK	800/Tape & Reel

Notes:

1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes

2. For Green Molding Compound version part numbers, add "-G" suffix to part number above. Examples: SBR40U200CTB-G.

3. For packaging details, go to our website at http://www.diodes.com.

Marking Information



SBR40U200CTB = Product Type Marking Code AB = Foundry and Assembly Code (if applicable) YYWW = Date Code Marking YY = Year (ex: U = 2007) WW = Week (01 - 53)



Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _{RM}	200	V
Average Rectified Output Current @ T _C = 150°C	lo	40	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	240	A

Thermal Characteristics

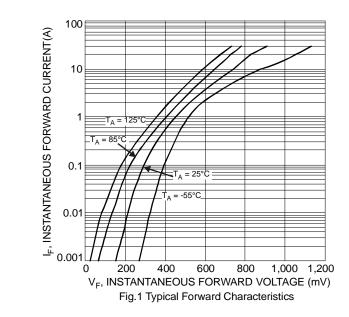
Characteristic	Symbol	Value	Unit
Typical Thermal Resistance (per leg) Thermal Resistance Junction to Case (Note 4) Thermal Resistance, Junction to Ambient (Note 4)	R _{ejc} R _{eja}	2 7	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +175	°C

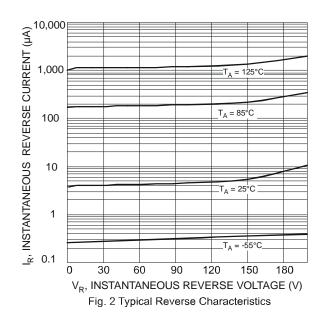
Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop (per leg)	VF	-	0.85 0.70	0.93 0.75	V	I _F = 20A, T _J = 25°C I _F = 20A, T _J = 125°C
Leakage Current (Note 5)	I _R	-		0.2 40	mA	V _R = 200V, T _J = 25°C V _R = 200V, T _J = 125°C
Reverse Recovery Time		-	38	50	ns	I _F = 0.5A, I _R = 1A, I _{RR} = 0.25A
	trr	-	25	35		I _F = 1A, V _R = 30V di/dt = 100A/μs, T _J = 25°C

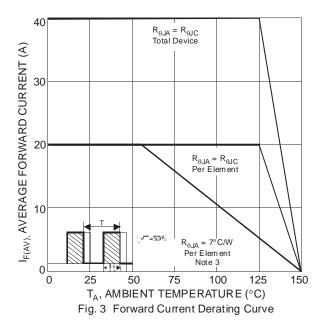
Notes: 4. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com.

5. Short duration pulse test used to minimize self-heating effect.

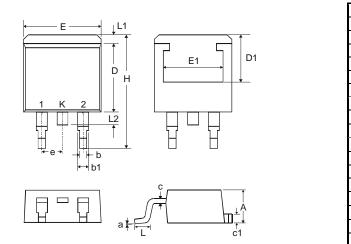






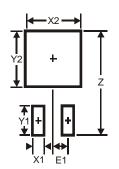


Package Outline Dimensions



D ² PAK			
Dim	Min	Max	
Α	4.07	4.82	
b	0.51	0.99	
b1	1.15	1.77	
С	0.356	0.58	
c1	1.143	1.65	
D	8.39	9.65	
D1	6.55	_	
Е	9.66	10.66	
E1	6.23		
е	2.54 Тур		
Н	14.61	15.87	
L	1.78	2.79	
L1	_	1.67	
L2	_	1.77	
а	0°	8°	
All Dim	All Dimensions in mm		

Suggested Pad Layout



Dimensions	Value (in mm)
Z	16.9
X1	1.1
X2	10.8
Y1	3.5
Y2	7.01
E1	2.5



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