

# **HAT2172H**

# Silicon N Channel Power MOS FET Power Switching

REJ03G0132-0500 Rev.5.00 Sep 20, 2005

### **Features**

- High speed switching
- Capable of 7 V gate drive
- Low drive current
- High density mounting
- Low on-resistance  $R_{DS(on)} = 5.8 \ m\Omega \ typ. \ (at \ V_{GS} = 10 \ V)$

### **Outline**

RENESAS Package code: PTZZ0005DA-A)
(Package name: LFPAK )

5
D
4
G
1, 2, 3 Source
4 Gate
5 Drain

# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	40	V
Gate to source voltage	$V_{GSS}$	±20	V
Drain current	I <sub>D</sub>	30	А
Drain peak current	I <sub>D(pulse)</sub> Note1	120	А
Body-drain diode reverse drain current	I <sub>DR</sub>	30	А
Avalanche current	I <sub>AP</sub> Note 2	20	А
Avalanche energy	E <sub>AR</sub> Note 2	32	mJ
Channel dissipation	Pch Note3	20	W
Channel to Case Thermal Resistance	θch-C	6.25	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

- 2. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$
- 3.  $Tc = 25^{\circ}C$

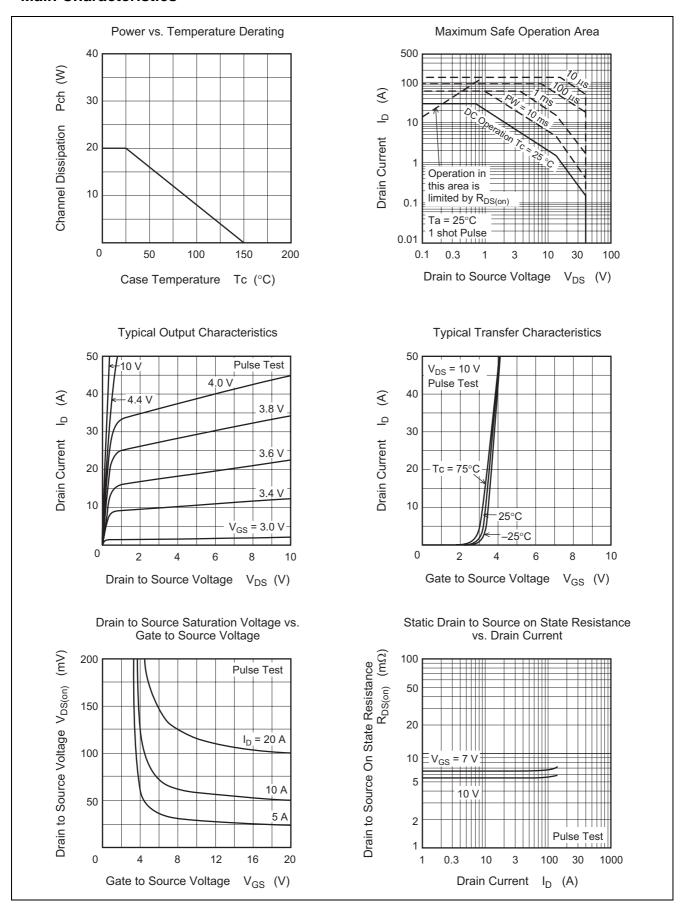
# **Electrical Characteristics**

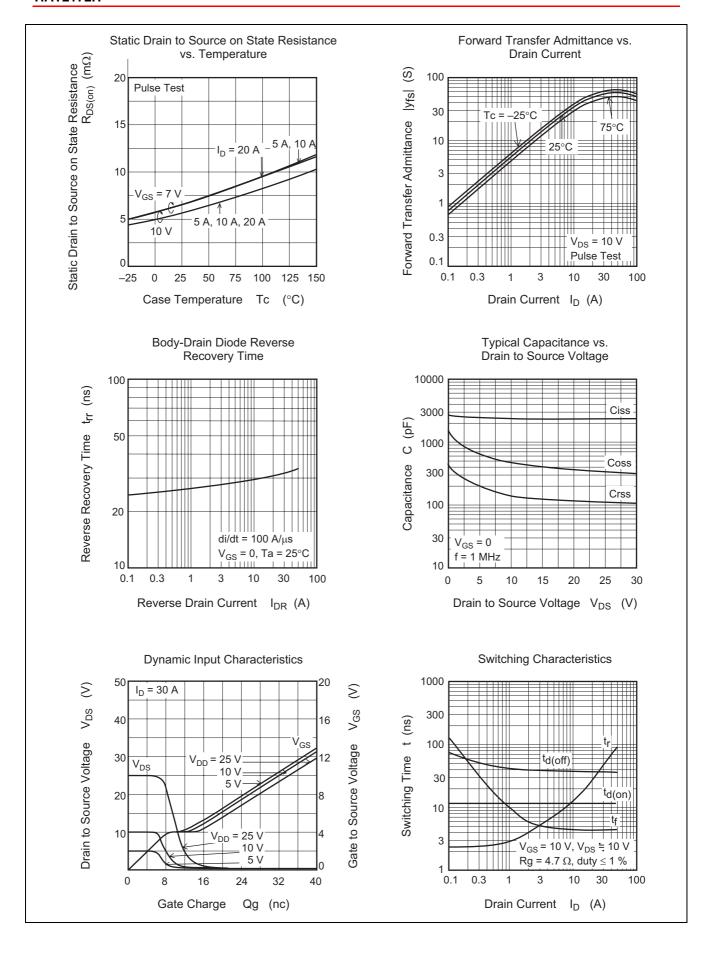
 $(Ta = 25^{\circ}C)$ 

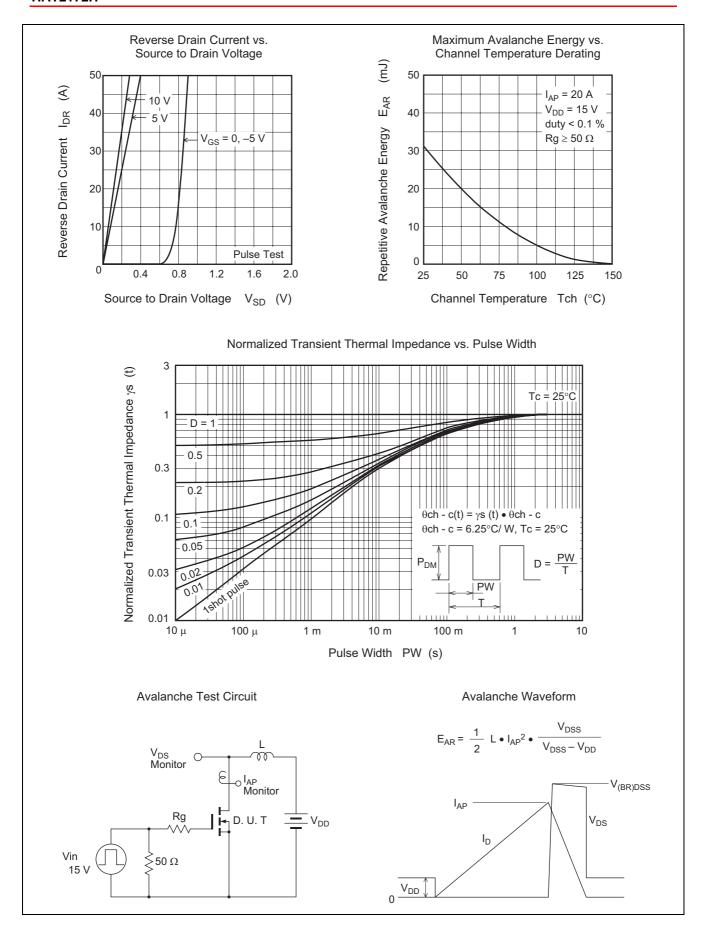
Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Drain to source breakdown voltage	$V_{(BR)DSS}$	40	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$	
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$	
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 40 \text{ V}, V_{GS} = 0$	
Gate to source cutoff voltage	$V_{GS(off)}$	1.5		3.0	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	
Static drain to source on state	R <sub>DS(on)</sub>	_	5.8	7.5	$m\Omega$	$I_D = 15 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$	
resistance	R <sub>DS(on)</sub>	_	6.6	9.2	mΩ	$I_D = 15 \text{ A}, V_{GS} = 7 \text{ V}^{\text{Note4}}$	
Forward transfer admittance	y <sub>fs</sub>	27	45		S	$I_D = 15 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$	
Input capacitance	Ciss	_	2420		pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$	
Output capacitance	Coss	_	480	_	pF	f = 1 MHz	
Reverse transfer capacitance	Crss	_	150	_	pF		
Gate Resistance	Rg	_	0.5	_	Ω		
Total gate charge	Qg	_	32	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 10 \text{ V},$	
Gate to source charge	Qgs	_	9	_	nC	I <sub>D</sub> = 30 A	
Gate to drain charge	Qgd	_	4.0	_	nC		
Turn-on delay time	t <sub>d(on)</sub>	_	12	_	ns	$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A},$	
Rise time	t <sub>r</sub>	_	20	_	ns	$V_{DD} \cong 10 \text{ V}, \text{ R}_{L} = 0.67 \Omega,$	
Turn-off delay time	t <sub>d(off)</sub>	_	38	_	ns	$Rg = 4.7 \Omega$	
Fall time	t <sub>f</sub>	_	4.5	_	ns		
Body-drain diode forward voltage	$V_{DF}$	_	0.84	1.10	V	IF = 30 A, V <sub>GS</sub> = 0 <sup>Note4</sup>	
Body-drain diode reverse recovery	t <sub>rr</sub>	_	32	_	ns	IF = 30 A, V <sub>GS</sub> = 0	
time						$di_F/dt = 100 A/ \mu s$	

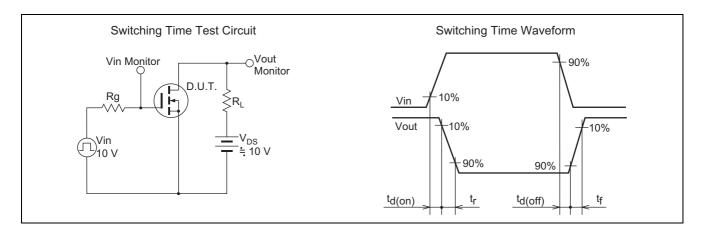
Notes: 4. Pulse test

### **Main Characteristics**

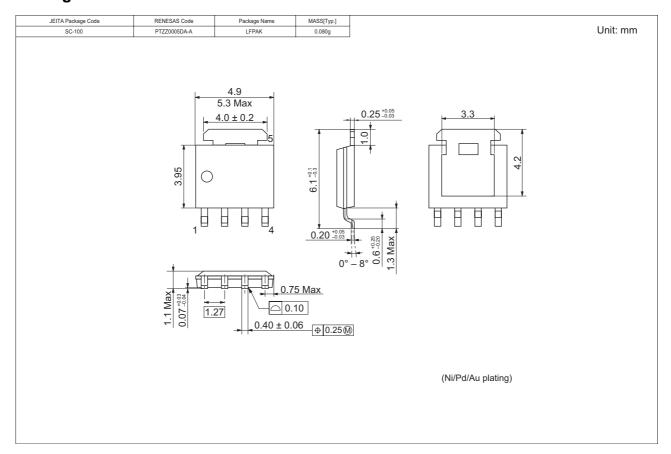








# **Package Dimensions**



# **Ordering Information**

Part Name	Quantity	Shipping Container
HAT2172H-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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