# **Power SMD LED PLCC-4**



### DESCRIPTION

The VLMK/Y32.. is an advanced development in terms of heat dissipation.

The leadframe profile of this PLCC-4 SMD package is optimized to reduce the thermal resistance.

This allows higher drive current and doubles the light output compared to Vishay's high intensity SMD LED in PLCC-2 package.

### **PRODUCT GROUP AND PACKAGE DATA**

- Product aroup: LED
- Package: SMD PLCC-4
- Product series: power

DADTS TARLE

Angle of half intensity: ± 60°

### **FEATURES**

- · Available in 8 mm tape
- High brightness SMD LED
- · Luminous intensity and color categorized per packing unit
- · Luminous intensity ratio per packing unit  $I_{Vmax}/I_{Vmin} \le 1.6$
- · ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- · Suitable for all soldering methods according to CECC 00802 and J-STD-020C
- Preconditioning: acc. to JEDEC level 2a
- Qualified according to JEDEC moisture sensitivity level 2a
- Automotive gualified: AEC-Q101
- Lead (Pb)-free device
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC
- Compatible with IR reflow solder processes according to CECC 00802 and J-STD-020C

#### **APPLICATIONS**

- Interior and exterior lighting
- · Indicator and backlighting purposes for audio, video, LCDs, switches, symbols, illuminated advertising etc.
- · Illumination purpose, alternative to incandescent lamps
- General use

PART	COLOR, LUMINOUS INTENSITY	TECHNOLOGY		
VLMK32ABBB-GS08	Amber, I <sub>V</sub> = (1400 to 2850) mcd	AlInGaP on Si		
VLMY32ABBB-GS08	Yellow, I <sub>V</sub> = (1400 to 2850) mcd	AllnGaP on Si		

ABSOLUTE MAXIMUM RATINGS <sup>1)</sup> VLMK/Y32						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Reverse voltage 2)		V <sub>R</sub>	5	V		
Forward current		١ <sub>F</sub>	70	mA		
Power dissipation		P <sub>tot</sub>	200	mW		
Junction temperature		Tj	125	°C		
Operating temperature range		T <sub>amb</sub>	- 40 to + 100	°C		
Storage temperature range		T <sub>stg</sub>	- 40 to + 100	°C		
Thermal resistance junction/ambient	mounted on PC board FR4	R <sub>thJA</sub>	290	K/W		

Note:

<sup>1)</sup>  $T_{amb} = 25 \ ^{\circ}C$ , unless otherwise specified

<sup>2)</sup> Driving the LED in reverse direction is suitable for short term application





OPTICAL AND ELECTRICAL CHARACTERISTICS <sup>1)</sup> VLMK32., AMBER								
PARAMETER	TEST CONDITION	PART	INCLUDED	SYMBOL	MIN.	TYP.	MAX.	UNIT
			VLMK32AB	Ι <sub>V</sub>	1400		1800	mcd
Luminous intensity 2)	I <sub>F</sub> = 50 mA	VLMK32ABBB	VLMK32BA	Ι <sub>V</sub>	1800		2240	mcd
			VLMK32BB	۱ <sub>۷</sub>	2240		2850	mcd
Dominant wavelength	I <sub>F</sub> = 50 mA			λ <sub>d</sub>	610		621	nm
Spectral bandwidth at 50 % I <sub>rel max</sub>	I <sub>F</sub> = 50 mA			Δλ		18		nm
Angle of half intensity	I <sub>F</sub> = 50 mA			φ		± 60		deg
Forward voltage 3)	I <sub>F</sub> = 50 mA			V <sub>F</sub>	1.85		3.03	V
Reverse current	V <sub>R</sub> = 5 V			I <sub>R</sub>		0.01	10	μA

Note:

<sup>1)</sup>  $T_{amb} = 25 \text{ °C}$ , unless otherwise specified

<sup>2)</sup> In one packing unit  $I_{Vmax}/I_{Vmin} \le 1.6$ <sup>3)</sup> Forward voltages are tested at a current pulse duration of 1 ms and a tolerance of ± 0.1 V

OPTICAL AND ELECTRICAL CHARACTERISTICS <sup>1)</sup> VLMY32, YELLOW								
PARAMETER	TEST CONDITION	PART	INCLUDED	SYMBOL	MIN.	TYP.	MAX.	UNIT
			VLMY32AB	Ι <sub>V</sub>	1400		1800	mcd
Luminous intensity 2)	I <sub>F</sub> = 50 mA	VLMY32ABBB	VLMY32BA	۱ <sub>۷</sub>	1800		2240	mcd
			VLMY32BB	۱ <sub>۷</sub>	2240		2850	mcd
Dominant wavelength	I <sub>F</sub> = 50 mA			$\lambda_{d}$	585	588	594	nm
Spectral bandwidth at 50 % I <sub>rel max</sub>	I <sub>F</sub> = 50 mA			Δλ		18		nm
Angle of half intensity	I <sub>F</sub> = 50 mA			φ		± 60		deg
Forward voltage 3)	I <sub>F</sub> = 50 mA			V <sub>F</sub>	1.85		3.03	V
Reverse current	V <sub>R</sub> = 5 V			I <sub>R</sub>		0.01	10	μA

Note:

<sup>1)</sup>  $T_{amb} = 25 \,^{\circ}C$ , unless otherwise specified

<sup>2)</sup> In one packing unit  $I_{Vmax}/I_{Vmin} \le 1.6$ <sup>3)</sup> Forward voltages are tested at a current pulse duration of 1 ms and a tolerance of ± 0.1 V

LUMINOUS INTENSITY CLASSIFICATION				
GROUP	GROUP LIGHT INTENSITY (MCD)			
STANDARD	MIN.	MAX.		
AB	1400	1800		
BA	1800	2240		
BB	2240	2850		

#### Note:

Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of  $\pm 11$  %.

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel). In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.

In order to ensure availability, single wavelength groups will not be orderable.

COLOR	CLASSIFICATION	
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	YEL	LOW	AMBER		
GROUP	DOM. WAVELENGTH (NM)				
	MIN.	MAX.	MIN.	MAX.	
Х	585	588			
Y	588	591			
Z	591	594			
W			610	615	
Х			615	621	

Note:

Wavelengths are tested at a current pulse duration of 25 ms and an accuracy of ± 1 nm.

# CROSSING TABLE

VISHAY	OSRAM			
VLMK32ABBB-GS08	LAE6SF-AABB			
VLMY32ABBB-GS08	LYE6SF-AABB			



### **TYPICAL CHARACTERISTICS**

 $T_{amb}$  = 25 °C, unless otherwise specified

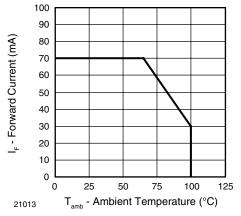


Figure 1. Forward Current vs. Ambient Temperature

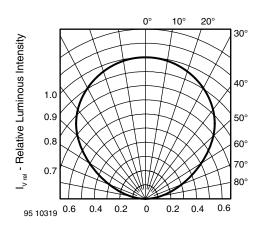


Figure 2. Rel. Luminous Intensity vs. Angular Displacement

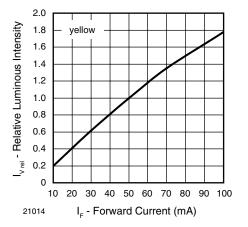


Figure 3. Relative Luminous Intensity vs. Forward Current

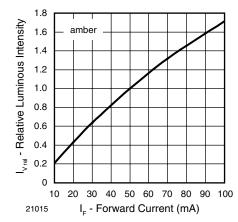


Figure 4. Relative Luminous Intensity vs. Forward Current

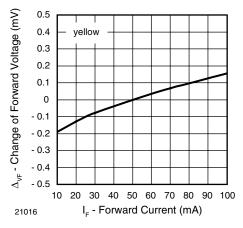


Figure 5. Change of Forward Voltage vs. Forward Current

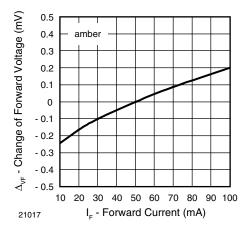


Figure 6. Change of Forward Voltage vs. Forward Current

# VLMK/Y32..

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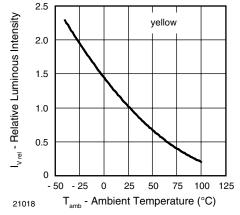


Figure 7. Relative Lum. Intensity vs. Ambient Temperature

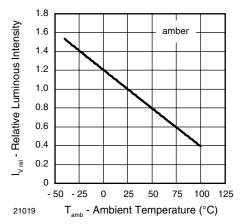


Figure 8. Relative Lum. Intensity vs. Ambient Temperature

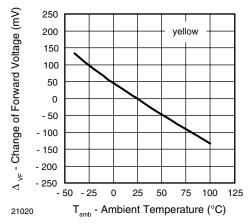


Figure 9. Change of Forward Voltage vs. Ambient Temperature

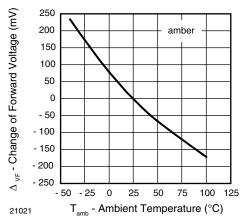
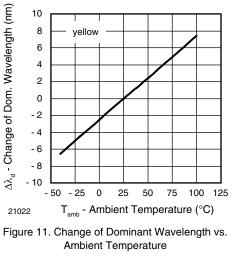


Figure 10. Change of Forward Voltage vs. Ambient Temperature



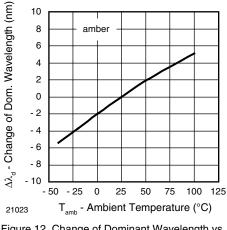
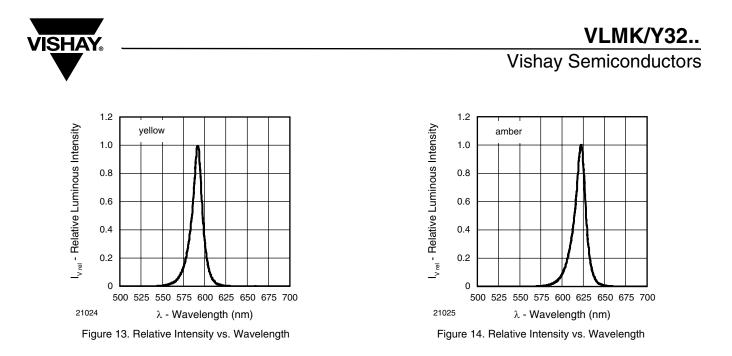
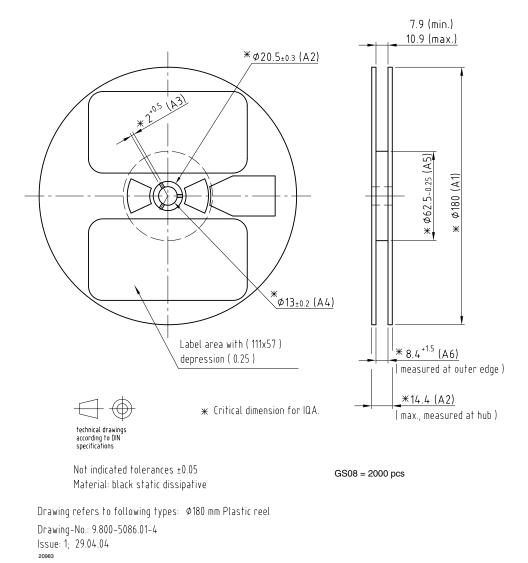


Figure 12. Change of Dominant Wavelength vs. Ambient Temperature





#### **REEL DIMENSIONS** in millimeters

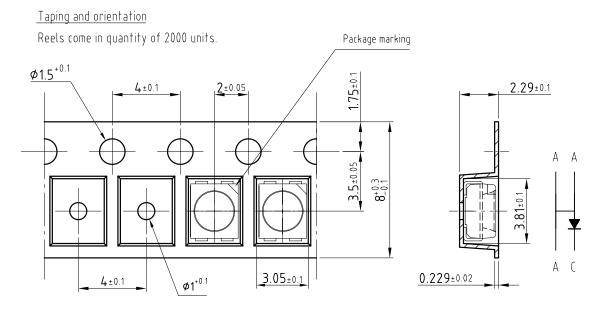


# VLMK/Y32..

# Vishay Semiconductors

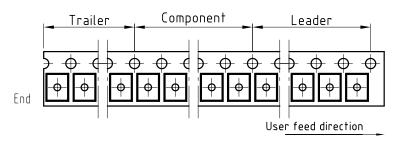


### TAPING DIMENSIONS in millimeters



200mm min. for Ø180 reel

480mm min. for Ø180 reel



Drawing-No.: 9.700-5334.01-4 Issue: 2; 22.02.08 21066



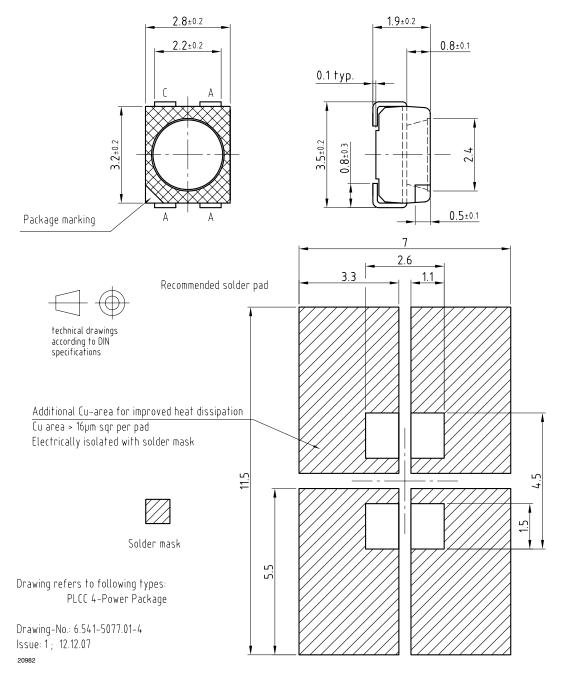
technical drawings according to DIN specifications

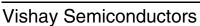


# VLMK/Y32..

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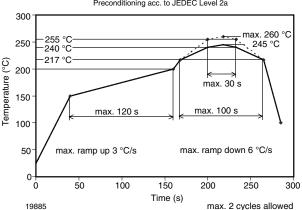
### PACKAGE/SOLDERING PADS DIMENSIONS in millimeters







# SOLDERING PROFILE



IR Reflow Soldering Profile for Lead (Pb)-free Soldering Preconditioning acc. to JEDEC Level 2a

# Figure 15. Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020B)

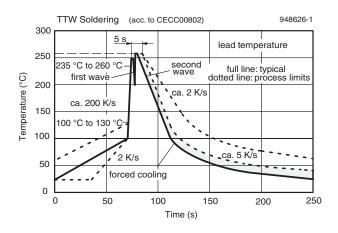
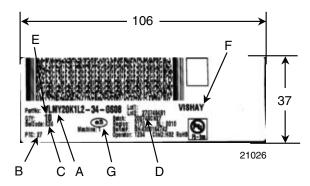


Figure 16. Double Wave Soldering of Opto Devices (all Packages)

### BAR CODE PRODUCT LABEL EXAMPLE:

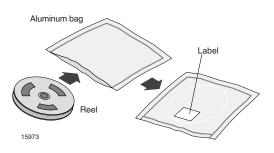


- A) Type of component
- B) PTC = manufacturing plant
- C) SEL selection code (bin):
  - e.g.: K2 = code for luminous intensity group 4 = code for color group
- D) Batch/date code
- E) Total quantity
- F) Company code
- G) Code for lead (Pb)-free classification (e3)



### **DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



### **FINAL PACKING**

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

#### **RECOMMENDED METHOD OF STORAGE**

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity  $\leq$  60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

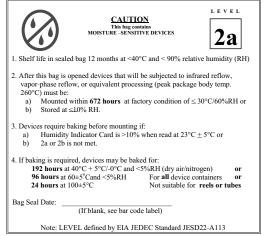
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

#### **ESD PRECAUTION**

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

#### VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



# **OZONE DEPLETING SUBSTANCES POLICY STATEMENT**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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Vishay

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