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FDA20N50_F109 N-Channel UniFETTM MOSFET 500 V, 20 A, 230 mΩ

Features

- $R_{DS(on)}$ = 230 m Ω (Max.) @ V_{GS} = 10 V, I_D = 10 A
- Low Gate Charge (Typ. 45.6 nC)
- Low C_{rss} (Typ. 27 pF)
- 100% Avalanche Tested
- Improved dv/dt Capability

Applications

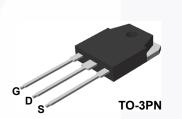
- PDP TV
- Uninterruptible Power Supply
- AC-DC Power Supply

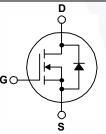
June 2014

FDA20N50_F109 — N-Channel UniFETTM MOSFET

Description

UniFETTM MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





Absolute Maximum Ratings T_C = 25^oC unless otherwise noted.

Symbol	Parameter			FDA20N50_F109	Unit	
V _{DSS}	Drain-Source Volta	ge		500	V	
I _D	Drain Current	- Continuous ($T_C = 25^{\circ}C$) - Continuous ($T_C = 100^{\circ}C$)		22 13.2	A A	
I _{DM}	Drain Current	- Pulsed	(Note 1)	88	A	
V _{GSS}	Gate-Source voltage			± 30	V	
E _{AS}	Single Pulsed Aval	anche Energy	(Note 2)	1110	mJ	
I _{AR}	Avalanche Current		(Note 1)	22	A	
E _{AR}	Repetitive Avalance	he Energy	(Note 1)	28.0	mJ	
dv/dt	Peak Diode Recov	ery dv/dt	(Note 3)	20	V/ns	
P _D	Power Dissipation	(T _C = 25°C) - Derate above 25°C		280 2.3	W W/°C	
T _{J,} T _{STG}	Operating and Sto	rage Temperature Range		-55 to +150	°C	
Τ _L	Maximum Lead Te 1/8" from Case for	mperature for Soldering Purpose 5 Seconds	Э,	300	°C	

Thermal Characteristics

Symbol	Parameter	FDA20N50_F109	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case, Max.	0.44	°C/W
R_{\thetaJA}	Thermal Resistance, Junction-to-Ambient, Max.	40	°C/W

Device Marking		Device	Pac	kage	Reel Size	Тар	e Width		Quantity	
FDA20N50 FDA20N		FDA20N50_F109	TO	TO-3PN Tube			N/A		30 units	
Electrica	l Cha	racteristics ⊤ _c	= 25 ^o C ur	less otherw	ise noted.					
Symbol	Parameter			Conditions		Min	Тур	Max	Unit	
Off Characte	eristics									
BV _{DSS}	Drain-Source Breakdown Voltage			V_{GS} = 0V, I _D = 250µA, T _J = 25°C			500			V
000	Breakdown Voltage Temperature Coefficient			$I_D = 250\mu A$, Referenced to $25^{\circ}C$				0.50		V/°C
I _{DSS}	Zero Gate Voltage Drain Current			$V_{DS} = 500V, V_{GS} = 0V$ $V_{DS} = 400V, T_{C} = 125^{\circ}C$					1 10	μΑ μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward			V _{GS} = 30V, V _{DS} = 0V					100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse			V _{GS} = -30V, V _{DS} = 0V				-	-100	nA
On Characte	eristics									
V _{GS(th)}	Gate Threshold Voltage			$V_{DS} = V_{GS}$, $I_D = 250 \mu A$			3.0		5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance			V _{GS} = 10V, I _D = 11A				0.20	0.23	Ω
g _{FS} I	Forward Transconductance			V _{DS} = 40V, I _D = 11A				24.6		S
Dynamic Ch	aracteris	stics								
C _{iss}	Input Capacitance Output Capacitance			$V_{DS} = 25V, V_{GS} = 0V,$			2400	3120	pF	
				f = 1.0MHz				355	465	pF
C _{rss} I	Reverse	Transfer Capacitance					27		pF	
Switching C	haracter	istics								
t _{d(on)}	Turn-On Delay Time			V _{DD} = 250V, I _D = 20A				95	200	ns
t _r	Turn-On l	Rise Time		R _G = 25Ω				375	760	ns
t _{d(off)}	Turn-Off I	Delay Time						100	210	ns
t _f	Turn-Off I	Fall Time				(Note 4)		105	220	ns
Q _g	Total Gate	e Charge		V _{DS} = 400V, I _D = 20A V _{GS} = 10V				45.6	59.5	nC
Q _{gs}	Gate-Sou	irce Charge						14.8		nC
Q _{gd}	Gate-Dra	in Charge		(Note 4)				21.6		nC
Drain-Sourc	e Diode (Characteristics and	Maximum	Ratings						
I _S Maximum Continuous Drain-Source Dio			ource Diod	de Forward Current					20	Α
I _{SM} I	Maximum Pulsed Drain-Source Diode Fo			orward Current					80	Α
V _{SD} I	Drain-Sou	urce Diode Forward V	oltage	V _{GS} = 0V,	I _S = 22A				1.4	V
t _{rr} I	Reverse I	Recovery Time		V _{GS} = 0V, I _S = 20A				507		ns
Q _{rr} I	Reverse I	Recovery Charge		dI _F /dt =100A/μs				7.20		μC

NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature

Package Marking and Ordering Information

2. L = 4.1mH, I_{AS} = 22A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}$ C

3. I_{SD} \leq 22A, di/dt \leq 200A/µs, V_{DD} \leq BV_{DSS}, Starting T_J = 25°C

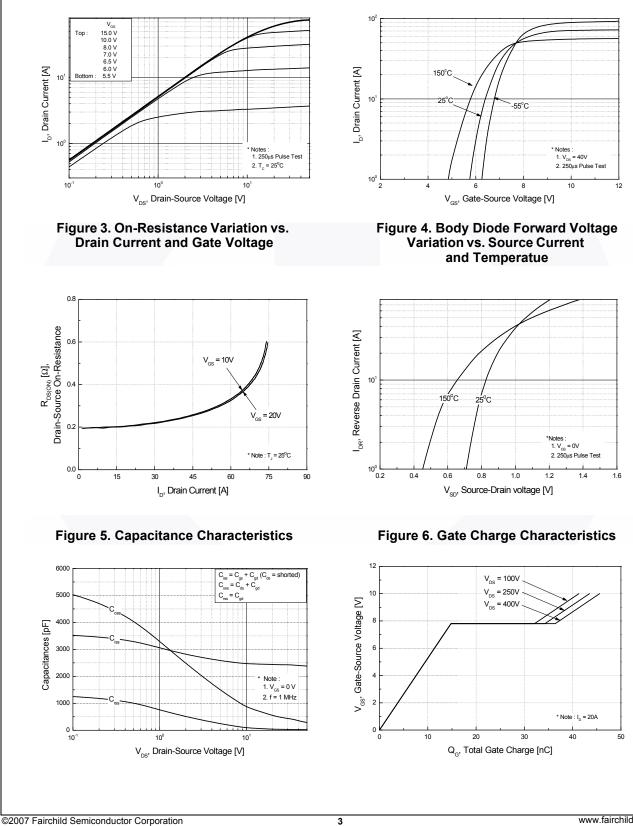
4. Essentially Independent of Operating Temperature Typical Characteristics



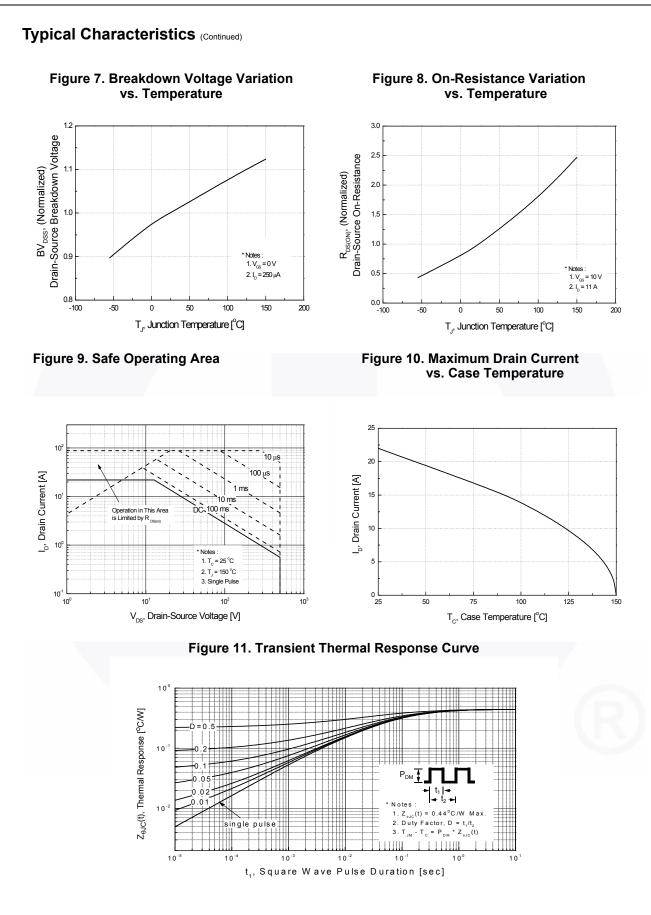
Typical Characteristics

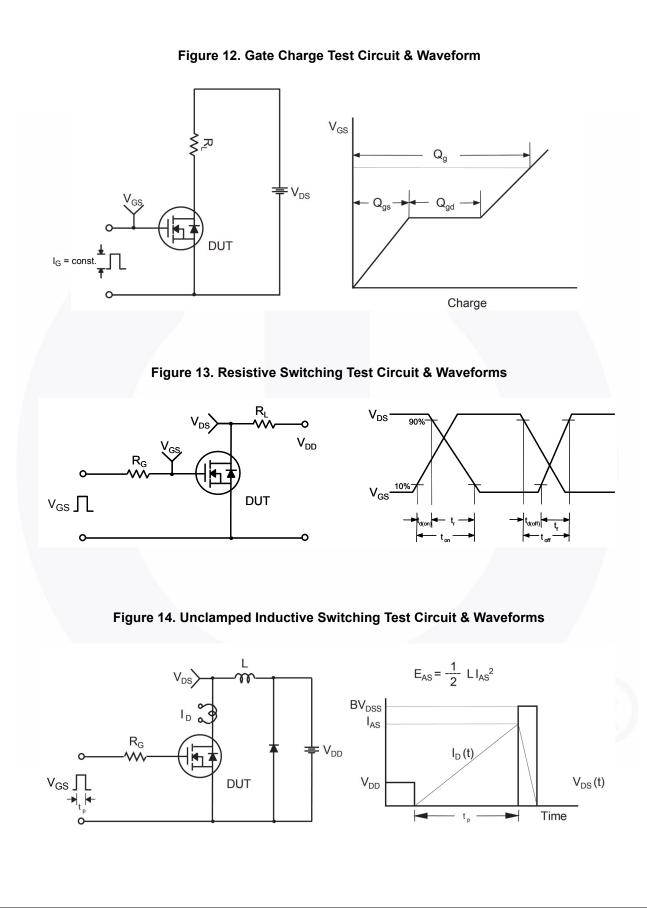


Figure 2. Transfer Characteristics



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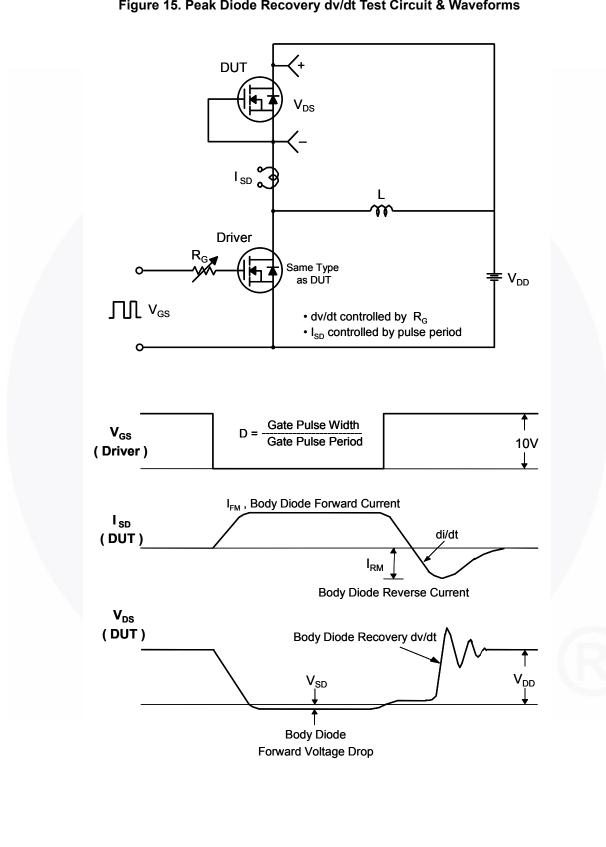
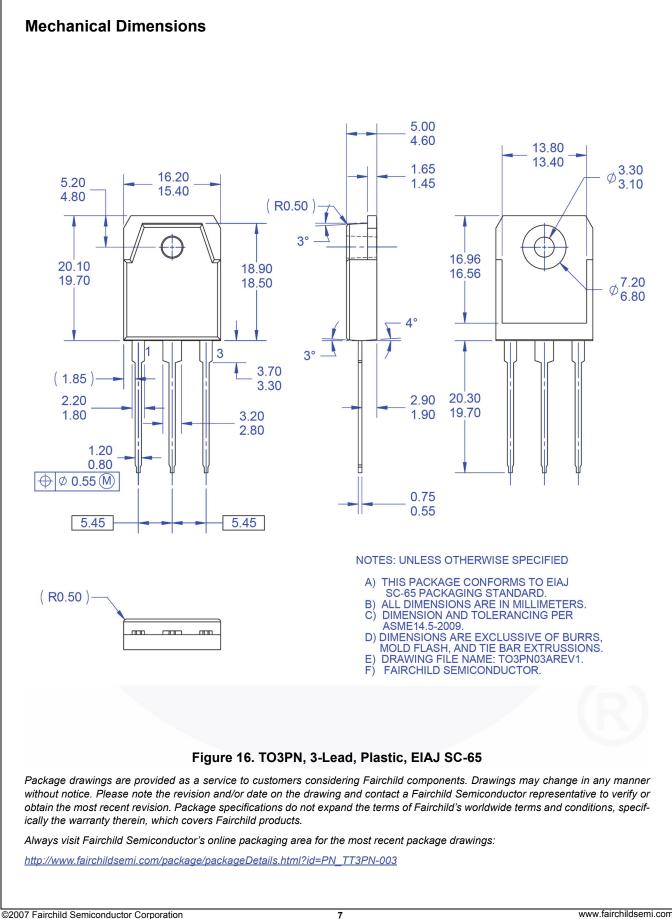


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms



FDA20N50 F109 Rev. C2

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