

Features

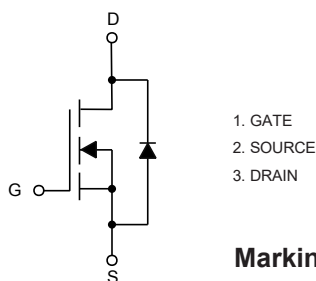
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free Available Upon Request By Adding Suffix "-HF"

Maximum Ratings

- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 105°C/W Junction to Ambient^(Note 2)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current	I_D	3.0	A
Pulsed Drain Current ^(Note1)	I_{DM}	10	A
Total Power Dissipation	P_D	1.2	W

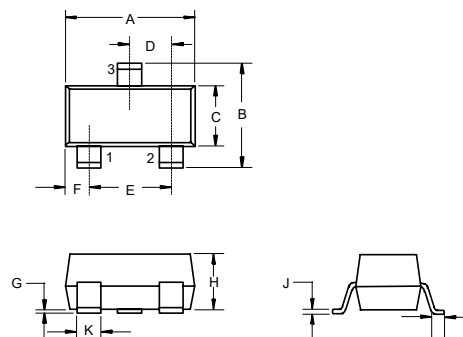
Internal Structure:



Marking Code: S10

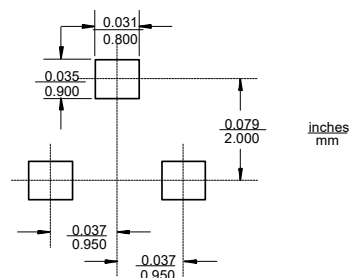
N-Channel Enhancement Mode Field Effect Transistor

SOT-23



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.110	0.120	2.80	3.04	
B	0.083	0.104	2.10	2.64	
C	0.047	0.055	1.20	1.40	
D	0.034	0.041	0.85	1.05	
E	0.067	0.083	1.70	2.10	
F	0.018	0.024	0.45	0.60	
G	0.0004	0.006	0.01	0.15	
H	0.035	0.043	0.90	1.10	
J	0.003	0.007	0.08	0.18	
K	0.012	0.020	0.30	0.51	
L	0.007	0.020	0.20	0.50	

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	60			V
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			1	μA
Gate-Threshold Voltage ^(Note 3)	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	0.5		2.0	V
Drain-Source On-Resistance ^(Note 3)	R _{DS(on)}	V _{GS} =10V, I _D =3A			105	mΩ
		V _{GS} =4.5V, I _D =3A			125	
Diode Forward Voltage ^(Note 3)	V _{SD}	V _{GS} =0V, I _S =3A			1.2	V
Forward Tranconductance ^(Note 3)	g _{FS}	V _{DS} =15V, I _D =2A	1.4			S
Dynamic Characteristics ^(Note 4)						
Input Capacitance	C _{iss}	V _{DS} =30V,V _{GS} =0V,f =1MHz		247		pF
Output Capacitance	C _{oss}			34		
Reverse Transfer Capacitance	C _{rss}			19.5		
Switching Characteristics ^(Note 4)						
Total Gate Charge	Q _g	V _{DS} =30V,V _{GS} =4.5V,I _D =3A		6		nC
Gate-Source Charge	Q _{gs}			1		
Gate-Drain Charge	Q _{gd}			1.3		
Turn-On Delay Time	t _{d(on)}	V _{GS} =10V,V _{DD} =30V,I _D =1.5A, R _{GEN} =1Ω		6		ns
Turn-On Rise Time	t _r			15		
Turn-Off Delay Time	t _{d(off)}			15		
Turn-Off Fall Time	t _f			10		

Note:

1. Repetitive rating : Pulse width limited by junction temperature.
2. Surface mounted on FR4 board , $t_s \leq 10s$.
3. Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$.
4. Guaranteed by design, not subject to producing.

Curve Characteristics

Fig. 1 - Output Characteristics

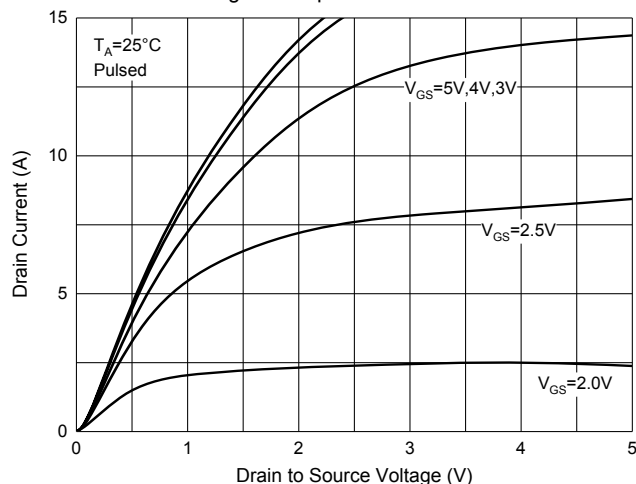


Fig. 2 - Transfer Characteristics

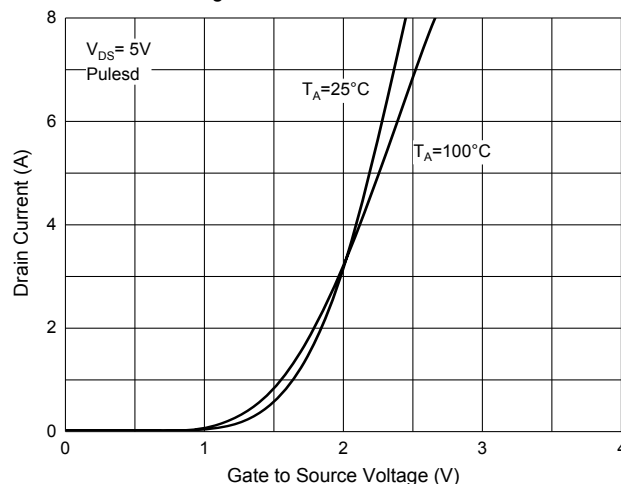


Fig. 3 - $R_{DS(ON)} - I_D$

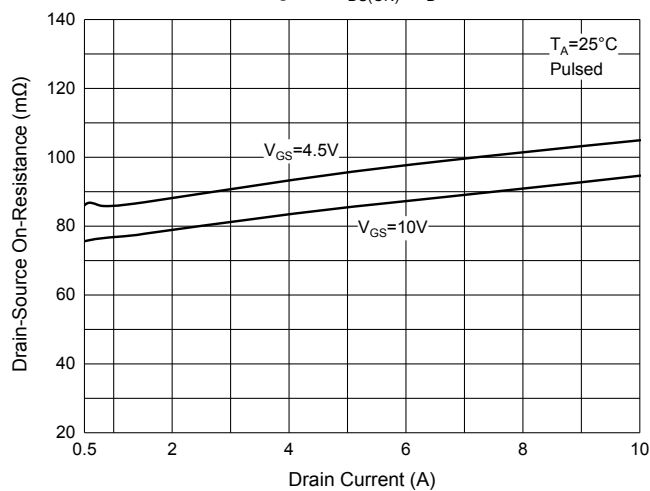


Fig. 4 - $R_{DS(ON)} - V_{GS}$

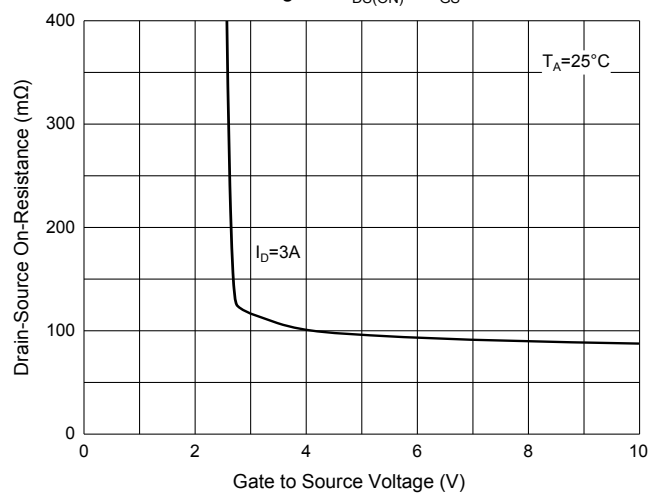


Fig. 5 - $I_S - V_{SD}$

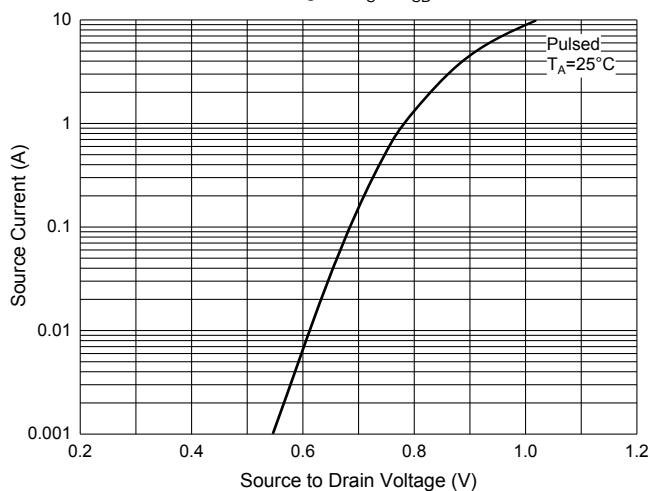
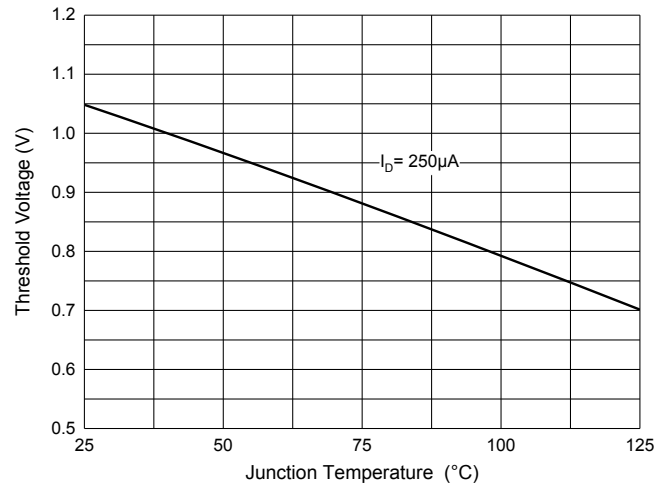


Fig. 6 - Threshold Voltage



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

Note : Adding "-HF" Suffix For Halogen Free, eg. Part Number-TP-HF

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