**Product data sheet** 

# 1. General description

Silicon Carbide Schottky diode designed for high frequency switched mode power supplies in a TO252 (DPAK) plastic package.

### 2. Features and benefits

- · Highly stable switching performance
- High forward surge capability I<sub>FSM</sub>
- Extremely fast reverse recovery time
- Superior in efficiency to Silicon Diode alternatives
- Reduced losses in associated MOSFET
- Reduced EMI
- · Reduced cooling requirements
- RoHS compliant

# 3. Applications

- Power factor correction
- Telecom/Server SMPS
- UPS
- PV inverter
- PC Silverbox
- LED/OLED TV
- Motor Drives

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>RRM</sub>	repetitive peak reverse voltage		-	-	650	V
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5 ; T <sub>mb</sub> ≤ 113 °C; square-wave pulse; Fig. 1; Fig. 2; Fig. 3; Fig. 4	-	-	10	A
Tj	junction temperature		-	-	175	°C
Static charac	teristics					
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 10 A; T <sub>j</sub> = 25 °C; <u>Fig. 6</u>	-	1.5	1.7	V
		I <sub>F</sub> = 10 A; T <sub>j</sub> = 150 °C; <u>Fig. 6</u>	-	1.8	2.1	V
Dynamic cha	racteristics					
Q <sub>r</sub>	recovered charge	I <sub>F</sub> = 10 A; dI <sub>F</sub> /dt = 500 A/μs; V <sub>R</sub> = 400 V; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	15	-	nC

# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	n.c.	not connected	mb	K — A
2	K	cathode[1]		001aaa020
3	Α	anode		
mb	К	mounting base; connected to cathode	DPAK (TO252NS)	

[1] It is not possible to connect to pin 2 of the TO252 package.

# 6. Ordering information

## **Table 3. Ordering information**

Type number	Package	ackage						
	Name	Description	Version					
NXPSC10650D	DPAK	plastic single-ended surface-mounted package (DPAK); 3 leads (one lead cropped)	TO252NS					

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# 7. Limiting values

### **Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{RRM}$	repetitive peak reverse voltage		-	650	V
$V_{RWM}$	crest working reverse voltage		-	650	V
$V_R$	reverse voltage	DC	-	650	V
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5 ; T <sub>mb</sub> ≤ 113 °C; square-wave pulse; Fig. 1; Fig. 2; Fig. 3; Fig. 4	-	10	Α
I <sub>FRM</sub>	repetitive peak forward current	$\delta$ = 0.5 ; t <sub>p</sub> = 25 µs; square-wave pulse	-	20	Α
I <sub>FSM</sub>	non-repetitive peak	$t_p$ = 10 ms; $T_{j(init)}$ = 25 °C; sine-wave pulse	-	50	Α
	forward current	$t_p$ = 10 $\mu$ s; $T_{j(init)}$ = 25 °C; square-wave pulse	-	450	Α
T <sub>stg</sub>	storage temperature		-55	175	°C
Tj	junction temperature		-	175	°C

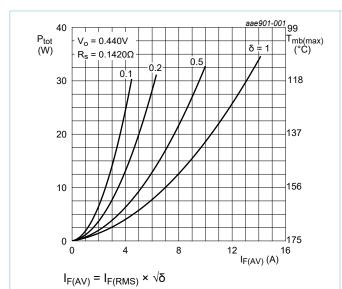


Fig. 1. Forward power dissipation as a function of average forward current; square waveform; maximum values

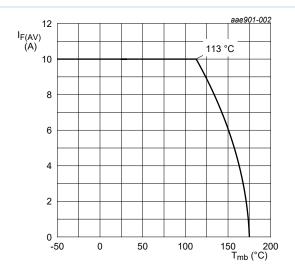


Fig. 2. Forward current as a function of mounting base temperature; maximum values

WeEn Semiconductors NXPSC10650D

### Silicon Carbide Diode

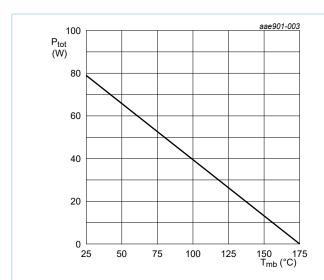


Fig. 3. Total power dissipation as a function of mounting base temperature

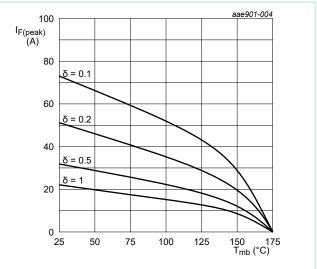


Fig. 4. Current derating as a function of mounting base temperature

## 8. Thermal characteristics

**Table 5. Thermal characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base	Fig. 5	-	-	1.9	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient free air	Device mounted on an FR4 Printed- Circuit Board	-	50	-	K/W

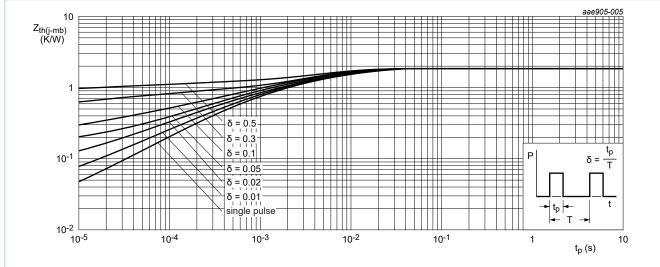
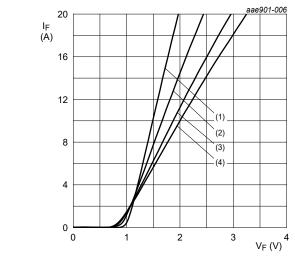


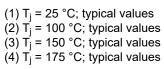
Fig. 5. Transient thermal impedance from junction to mounting base as a function of pulse duration

## 9. Characteristics

### **Table 6. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static char	acteristics					
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 10 A; T <sub>j</sub> = 25 °C; <u>Fig. 6</u>	-	1.5	1.7	V
		I <sub>F</sub> = 10 A; T <sub>j</sub> = 150 °C; <u>Fig. 6</u>	-	1.8	2.1	V
I <sub>R</sub>	reverse current	V <sub>R</sub> = 650 V; T <sub>j</sub> = 25 °C	-	-	250	μΑ
		V <sub>R</sub> = 650 V; T <sub>j</sub> = 150 °C	-	-	800	μΑ
Dynamic cl	haracteristics			,		
Q <sub>r</sub>	recovered charge	$I_F = 10 \text{ A; } dI_F/dt = 500 \text{ A/}\mu\text{s;}$ $V_R = 400 \text{ V; } T_j = 25 \text{ °C; } Fig. 7$	-	15	-	nC
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 1 V; T <sub>j</sub> = 25 °C	-	300	-	pF
		f = 1 MHz; V <sub>R</sub> = 300 V; T <sub>j</sub> = 25 °C	-	34	-	pF
		$f = 1 \text{ MHz}; V_R = 600 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	28	-	pF







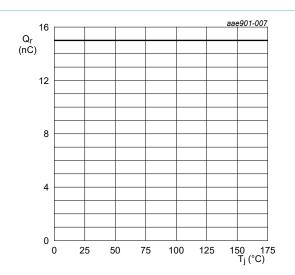


Fig. 7. Recovered charge as a function of junction temperature

## 10. Package outline

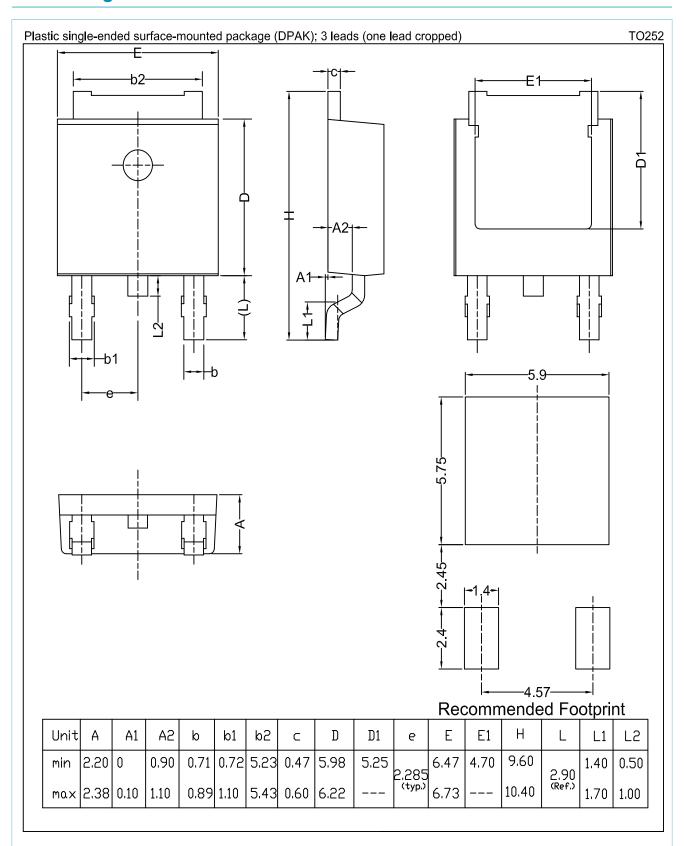


Fig. 8. Package outline DPAK (TO252NS)

# 11. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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