

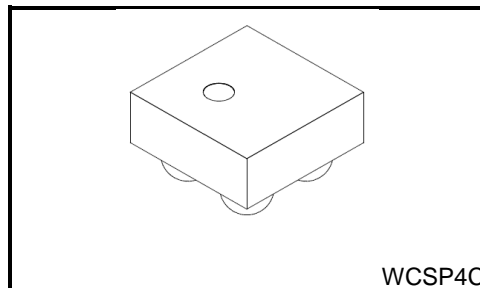
TCK206G, TCK207G, TCK208G

0.75 V, 2A Load Switch IC with Reverse Current Blocking in Ultra Small Package

The TCK206G, TCK207G and TCK208G are 0.75 V ultra low voltage load switch ICs for a power management with slew rate control driver and reverse current blocking(SW OFF state) function.

Switch ON resistance is only 18.4 mΩ (typ.) at 0.75 V, -1.5 A condition and output current is available up to 2.0 A (DC). TCK207G and TCK208G feature output auto-discharge function.

These devices are available in 0.5 mm pitch ultra small package WCSP4C (0.9 mm x 0.9 mm, t: 0.5 mm) .Thus this devices is ideal for portable applications that require high-density board assembly such as cellular phone.

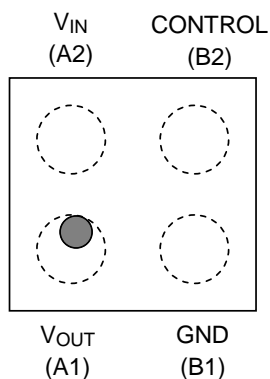


Weight : 0.9 mg (typ.)

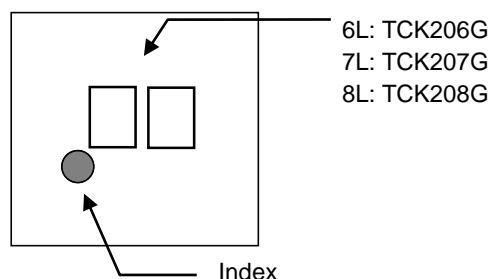
Feature

- Low input voltage operation: $V_{IN} = 0.75$ to 3.6 V
- High output current: I_{OUT} (DC) = 2.0 A
- Low ON resistance :
 $R_{ON} = 18.1$ mΩ (typ.) at $V_{IN} = 3.3$ V, -1.5 A
 $R_{ON} = 18.2$ mΩ (typ.) at $V_{IN} = 1.2$ V, -1.5 A
 $R_{ON} = 18.4$ mΩ (typ.) at $V_{IN} = 0.75$ V, -1.5 A
- Built in Slew rate control driver
- Built in Reverse current blocking(SW OFF state)
- Built in Auto-discharge (TCK207G and TCK208G)
- Active High and Pull down connection between CONTROL and GND (TCK206G and TCK207G)
- Active Low (TCK208G)
- Ultra small package : WCSP4C (0.9 mm x 0.9 mm, t: 0.5 mm)

Pin Assignment(Top view)



Top marking



Start of commercial production
2014-06

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating		Unit
Input voltage	V _{IN}	-0.3 to 4.0		V
Control voltage	V _{CT}	-0.3 to 4.0		V
Output voltage	V _{OUT}	-0.3 to V _{IN} +0.3		V
Output current	I _{OUT}	DC	2.0	A
		Pulse	3.0 (Note 1)	A
Power dissipation	P _D	800 (Note 2)		mW
Operating temperature range	T _{opr}	-40 to 85		°C
Junction temperature	T _j	150		°C
Storage temperature	T _{stg}	-55 to 150		°C

Note : Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note1: 100 μs pulse, 2% duty cycle

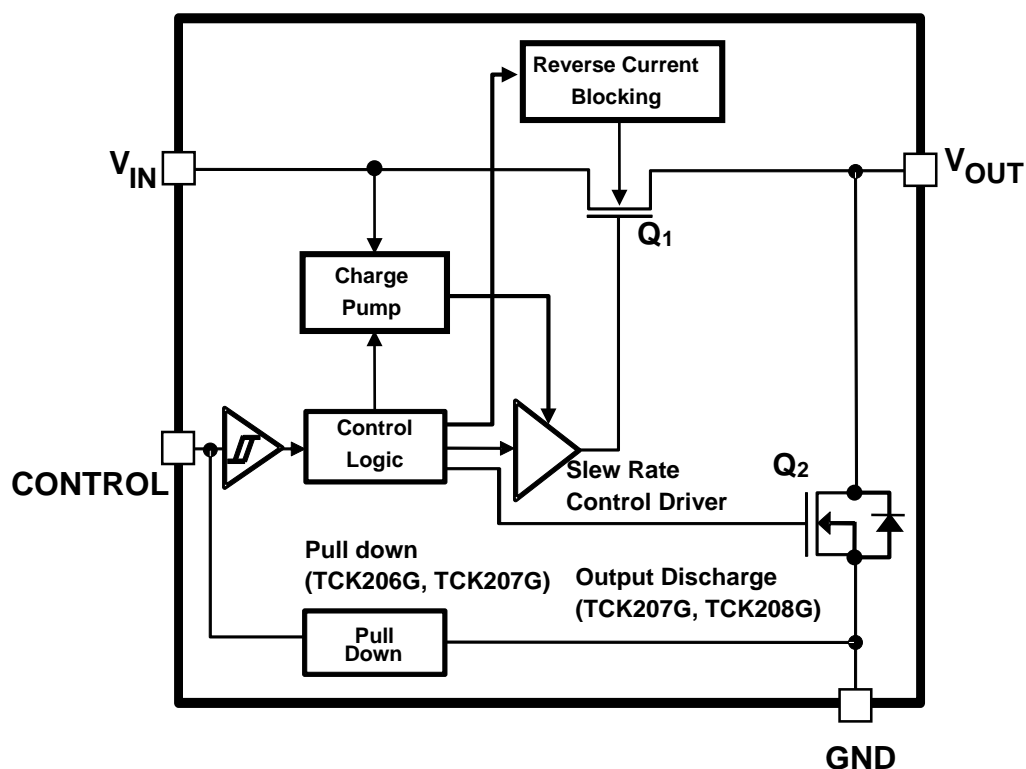
Note2: Rating at mounting on a board

(Glass epoxy(FR4) board dimension : 40mm x 40mm, both sides of board

Metal pattern ratio : a surface approximately 50%, the reverse side approximately 50%

Through hole hole : diameter 0.5mm x 28)

Block Diagram



Function Table

Part number	Function		
	Reverse current blocking	Output auto-discharge	Control pin connection
TCK206G	Bulit in	N/A	Pull down (Active High)
TCK207G	Bulit in	Bulit in	Pull down (Active High)
TCK208G	Bulit in	Bulit in	OPEN (Active Low)

Operation Logic Table

0.75 V ≤ V_{IN} ≤ 3.6V (Ta = -40 to 85°C)

		TCK206G	TCK207G	TCK208G
Control voltage “High”	Output Q ₁	ON	ON	OFF
	Discharge Q ₂	—	OFF	ON
	Reverse current block	Disable	Disable	Active
Control voltage “Low”	Output Q ₁	OFF	OFF	ON
	Discharge Q ₂	—	ON	OFF
	Reverse current block	Active	Active	Disable
Control voltage “OPEN”	Output Q ₁	OFF	OFF	—
	Discharge Q ₂	—	ON	—
	Reverse current block	Active	Active	—

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
				Min	Typ.	Max	Min	Max	
Input voltage	V _{IN}	—		0.75	—	3.6	0.75	3.6	V
CONTROL High-level input voltage	V _{IH}	—	0.9 V ≤ V _{IN}	0.9	—	—	0.9	—	V
			V _{IN} < 0.9V	V _{IN}	—	—	V _{IN}	—	V
CONTROL Low-level input voltage	V _{IL}	—	0.9 V ≤ V _{IN}	—	—	0.4	—	0.4	V
			V _{IN} < 0.9V	—	—	0.3	—	0.3	V
Quiescent current (ON state)	I _Q	V _{IN} = V _{CT} = 3.6 V, I _{OUT} = 0 mA	TCK206G TCK207G	—	22	—	—	35	μA
		V _{IN} = 3.6 V, V _{CT} = 0 V, I _{OUT} = 0 mA	TCK208G						
Standby current (SW OFF state)	I _{Q(OFF)}	V _{IN} = 3.6 V, V _{CT} = 0 V, V _{OUT} = OPEN (Note 3)	TCK206G TCK207G	—	0.7	—	—	5	μA
		V _{IN} = V _{CT} = 3.6 V, V _{OUT} = OPEN (Note 3)	TCK208G						
Reverse blocking current	I _{RB}	V _{OUT} = 3.6 V, V _{IN} = 0 V, V _{CT} = 0 V		—	0.1	—	—	10	μA
On resistance	R _{ON}	V _{IN} = 3.3 V, I _{OUT} = -1.5 A		—	18.1	—	—	28	mΩ
		V _{IN} = 1.8 V, I _{OUT} = -1.5 A		—	18.1	—	—	28	
		V _{IN} = 1.2 V, I _{OUT} = -1.5 A		—	18.2	—	—	28	
		V _{IN} = 1.0 V, I _{OUT} = -1.5 A		—	18.2	—	—	28	
		V _{IN} = 0.75 V, I _{OUT} = -1.5 A		—	18.4	—	—	28	
Discharge on resistance	R _{SD}	— (TCK207G and TCK208G)		—	380	—	—	—	Ω

Note 3: Except SW OFF state switch current (I_{SD(OFF)}): V_{IN} = 5.5 V, V_{CT} = 0 V, V_{OUT} = GND).

AC Characteristics (Ta = 25°C)

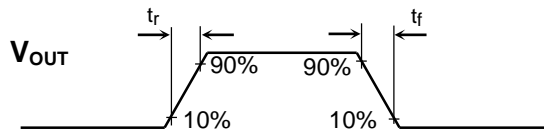
V_{IN} = 0.75 V

Characteristics	Symbol	Test Condition(Figure 1)	Min	Typ.	Max	Unit
V _{OUT} rise time	t _r	R _L =500Ω , C _L =0.1μF	—	180	—	μs
V _{OUT} fall time	t _f	R _L =500Ω , C _L =0.1μF	TCK207G TCK208G	—	50	μs
			TCK206G	—	80	
Turn on delay	t _{ON}	R _L =500Ω , C _L =0.1μF	—	75	—	μs
Turn off delay	t _{OFF}	R _L =500Ω , C _L =0.1μF	TCK206G TCK207G	—	8	μs
			TCK208G	—	14	

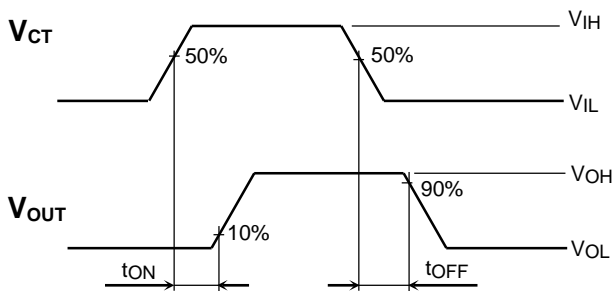
V_{IN} = 1.2 V

Characteristics	Symbol	Test Condition(Figure 1)	Min	Typ.	Max	Unit
V _{OUT} rise time	t _r	R _L =500Ω , C _L =0.1μF	—	240	—	μs
V _{OUT} fall time	t _f	R _L =500Ω , C _L =0.1μF	TCK207G TCK208G	—	50	μs
			TCK206G	—	90	
Turn on delay	t _{ON}	R _L =500Ω , C _L =0.1μF	—	65	—	μs
Turn off delay	t _{OFF}	R _L =500Ω , C _L =0.1μF	TCK206G TCK207G	—	8	μs
			TCK208G	—	13	

AC Waveform



TCK206G, TCK207G



TCK208G

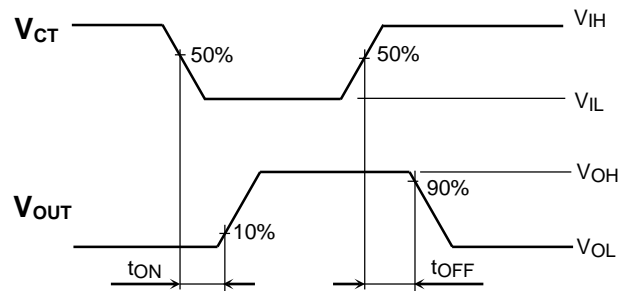
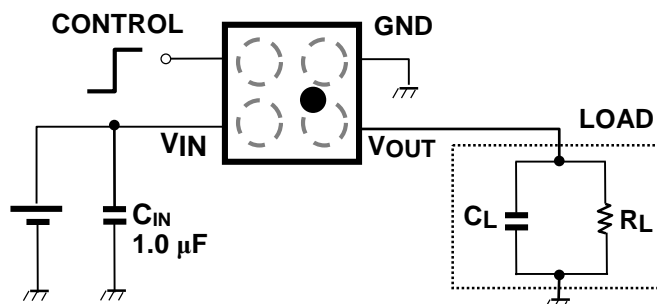


Figure 1 t_r, t_f, t_{ON}, t_{OFF} Waveforms

Application Note

1. Application circuit example (top view)

The figure below shows the recommended configuration for TCK206G, TCK207G and TCK208G.



1) Input capacitor

An input capacitor (C_{IN}) is necessary for the stable operation of TCK206G, TCK207G and TCK208G. And it is effective to reduce voltage drop due to sharp changes in output current and also for improved stability of the power supply. When used, place C_{IN} more than $1.0 \mu F$ as close to V_{IN} pin to improve stability of the power supply.

2) Output capacitor

An output capacitor (C_{OUT}) is not necessary for the guaranteed operation of TCK206G, TCK207G and TCK208G. However, there is a possibility of overshoot or undershoot caused by output load transient response, board layout and parasitic components of load switch IC. In this case, an output capacitor with C_{OUT} more than $0.1 \mu F$ is recommended.

3) Control pin

A control pins for TCK206G and TCK207G are both Active High and TCK208G is Active Low. These controls both the pass-through n-ch MOSFET and the discharge n-ch MOSFET (except TCK206G), operated by the control voltage and Schmitt trigger. When the control voltage level is High (Low; TCK208G), Output n-ch MOSFET is ON state and discharge n-ch MOSFET is SW OFF state. When control voltage level is Low (High; TCK208G), and the state of the MOSFETs is reversed. Also, pull down resistance equivalent to a few $M\Omega$ is connected between CONTROL and GND, thus the load switch IC is in SW OFF state even when CONTROL pin is OPEN(except TCK208G). In addition, CONTROL pin has a tolerant function such that it can be used even if the control voltage is higher than the input voltage.

2. Reverse current blocking

Reverse current blocking(SW OFF state) function is designed in these products. This function is active at output n-ch MOSEFT turned off.

However these does not assure for the suppression of uprising device operation. In use of these products, please read through and understand dissipation idea for absolute maximum ratings from the above mention or our 'Semiconductor Reliability Handbook'. Then use these products under absolute maximum ratings in any condition. Furthermore, Toshiba recommend inserting failsafe system into the design.

3. Power Dissipation

Board-mounted power dissipation ratings for TCK206G, TCK207G and TCK208G are available in the Absolute Maximum Ratings table

Power dissipation is measured on the board condition shown below.

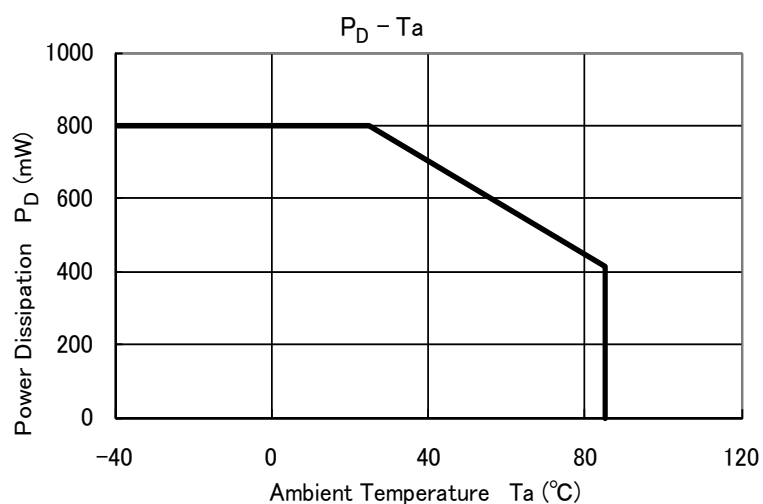
[The Board Condition]

Board material: Glass epoxy (FR4)

Board dimension: 40mm x 40mm (both sides of board), $t=1.8\text{mm}$

Metal pattern ratio: a surface approximately 50%, the reverse side approximately 50%

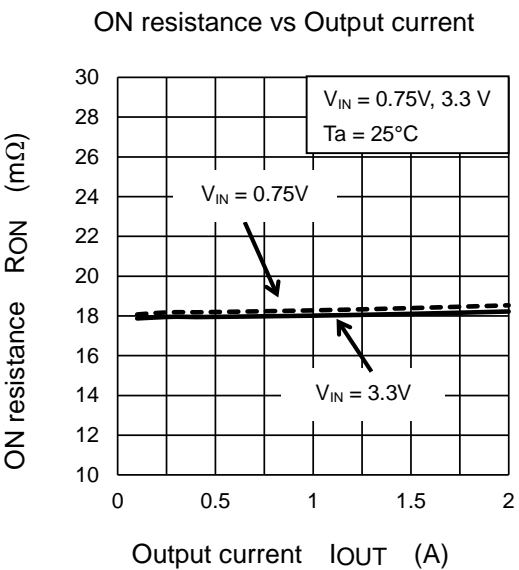
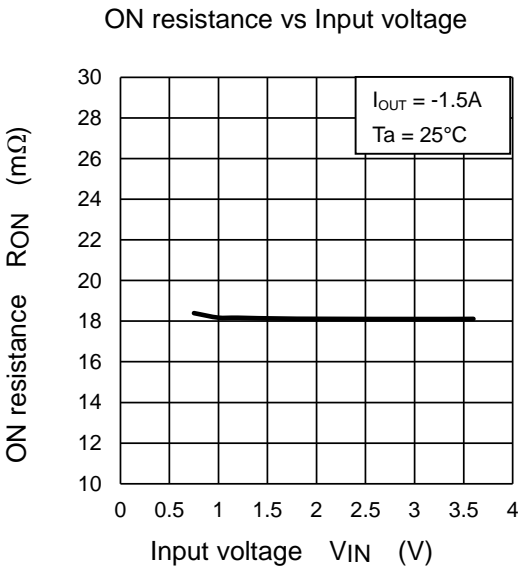
Through hole: diameter 0.5mm x 28



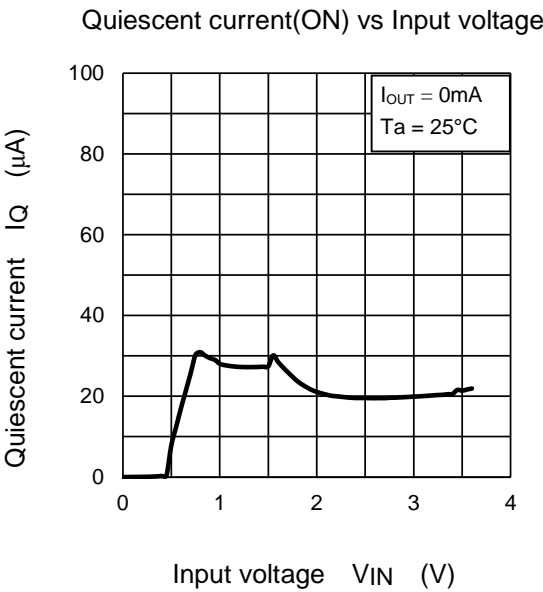
Please allow sufficient margin when designing a board pattern to fit the expected power dissipation. Also take into consideration the ambient temperature, input voltage, output current etc. and applying the appropriate derating for allowable power dissipation during operation.

Representative Typical Characteristics

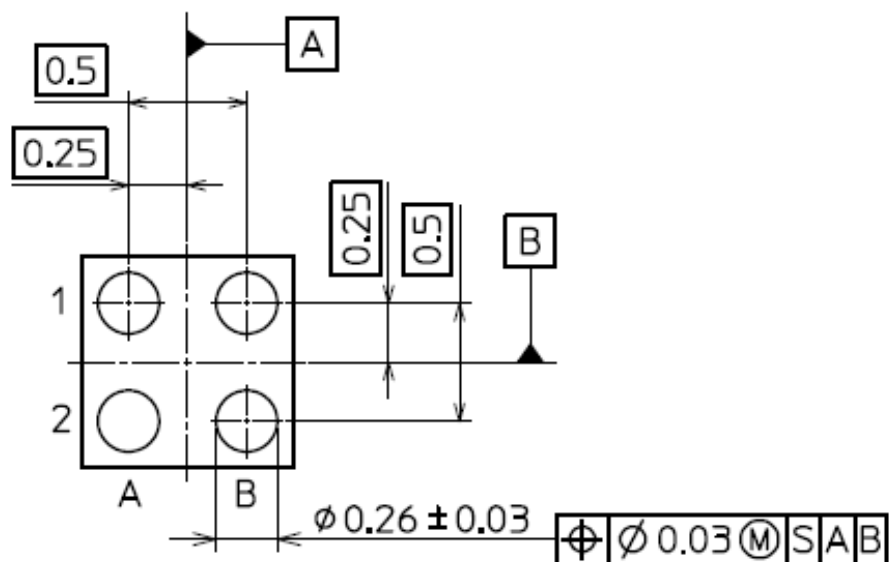
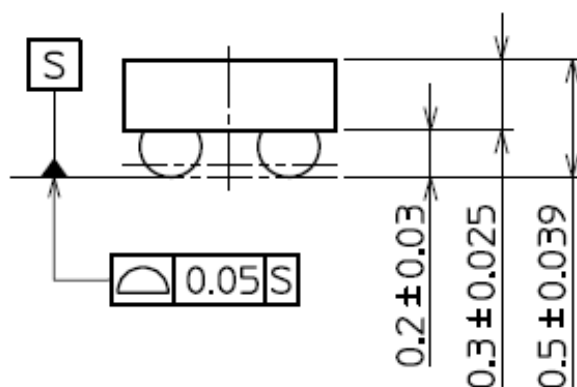
1) ON resistance



2) Quiescent current



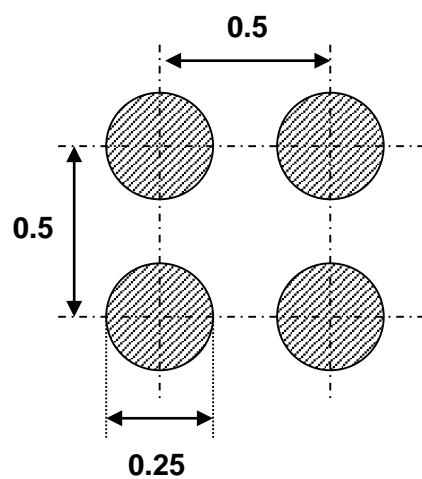
Unit: mm



Weight : 0.9 mg (typ.)

Land pattern dimensions (for reference only)

Unit:mm



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