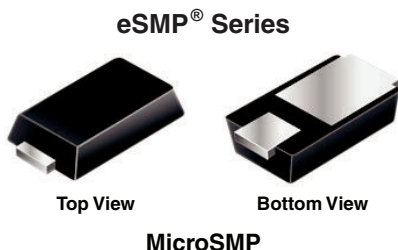


## Surface Mount Schottky Barrier Rectifiers



### FEATURES

- Very low profile - typical height of 0.65 mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition



**RoHS**  
COMPLIANT  
HALOGEN  
FREE

### MECHANICAL DATA

**Case:** MicroSMP

Molding compound meets UL 94 V-0 flammability rating  
Base P/N-M3 - halogen-free, RoHS compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS compliant, and automotive grade

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix meets JESD 201 class 2 whisker test

**Polarity:** Color band denotes the cathode end

### PRIMARY CHARACTERISTICS

$I_{F(AV)}$	2.0 A
$V_{RRM}$	20 V, 30 V
$I_{FSM}$	30 A
$V_F$ at $I_F = 2.0$ A	0.47 V
$T_J$ max.	150 °C

### TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

### MAXIMUM RATINGS ( $T_A = 25$ °C unless otherwise noted)

PARAMETER	SYMBOL	MSS2P2	MSS2P3	UNIT
Device marking code		22	23	
Maximum repetitive peak reverse voltage	$V_{RRM}$	20	30	V
Maximum average forward rectified current (fig. 1)	$I_{F(AV)}$	2.0		A
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	$I_{FSM}$	30		A
Operating junction and storage temperature range	$T_J, T_{STG}$	- 55 to + 150		°C

### ELECTRICAL CHARACTERISTICS ( $T_A = 25$ °C unless otherwise noted)

PARAMETER	TEST CONDITIONS	SYMBOL	TYP.	MAX.	UNIT
Maximum instantaneous forward voltage	$I_F = 1.0$ A	$V_F$ (1)	0.44	-	V
	$I_F = 2.0$ A		0.52	0.60	
	$I_F = 1.0$ A		0.36	-	
	$I_F = 2.0$ A		0.47	0.55	
Maximum reverse current	Rated $V_R$	$I_R$ (2)	15	250	μA
			6.0	20	mA
Typical junction capacitance	4.0 V, 1 MHz	$C_J$	65	-	pF

#### Notes

(1) Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

## MSS2P2, MSS2P3

Vishay General Semiconductor

**THERMAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$  unless otherwise noted)

PARAMETER	SYMBOL	MSS2P2	MSS2P3	UNIT
Typical thermal resistance	$R_{\theta JA}^{(1)}$	105		°C/W
	$R_{\theta JL}^{(1)}$	15		
	$R_{\theta JC}^{(1)}$	20		

**Note**

(1) Thermal resistance from junction to ambient and junction to lead mounted on PCB with 6.0 mm x 6.0 mm copper pad areas  $R_{\theta JL}$  is measured at the terminal of cathode band.  $R_{\theta JC}$  is measured at the top center of the body

**ORDERING INFORMATION** (Example)

PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
MSS2P3-M3/89A	0.006	89A	4500	7" diameter plastic tape and reel
MSS2P3HM3/89A <sup>(1)</sup>	0.006	89A	4500	7" diameter plastic tape and reel

**Note**

(1) Automotive grade

**RATINGS AND CHARACTERISTICS CURVES**

( $T_A = 25\text{ }^{\circ}\text{C}$  unless otherwise noted)

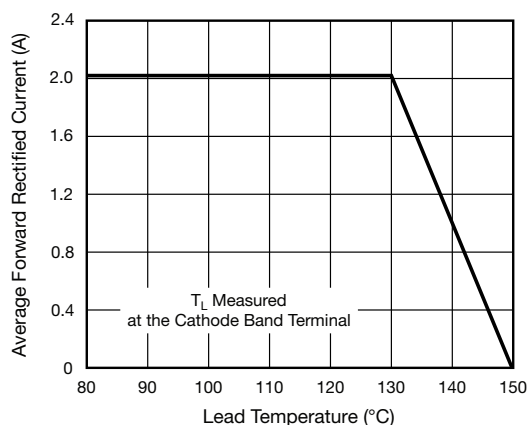


Fig. 1 - Maximum Forward Current Derating Curve

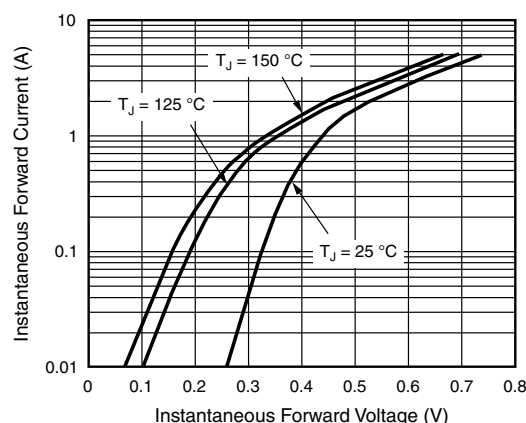


Fig. 3 - Typical Instantaneous Forward Characteristics

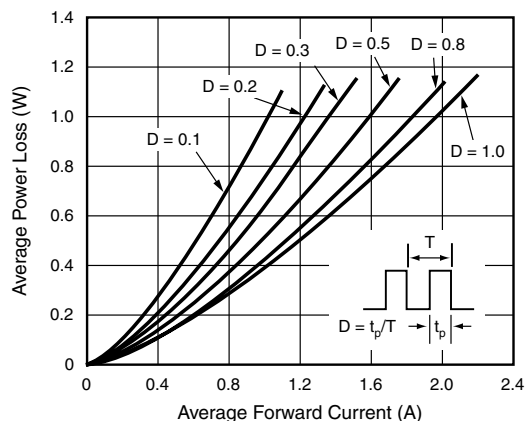


Fig. 2 - Forward Power Loss Characteristics

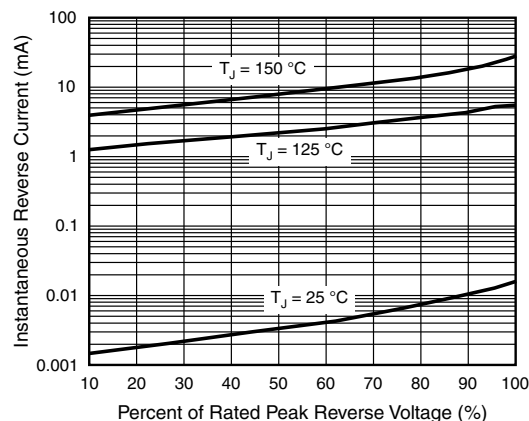


Fig. 4 - Typical Reverse Characteristics

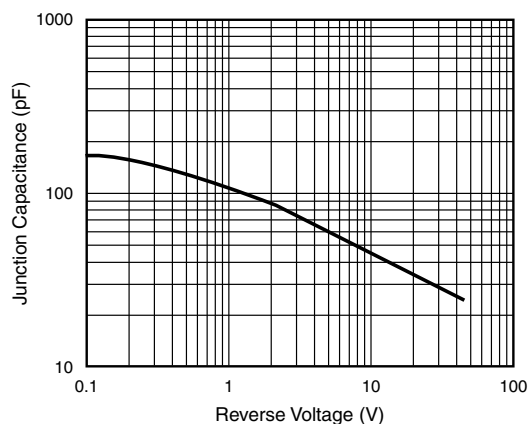


Fig. 5 - Typical Junction Capacitance

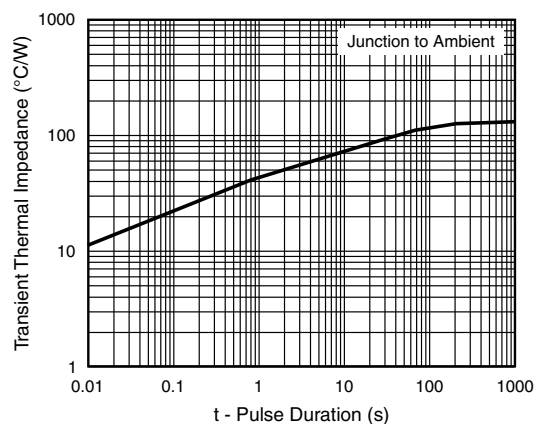
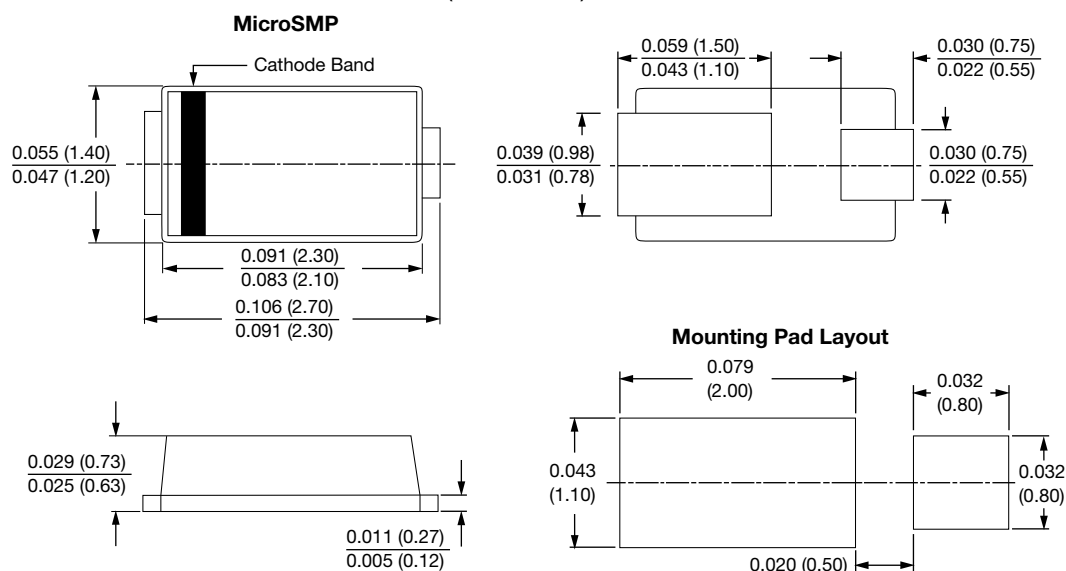


Fig. 6 - Typical Transient Thermal Impedance

**PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



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