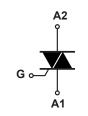
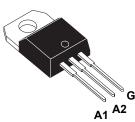


30 A - 800 V TO-220AB insulated H-series Snubberless™ Triac





TO-220AB insulated

Features

- · 30 A high current Triac
- 800 V symmetrical blocking voltage
- 150 °C maximum junction temperature T_i
- · Three triggering quadrants
- High noise immunity, static dV / dt
- Robust dynamic turn-off commutation (dl/dt)c
- ECOPACK®2 compliant component
- Comply with UL1557 insulation: 2.5 kV
 - Reference file: E81734

Applications

- · Home automation Smart AC plug
- Water heater, room heater and coffee machine
- AC Induction and Universal Motor control
- Inrush current limiter in AC DC rectifiers
- · Lighting and automation I/O control
- · General purpose AC line load control

Description

Specifically designed to operate at 800 V and 150 °C, the T3035H-8I Triac provides an enhanced thermal management: this 30 A triac is the right choice for a compact drive of heavy AC loads and enables the heatsink size reduction.

Based on the ST Snubberless™ high temperature technology, it offers higher specified turn off commutation and noise immunity levels up to the Tj max.

The T3035H-8I safely optimizes the control of the hardest universal motors, heaters and inductive loads for industrial control and home appliances.

By using an internal ceramic pad, it provides a recognized voltage insulation, rated at 2500 V_{RMS} .

Snubberless™ is a trademark of STMicroelectronics.



1 Characteristics

Table 1. Absolute maximum ratings (limiting values)

Symbol	Parameter	Value	Unit	
I _{T(RMS)}	RMS on-state current (full sine wave)		30	Α
	Non repetitive surge peak on-state current (full cycle,	t = 16.7 ms	283	Α
I _{TSM}	T _j initial = 25 °C)	t = 20 ms	270	A
I ² t	I ² t value for fusing	t _p = 10 ms	482	A^2s
dl/dt	Critical rate of rise of on-state current, $I_G = 2 \times I_{GT}$, tr \leq 100 ns, f = 100 Hz		100	A/µs
V _{DRM} /V _{RRM}	Repetitive peak off-state voltage	800	V	
V _{DSM} /V _{RSM}	Non Repetitive peak off-state voltage t_p = 10 ms, T_j = 25 °C		900	V
I _{GM}	Peak gate current		4	Α
P _{GM}	t_p = 20 μs, T_j = 150 °C Maximum gate power dissipation		5	W
P _{G(AV)}	Average gate power dissipation T _j = 150 °C		1	W
T _{stg}	Storage temperature range	-40 to +150	°C	
T _j	Operating junction temperature range	-40 to +150	°C	
TL	Maximum lead temperature for soldering during 10 s	260	°C	
V _{INS}	Insulation RMS voltage, 1 minute	2.5	kV	

Table 2. Electrical characteristics (T_j = 25 °C, unless otherwise specified)

Symbol	Test conditions		Quadrants		Value	Unit
la-	$V_D = 12 \text{ V}, R_L = 30 \Omega$		1 - 11 - 111	Min.	5	mA
l _{GT}	$V_D = 12 \text{ V}, R_L = 30 \Omega$		1 - 11 - 111	Max.	35	mA
V _{GT}	$V_D = 12 \text{ V}, R_L = 30 \Omega$		1 - 11 - 111	Max.	1.3	V
V_{GD}	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega$	T _j = 150 °C	1 - 11 - 111	Max.	0.15	V
IL	I _G = 1.2 x I _{GT}		1 - 111	Max.	75	mA
'L			II	Max.	90	mA
I _H ⁽¹⁾	I _T = 500 mA, gate open			Max.	60	mA
dV/dt (1)	V _D = 536 V, gate open		T _j = 150 °C	Min.	1500	V/µs
(dl/dt)c (1)	Without snubber network		T _j = 150 °C	Min.	25	A/ms

^{1.} For both polarities of A2 referenced to A1.

DS12682 - Rev 1 page 2/11



Table 3. Static characteristics

Symbol	Test conditions	Тj		Value	Unit
V _{TM} ⁽¹⁾	I _T = 42 A, t _p = 380 μs	25 °C	Max.	1.55	V
V _{TO} ⁽¹⁾	Threshold voltage	150 °C	Max.	0.83	V
R _D ⁽¹⁾	Dynamic resistance	150 °C	Max.	16	mΩ
	V _{DRM} = V _{RRM} = 800 V	25 °C	Max.	5	μA
I _{DRM} /I _{RRM}	VDRM - VRRM - 000 V	150°C	IVIAX.	8.5	mA
	V _{DRM} = V _{RRM} = 400 V, peak voltage	150 °C	Max.	3.6	mA

^{1.} For both polarities of A2 referenced to A1.

Table 4. Thermal resistance

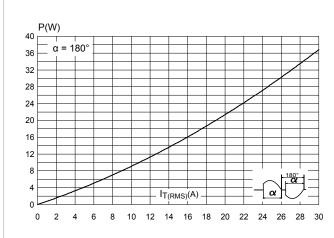
Symbol	Parameter	Value	Unit	
R _{th(j-c)}	Junction to case (AC)	Max.	1.6	°C/W
R _{th(j-a)}	Junction to ambient	Тур.	60	°C/W

DS12682 - Rev 1 page 3/11



1.1 Characteristics (curves)

Figure 1. Maximum power dissipation versus on-state RMS current



I_{T(RMS)}(A)

32

28

24

20

16

12

 $T_C(^{\circ}C)$

75

100

125

150

8

0

25

50

Figure 2. On-state RMS current versus case temperature

Figure 3. On-state RMS current versus ambient temperature (free air convection)

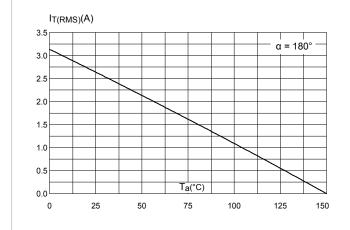
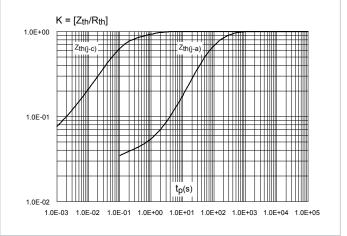


Figure 4. Relative variation of thermal impedance versus pulse duration



DS12682 - Rev 1 page 4/11



Figure 5. Relative variation of gate trigger voltage and current versus junction temperature (typical values)

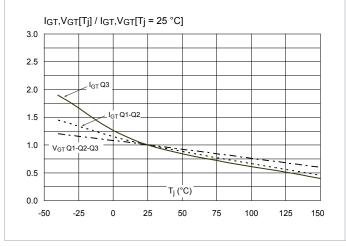


Figure 6. Relative variation of holding current and latching current versus junction temperature (typical values)

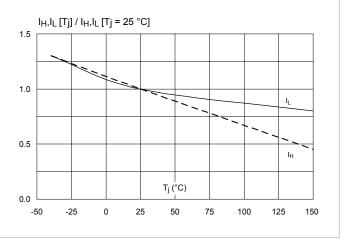


Figure 7. Surge peak on-state current versus number of cycles

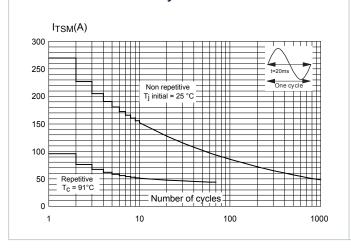


Figure 8. On-state characteristics (maximum values)

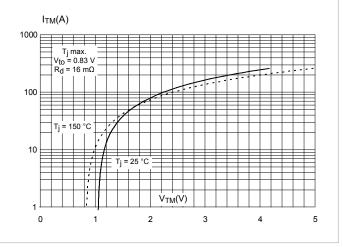


Figure 9. Relative variation of static dV/dt immunity versus junction temperature

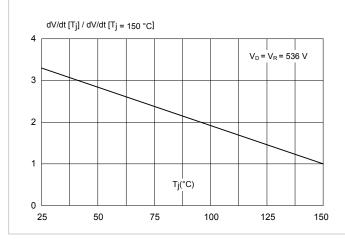
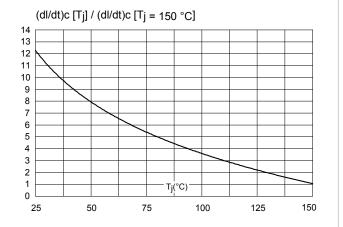


Figure 10. Relative variation of critical rate of decrease of main current versus junction temperature



DS12682 - Rev 1 page 5/11



Figure 11. Relative variation of leakage current versus junction temperature for different values of blocking voltage

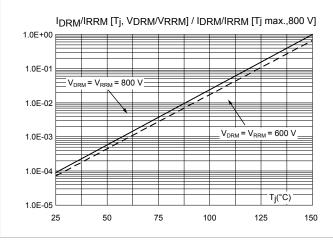
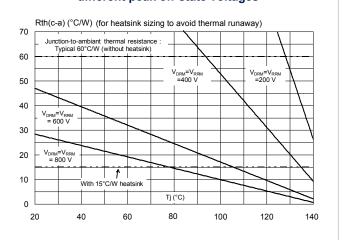


Figure 12. Recommended maximum case-to-ambient thermal resistance versus ambient temperature for different peak off-state voltages



DS12682 - Rev 1 page 6/11



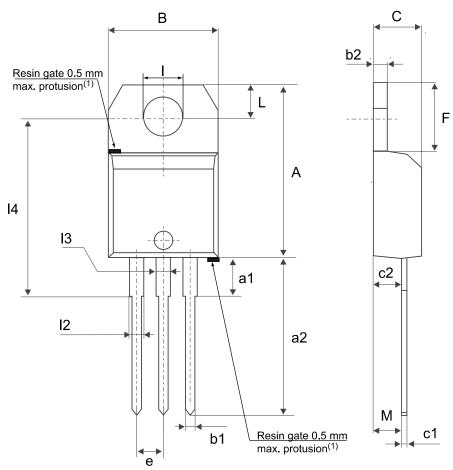
2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

2.1 TO-220AB insulated package information

- Epoxy resin is halogen free and meets UL94 flammability standard, level V0
- Lead-free plating package leads
- Recommended torque: 0.4 to 0.6 N·m

Figure 13. TO-220AB insulated package outline



(1)Resin gate position accepted in one of the two positions or in the symmetrical opposites.

DS12682 - Rev 1 page 7/11



Table 5. TO-220AB insulated package mechanical data

	Dimensions						
Ref.	Millimeters			Inches ⁽¹⁾			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α	15.20		15.90	0.5984		0.6260	
a1		3.75			0.1476		
a2	13.00		14.00	0.5118		0.5512	
В	10.00		10.40	0.3937		0.4094	
b1	0.61		0.88	0.0240		0.0346	
b2	1.23		1.32	0.0484		0.0520	
С	4.40		4.60	0.1732		0.1811	
c1	0.49		0.70	0.0193		0.0276	
c2	2.40		2.72	0.0945		0.1071	
е	2.40		2.70	0.0945		0.1063	
F	6.20		6.60	0.2441		0.2598	
I	3.73		3.88	0.1469		0.1528	
L	2.65		2.95	0.1043		0.1161	
12	1.14		1.70	0.0449		0.0669	
13	1.14		1.70	0.0449		0.0669	
14	15.80	16.40	16.80	0.6220	0.6457	0.6614	
М		2.6			0.1024		

^{1.} Inch dimensions are for reference only.

DS12682 - Rev 1 page 8/11



3 Ordering information

Figure 14. Ordering information scheme

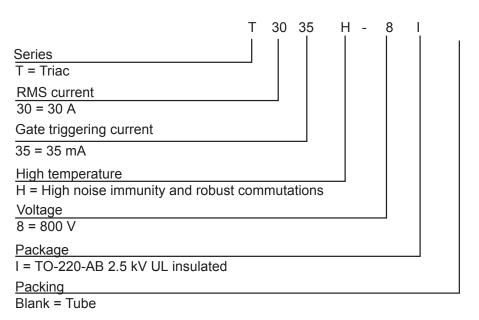


Table 6. Ordering information

Marking Package		Weight	Base qty.	Delivery mode
T3035H-8I	TO-220AB Ins.	2.3 g	50	Tube

DS12682 - Rev 1 page 9/11



Revision history

Table 7. Document revision history

Date	Version	Changes
27-Jul-2018	1	Initial release.

DS12682 - Rev 1 page 10/11



IMPORTANT NOTICE - PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2018 STMicroelectronics - All rights reserved

DS12682 - Rev 1 page 11/11