



# AAP Gen 7 (TO-240AA) Power Modules Thyristor/Diode and Thyristor/Thyristor, 27 A




ADD-A-PAK

PRIMARY CHARACTERISTICS	
$I_{T(AV)}$ or $I_{F(AV)}$	27 A
Type	Modules - thyristor, standard
Package	AAP Gen 7 (TO-240AA)

### MECHANICAL DESCRIPTION

The AAP Gen 7 (TO-240AA), new generation of APP module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

### FEATURES

- High voltage
- Industrial standard package
- UL approved file E78996 
- Low thermal resistance
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



RoHS  
COMPLIANT

### BENEFITS

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- Up to 1600 V
- High surge capability
- Easy mounting on heatsink

### ELECTRICAL DESCRIPTION

These modules are intended for general purpose high voltage applications such as high voltage regulated power supplies, lighting circuits, temperature and motor speed control circuits, UPS and battery charger.

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{T(AV)}$ or $I_{F(AV)}$	85 °C	27	A
$I_{O(RMS)}$	As AC switch	60	
$I_{TSM}$ , $I_{FSM}$	50 Hz	400	
	60 Hz	420	
$I^2t$	50 Hz	800	kA <sup>2</sup> s
	60 Hz	730	
$I^2\sqrt{t}$		8000	kA <sup>2</sup> √s
$V_{RRM}$	Range	400 to 1600	V
$T_{Stg}$		-40 to +125	°C
$T_J$		-40 to +125	°C



**ELECTRICAL SPECIFICATIONS**

VOLTAGE RATINGS					
TYPE NUMBER	VOLTAGE CODE	V <sub>RRM</sub> , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>DRM</sub> , MAXIMUM REPETITIVE PEAK OFF-STATE VOLTAGE, GATE OPEN CIRCUIT V	I <sub>RRM</sub> , I <sub>DRM</sub> AT 125 °C mA
VS-VSK.26	04	400	500	400	15
	06	600	700	600	
	08	800	900	800	
	10	1000	1100	1000	
	12	1200	1300	1200	
	14	1400	1500	1400	
	16	1600	1700	1600	

ON-STATE CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS
Maximum average on-state current (thyristors)	I <sub>T(AV)</sub>	180° conduction, half sine wave,			27	
Maximum average forward current (diodes)	I <sub>F(AV)</sub>	T <sub>C</sub> = 85 °C				
Maximum continuous RMS on-state current, as AC switch	I <sub>O(RMS)</sub>				60	A
Maximum peak, one-cycle non-repetitive on-state or forward current	I <sub>TSM</sub> or I <sub>FSM</sub>	t = 10 ms	No voltage reapplied	Sinusoidal half wave, initial T <sub>J</sub> = T <sub>J</sub> maximum	400	
		t = 8.3 ms			420	
		t = 10 ms	100 % V <sub>RRM</sub> reapplied		335	
		t = 8.3 ms			350	
Maximum I <sup>2</sup> t for fusing	I <sup>2</sup> t	t = 10 ms	No voltage reapplied	Initial T <sub>J</sub> = T <sub>J</sub> maximum	800	A <sup>2</sup> s
		t = 8.3 ms			730	
		t = 10 ms	100 % V <sub>RRM</sub> reapplied		560	
		t = 8.3 ms			510	
Maximum I <sup>2</sup> √t for fusing	I <sup>2</sup> √t (1)	t = 0.1 ms to 10 ms, no voltage reapplied T <sub>J</sub> = T <sub>J</sub> maximum			8000	A <sup>2</sup> √s
Maximum value or threshold voltage	V <sub>T(TO)</sub> (2)	Low level (3)	T <sub>J</sub> = T <sub>J</sub> maximum		0.86	V
		High level (4)			1.09	
Maximum value of on-state slope resistance	r <sub>t</sub> (2)	Low level (3)	T <sub>J</sub> = T <sub>J</sub> maximum		9.58	mΩ
		High level (4)			7.31	
Maximum peak on-state or forward voltage	V <sub>TM</sub>	I <sub>TM</sub> = π × I <sub>T(AV)</sub>	T <sub>J</sub> = 25 °C		1.65	V
	V <sub>FM</sub>	I <sub>FM</sub> = π × I <sub>F(AV)</sub>				
Maximum non-repetitive rate of rise of turned on current	di/dt	T <sub>J</sub> = 25 °C, from 0.67 V <sub>DRM</sub> , I <sub>TM</sub> = π × I <sub>T(AV)</sub> , I <sub>g</sub> = 500 mA, t <sub>r</sub> < 0.5 μs, t <sub>p</sub> > 6 μs			150	A/μs
Maximum holding current	I <sub>H</sub>	T <sub>J</sub> = 25 °C, anode supply = 6 V, resistive load, gate open circuit			200	mA
Maximum latching current	I <sub>L</sub>	T <sub>J</sub> = 25 °C, anode supply = 6 V, resistive load			400	

**Notes**

- (1) I<sup>2</sup>t for time t<sub>x</sub> = I<sup>2</sup>√t × √t<sub>x</sub>
- (2) Average power = V<sub>T(TO)</sub> × I<sub>T(AV)</sub> + r<sub>t</sub> × (I<sub>T(RMS)</sub>)<sup>2</sup>
- (3) 16.7 % × π × I<sub>AV</sub> < I < π × I<sub>AV</sub>
- (4) I > π × I<sub>AV</sub>



TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum peak gate power	$P_{GM}$			10	W
Maximum average gate power	$P_{G(AV)}$			2.5	
Maximum peak gate current	$I_{GM}$			2.5	A
Maximum peak negative gate voltage	$-V_{GM}$			10	V
Maximum gate voltage required to trigger	$V_{GT}$	$T_J = -40\text{ }^\circ\text{C}$	Anode supply = 6 V resistive load	4.0	
		$T_J = 25\text{ }^\circ\text{C}$		2.5	
		$T_J = 125\text{ }^\circ\text{C}$		1.7	
Maximum gate current required to trigger	$I_{GT}$	$T_J = -40\text{ }^\circ\text{C}$	Anode supply = 6 V resistive load	270	mA
		$T_J = 25\text{ }^\circ\text{C}$		150	
		$T_J = 125\text{ }^\circ\text{C}$		80	
Maximum gate voltage that will not trigger	$V_{GD}$	$T_J = 125\text{ }^\circ\text{C}$ , rated $V_{DRM}$ applied		0.25	V
Maximum gate current that will not trigger	$I_{GD}$	$T_J = 125\text{ }^\circ\text{C}$ , rated $V_{DRM}$ applied		6	mA

BLOCKING					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum peak reverse and off-state leakage current at $V_{RRM}$ , $V_{DRM}$	$I_{RRM}$ , $I_{DRM}$	$T_J = 125\text{ }^\circ\text{C}$ , gate open circuit		15	mA
Maximum RMS insulation voltage	$V_{INS}$	50 Hz		3000 (1 min) 3600 (1 s)	V
Maximum critical rate of rise of off-state voltage	$dV/dt$	$T_J = 125\text{ }^\circ\text{C}$ , linear to 0.67 $V_{DRM}$		1000	V/ $\mu$ s

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Junction operating and storage temperature range	$T_J$ , $T_{Stg}$			-40 to +125	$^\circ\text{C}$
Maximum internal thermal resistance, junction to case per leg	$R_{thJC}$	DC operation		0.76	$^\circ\text{C/W}$
Typical thermal resistance, case to heatsink per module	$R_{thCS}$	Mounting surface flat, smooth and greased		0.1	
Mounting torque $\pm 10\%$	to heatsink	A mounting compound is recommended and the torque should be rechecked after a period of 3 hours to allow for the spread of the compound.		4	Nm
	busbar			3	
Approximate weight				75	g
				2.7	oz.
Case style			JEDEC <sup>®</sup>	AAP Gen 7 (TO-240AA)	

$\Delta R$ CONDUCTION PER JUNCTION											
DEVICES	SINE HALF WAVE CONDUCTION					RECTANGULAR WAVE CONDUCTION					UNITS
	180 $^\circ$	120 $^\circ$	90 $^\circ$	60 $^\circ$	30 $^\circ$	180 $^\circ$	120 $^\circ$	90 $^\circ$	60 $^\circ$	30 $^\circ$	
VSK.26..	0.212	0.258	0.330	0.466	0.72	0.166	0.276	0.357	0.482	0.726	$^\circ\text{C/W}$

**Note**

- Table shows the increment of thermal resistance  $R_{thJC}$  when devices operate at different conduction angles than DC

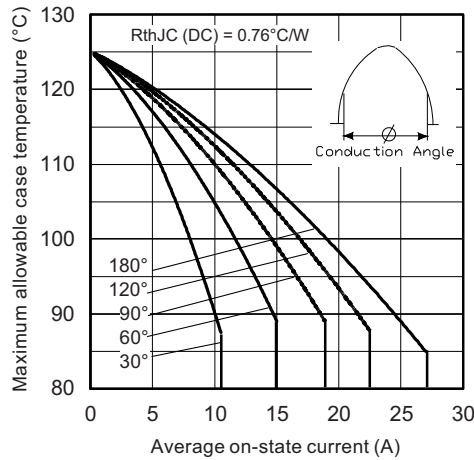


Fig. 1 - Current Ratings Characteristics



Fig. 4 - On-State Power Loss Characteristics



Fig. 2 - Current Ratings Characteristics

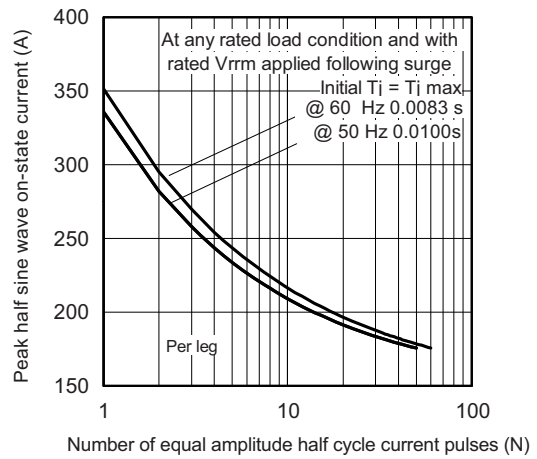


Fig. 5 - Maximum Non-Repetitive Surge Current



Fig. 3 - On-State Power Loss Characteristics



Fig. 6 - Maximum Non-Repetitive Surge Current



Fig. 7 - On-State Power Loss Characteristics



Fig. 8 - On-State Power Loss Characteristics



Fig. 9 - On-State Power Loss Characteristics



Fig. 10 - On-State Voltage Drop Characteristics



Fig. 11 - Thermal Impedance  $Z_{thJC}$  Characteristics



Fig. 12 - Gate Characteristics



**ORDERING INFORMATION TABLE**

Device code	<b>VS-VS</b>	<b>K</b>	<b>T</b>	<b>26</b>	<b>/</b>	<b>16</b>
	①	②	③	④		⑤
	<b>1</b>	<b>2</b>	<b>3</b>	<b>4</b>		<b>5</b>
	-	-	-	-		-
	Vishay Semiconductors product					
	Module type					
	Circuit configuration (see Circuit Configuration table)					
	Current code (26 A)					
	Voltage code (see Voltage Ratings table)					

**Note**

- To order the optional hardware go to [www.vishay.com/doc?95172](http://www.vishay.com/doc?95172)

<b>CIRCUIT CONFIGURATION</b>		
<b>CIRCUIT DESCRIPTION</b>	<b>CIRCUIT CONFIGURATION CODE</b>	<b>CIRCUIT DRAWING</b>
Two SCRs doubler circuit	T	<p>VSKT</p>
SCR/diode doubler circuit, positive control	H	<p>VSKH</p>
SCR/diode doubler circuit, negative control	L	<p>VSKL</p>



CIRCUIT CONFIGURATION		
CIRCUIT DESCRIPTION	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
SCR/diode common anodes	N	<p>VS-KN</p> <p>(1) (2) (3) G1 K1 (4) (5)</p>
Two SCRs common cathodes	U	<p>VS-KU</p> <p>(1) (2) (3) G1 K1 K2 G2 (4) (5) (7) (6)</p>
Two SCRs common anodes	V	<p>VS-KV</p> <p>(1) (2) (3) G1 K1 K2 G2 (4) (5) (7) (6)</p>

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?95368">www.vishay.com/doc?95368</a>



## ADD-A-PAK Generation VII - Thyristor

**DIMENSIONS** in millimeters (inches)





## Disclaimer

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