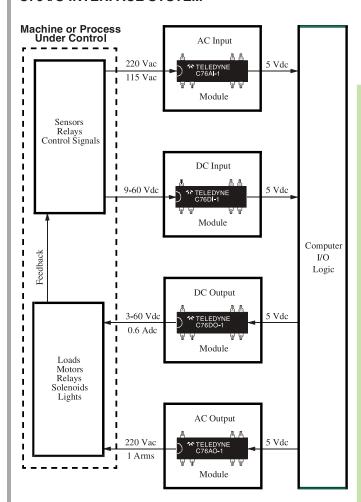


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#### **C76 I/O INTERFACE SYSTEM**



| Part<br>Number | Туре      | Characteristics                                    |
|----------------|-----------|--|
| C76AO-1        | AC Output | 3.8 to 16 Vdc Input<br>5 to 250 Vrms, 1 A Output   |
| C76AI-1        | AC Input  | 90 to 250 Vrms Input<br>0 to 60 Vdc, 100 mA Output |
| C76DO-1        | DC Output | 3.8 to 16 Vdc Input<br>3 to 60 Vdc, 0.6 A Output   |
| C76DI-1        | DC Input  | 9 to 60 Vdc Input<br>0 to 60 Vdc, 100 mA Output    |



#### **APPLICATIONS**

- Robotics
- Programmable Controllers
- Process Control
- · Machine Tool Control
- Energy Management
- Automatic Test Equipment

#### FEATURES/BENEFITS

- Input Enable Function: For computer timing function control.
- Floating Outputs: Eliminates ground loops and signal noise. Protects computer I/O and logic circuits
- Low Off-State Leakage: High off-state impedance
- Switches/Controls High Voltages: To 250 Vrms Switches/Controls High Currents: To 1.0 Arms
- High Noise Immunity: Control signals isolated from switching noise
- High Dielectric Strength: Safety and protection of control and signal level circuits

#### **DESCRIPTION**

The Series C76 solid-state computer input/output modules are designed expressly for application in computerized control systems where reliable noise-free interface of switching is required to isolate computer logic elements from high conducted noise encountered in industrial environments. Sensitive logic circuitry is kept noise-free by means of optical isolation between logic and power lines.

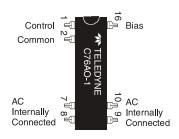
Output modules allow either TTL or CMOS level signals to control the switching of power to high voltage and high current loads. Hysteresis at the input significantly increases the noise margin when used in the CMOS input mode, preventing false triggering in noisy environments

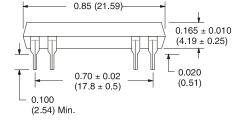
Input modules convert the presence or absence of load level voltages from pressure, flow, temperature and other transducers, limit switches, solenoids or relays to "clean" low level logic signals for computer input. An ENABLE function maintains the module's output in an "open" state until the ENABLE terminal is brought up to the bias supply level.

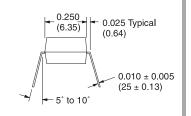
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#### PIN CONFIGURATIONS

#### MECHANICAL SPECIFICATION



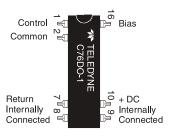




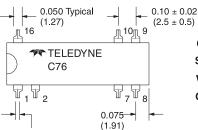
# C76AO-1

# **DIMENSIONS ARE SHOWN IN INCHES (MILLIMETERS)**

Tolerances (unless otherwise specified) ± 0.015 (0.38)



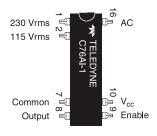
C76DO-1



Operating Temperature Range: -40°C to 85°C Storage Temperature Range: -40°C to 100°C

Weight: 2.0 gm. maximum

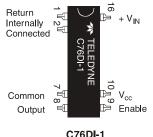
Case: Special 16 pin dual In line, filled epoxy.



C76AI-1

# TRUTH TABLE FOR ENABLE FUNCTION

| $V_{IN}^{1}$ | ENABLE <sup>2</sup> | OUTPUT <sup>3</sup> |
|--------------|---------------------|---------------------|
| 0            | 0                   | 0                   |
| 1            | 0                   | 0                   |
| 0            | 1                   | 0                   |
| 1            | 1                   | 1                   |



(TOP VIEW)

#### 1. For C76AI-1:

 $0.020 \pm 0.005$ 

 $(0.51 \pm 0.13)$ 

When using 115 Vrms input,  $V_{IN}$  is a "1" when the voltage is  $\geq$  90 Vrms When using 220 Vrms input,  $V_{IN}$  is a "1" when the voltage is  $\geq$  180 Vrms

#### 2. For C76AI-1 and C76DI-1:

The Enable input is a "1" when the Enable voltage  $V_E$  is  $\geq 2.0$  Vdc. The Enable input is a "0" when the Enable voltage  $V_E$  is  $\leq 0.4$  Vdc.

#### 3. A "0" represents an open output switch.

A "1" represents a closed output switch.

### NOTE:

When used in the CMOS input configuration, the C76AO-1 and the C76DO-1 provide inversion. When the input voltage is 0.5 Vdc or less the output will be guaranteed "On". When the input voltage is 2.8 Vdc or more the output will be guaranteed "Off".

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# ELECTRICAL SPECIFICATIONS (25°C UNLESS OTHERWISE SPECIFIED)

#### TTL INPUT (BIAS) SPECIFICATIONS (See Figure 4)

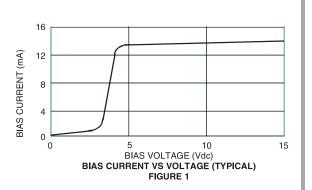
| Parameter                       | Min | Max   | Units |
|---------------------------------|-----|-------|-------|
| Bias Voltage Range (See Fig. 1) | 3.8 | 16.0  | Vdc   |
| Bias Current @ 5 Vdc            |     | 16.0  | mA    |
| Must Turn-On Voltage            | 3.8 |       | Vdc   |
| Must Turn-Off Voltage           |     | 1.5   | Vdc   |
| Reverse Voltage Protection      |     | -32.0 | Vdc   |

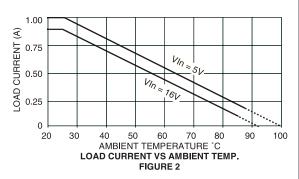
#### CMOS INPUT (CONTROL) SPECIFICATIONS (See Figure 4)

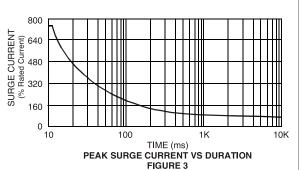
| Parameter                | Min | Max  | Units |
|--------------------------|-----|------|-------|
| Control Voltage Range    |     | 16.0 | Vdc   |
| Control Current at 5 Vdc |     | 250  | Adc   |
| Must Turn-On Voltage     | 0.5 |      | Vdc   |
| Must Turn-Off Voltage    |     | 2.8  | Vdc   |
| Bias Voltage Range       | 3.8 | 16   | Vdc   |

# **OUTPUT (LOAD) SPECIFICATIONS**

| Parameter                              | Min             | Max    | Units |
|--|-----------------|--------|-------|
| Load Voltage Range                     | 5.0             | 250    | Vrms  |
| Output Current Rating (See Fig. 2)     | 0.01            | 1.0    | Arms  |
| Frequency Range                        | 40              | 80     | Hz    |
| Over Voltage Rating (25°C)             |                 | ±500   | Vpeak |
| On-State Voltage Drop @ 1 Arms         |                 | 1.5    | Vrms  |
| Zero Voltage Turn-On                   |                 | ±17.0  | Vpeak |
| Surge Current Rating (See Fig. 3)16    | msec, 25        | °C 8.0 | Apeak |
| Turn-On Time                           |                 | 1/2    | Cycle |
| Turn-Off Time                          |                 | 1      | Cycle |
| Leakage Current (Off-State) @ 230 V    | /rms            | 1.0    | mA    |
| Off-State dV/dt w/o Snubber            | 200             |        | V/μs  |
| Isolation (Input to Output)            | 10 <sup>9</sup> |        | Ohms  |
| Dielectric Strength (Input to Output)  | 3750            |        | Vac   |
| Capacitance (Input to Output)          |                 | 5.0    | pF    |
| Junction Temperature (T <sub>J</sub> ) |                 | 150    | °C    |

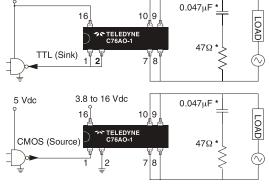






# TYPICAL INTERFACE TO TTL AND CMOS LOGIC

5 Vdc



\* RC snubber network is optional for protecting switching system from high voltage transients

FIGURE 4

# Series C76 / C76Al-1 Input Modules

 $V_{IN} = 240 \text{ Vrms } (- - -)$ 

275

150

100

150

INPUT CURRENT (mA)

3 2

0

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#### INPUT (CONTROL) SPECIFICATIONS **Parameter** Max Units Min V<sub>IN</sub>=115 Vrms 90 135 Vrms Control Voltage Range V<sub>IN</sub> = 220 Vrms <sub>180</sub> 250 Vrms 3.5 V<sub>IN</sub> = 115 Vrms mΑ Input Current V<sub>IN</sub> = 220 Vrms 3.0 mΑ 115 Vrms; V<sub>IN</sub> Vrms Must Turn-Off Voltage 230 Vrms; V<sub>IN</sub> 50 Vrms ±600 Vpeak Input Transient (≤ 1ms) INPUT (ENABLE) SPECIFICATIONS **Parameter** Min Max Units Enable Voltage 2.0 15.0 Vdc **Enable Current** 10.0 μΑ **OUTPUT SPECIFICATIONS**

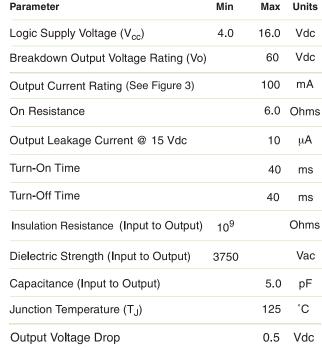
# FIGURE 1 VOLTAGE DROP (Vdc) .20 = 4 Vdc .15 $V_{cc} = 16 \text{ Vdc}$ .05

75 V<sub>IN</sub> = 120 Vrms (...

INPUT CURRENT VS INPUT VOLTAGE (TYPICAL)

LOAD CURRENT (mA) LOAD CURRENT VS OUTPUT VOLTAGE DROP (TYPICAL) FIGURE 2

60



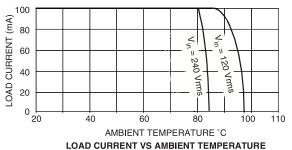


FIGURE 3

### I/O PORT $V_{cc}$ \* NOTE ENABLE ≤ V<sub>cc</sub> ՝ 0.1սք V<sub>IN</sub> = 220 Vrms V<sub>o</sub>\* \* OUTPUT TELEDYNE C76Al-1 $V_{IN} = 115 \text{ Vrms}$ STATUS RETURN

TYPICAL INTERFACE TO I/O PORT

 $<sup>^{\</sup>star}$  NOTE: 0.1  $\mu f$  decoupling capacitor is recommended  $^{\star\star}$  V  $_{o}$  maybe supplied by V  $_{cc}$ 

# Series C76 / C76D1-1 Output Modules

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# ELECTRICAL SPECIFICATIONS (25°C UNLESS OTHERWISE SPECIFIED)

## INPUT (CONTROL) SPECIFICATIONS

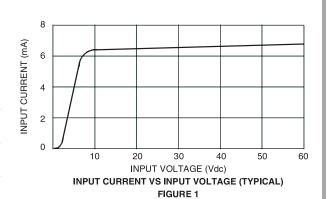
| Parameter                | Min | Max  | Units |
|--------------------------|-----|------|-------|
| Control Voltage Range    | 9.0 | 60.0 | Vdc   |
| Control Current @ 55 Vdc |     | 10.0 | mA    |
| Must Turn-On Voltage     | 9.0 |      | Vdc   |
| Must Turn-Off Voltage    |     | 1.5  | Vdc   |
| Input Transient (≤ 1ms)  |     | 100  | Vdc   |

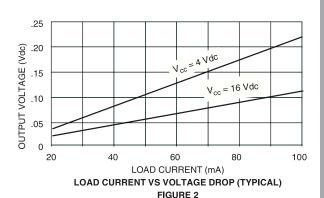
## INPUT (ENABLE) SPECIFICATIONS

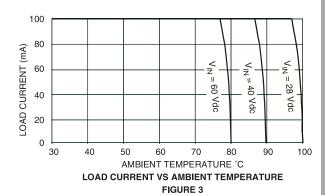
| Parameter      | Min | Max  | Units |
|----------------|-----|------|-------|
| Enable Voltage | 2.0 | 15.0 | Vdc   |
| Enable Current |     | 10.0 | μΑ    |

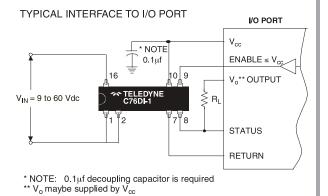
#### **OUTPUT (LOAD) SPECIFICATIONS**

| Parameter                               | Min             | Max  | Units |
|---|-----------------|------|-------|
| Logic Supply Voltage (V <sub>cc</sub> ) | 4.0             | 16.0 | Vdc   |
| Output Breakdown Voltage Rating (V      | (o)             | 60   | Vdc   |
| Output Current Rating                   |                 | 100  | mA    |
| Output Voltage Drop                     |                 | 0.5  | Vdc   |
| Leakage Current (Off-State) @ 15 Vo     | dc              | 10.0 | μΑ    |
| Turn-On Time                            |                 | 3.0  | ms    |
| Turn-Off Time                           |                 | 3.0  | ms    |
| Isolation (Input to Output)             | 10 <sup>9</sup> |      | Ohms  |
| Dielectric Strength (Input to Output)   | 3750            |      | Vac   |
| Capacitance (Input to Output)           |                 | 5.0  | pF    |
| Junction Temperature (T <sub>J</sub> )  |                 | 125  | °C    |









# Series C76 / C76DO-1 Output Modules

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#### **ELECTRICAL SPECIFICATIONS**

(25°C UNLESS OTHERWISE SPECIFIED)

TTL INPUT (2 TERMINAL) SPECIFICATIONS (See Figure 4)

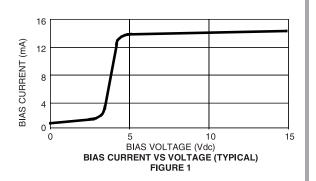
| Parameter                  | Min | Max   | Units |
|----------------------------|-----|-------|-------|
| Control Voltage Range      | 3.8 | 16.0  | Vdc   |
| Control Current @ 5.0 Vdc  |     | 15.0  | mA    |
| Must Turn-On Voltage       | 3.8 |       | Vdc   |
| Must Turn-Off Voltage      |     | 1.5   | Vdc   |
| Reverse Voltage Protection |     | -32.0 | Vdc   |

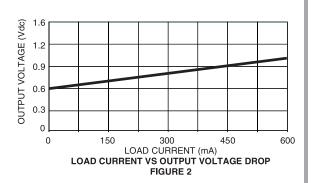
## CMOS INPUT (3 TERMINAL) SPECIFICATIONS (See Figure 4)

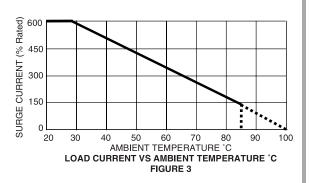
| Parameter               | Min | Max  | Units |
|-------------------------|-----|------|-------|
| Control Voltage         |     | 16.0 | Vdc   |
| Control Current @ 5 Vdc |     | 250  | μΑ    |
| Must Turn-On Voltage    | 0.5 |      | Vdc   |
| Must Turn-Off Voltage   |     | 2.8  | Vdc   |
| Bias Supply Range       | 3.8 | 16   | Vdc   |
| Bias Current            |     | 15   | mAdc  |

### **OUTPUT (LOAD) SPECIFICATIONS**

| Min             | Max | Units   |
|-----------------|-----|---|
| 3.0             | 60  | Vdc   |
|                 | 600 | mAdc  |
|                 | 1.5 | Vdc   |
|                 | 50  | μS  |
|                 | 180 | μs  |
|                 | 20  | μΑ  |
| 10 <sup>9</sup> |     | Ohms  |
| 3750            |     | Vac   |
|                 | 5.0 | pF  |
|                 | 150 | °C  |
|                 | 3.0 | 3.0 60 600 1.5 50 180 20 10 <sup>9</sup> 3750 5.0 |







#### TYPICAL INTERFACE TO TTL AND CMOS LOGIC

