

# **Isolation Amplifier with Video Driver**

SOT-23-5

#### **■FEATURES**

Operating Voltage
 Operating Temperature
 Common Mode Rejection Ratio
 75Ω Driver
 DC Coupling, AC Coupling
 Voltage Gain
 OdBtyp.
 Frequency Characteristics
 4.5 to 5.5V
 -40 to 105°C
 -55dBtyp.
 -55dBtyp.
 OdBtyp.
 OdB at 10MHz

#### **■GENERAL DESCRIPTION**

NJM41005 is the isolation amplifier that has been developed in the video signal applications. It can remove the common-mode noise of the signal by the isolation amplifier. In addition, it has a built-in  $75\Omega$  driver, well suited to the interface of CAR AV.

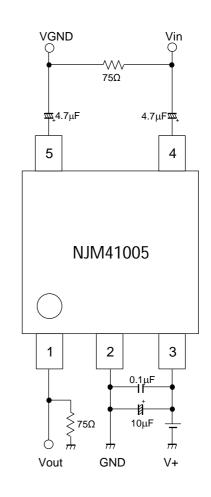
#### **■**APPLICATION

- Car Navigation
- Vehicle Camera ECU

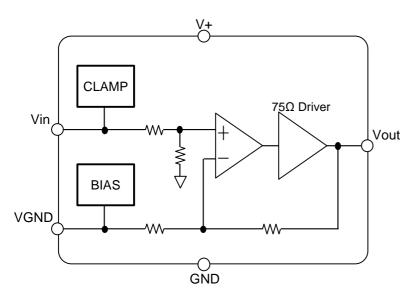
Bipolar Technology

Package Outline

### **■APPLICATION CIRCUIT (DC Coupling)**



#### **■EQUIVALENT CIRCUIT·BLOCK DIAGRAM**

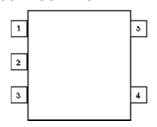




### **■**Isolation amplifier series

Channel	Part No.
1ch	NJM2505A
3ch	NJM41033

#### **■PIN CONFIGURATION**



PIN NO.	SYMBOL	DESCRIPTION
1	Vout Video Signal Output Terminal	
2	GND	GND Terminal
3	V+	Power Supply Terminal
4 Vin Video Signal Input Terminal		Video Signal Input Terminal
5 VGND GND Input Terminal (from sourse sid		GND Input Terminal (from sourse side)

#### **■MARK INFORMATION**



### **■ORDERING INFORMATION**

PART NUMBER	PACKAGE OUTLINE	RoHS	HALOGEN- FREE	TERMINAL FINISH	MARKING	WEIGHT (mg)	MOQ(pcs)
NJM41005F-T	SOT-23-5	YES	YES	Sn-Bi	AK2	15.0	3,000



#### **■ABSOLUTE MAXIMUM RATINGS**

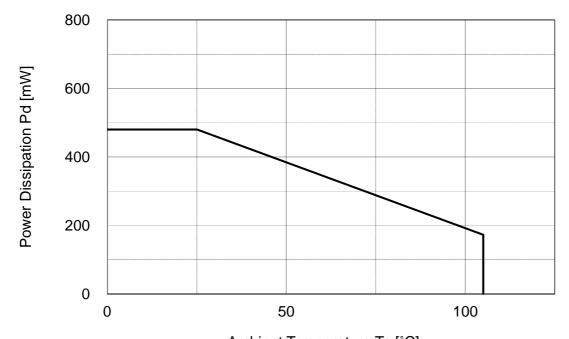
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V+	7	V
Power Dissipation (Ta=25°C) <sup>(4)</sup>	$P_{D}$	480 *1	mW
Operating Temperature Range	T <sub>opr</sub>	-40 to 105	°C
Storage Temperature Range	T <sub>stg</sub>	-40 to 150	°C

<sup>1)</sup> At on a board of EIA/JEDEC specification. (114.3 x 76.2 x 1.6mm 2 layers, FR-4)

#### **■RECOMMENDED OPERATING CONDITIONS**

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V+	4.5 to 5.5	V

#### **■POWER DISSIPATION vs. AMBIENT TEMPERATURE**



Ambient Temperature Ta [°C]



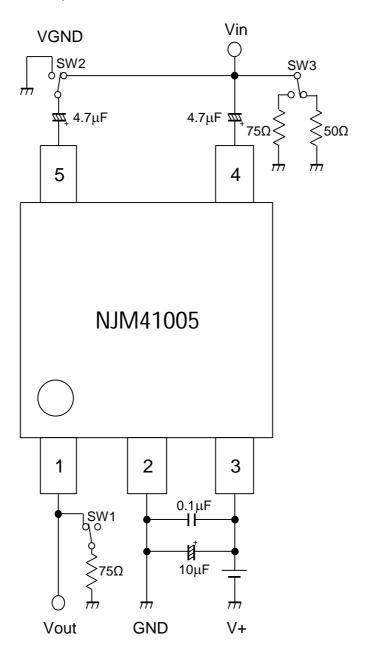
### ■ELECTRICAL CHARACTERISTICS (Ta=25°C, V<sup>+</sup>=5.0V, RL=75Ω, unless otherwise specified)

SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
	No Signal, RL=OPEN	ı	10.0	14.0		
$I_{CC}$	No Signal, RL=OPEN ,		-	14.0	mA	
	Ta=-40 to 105°C					
	f=100kHz, Input Sine Signal,	2.2	2.4	-	Vp-p	
\/om	THD=1%	2.2				
VOITI	f=100kHz, Input Sine Signal,	2.2	ı	-		
	THD=1%, Ta=-40 to 105°C	2.2				
	Vin=100kHz, 1.0Vp-p,	-0.5	0	0.5	· dB	
Gv	Input Sine Signal					
ÖV	Vin=100kHz, 1.0Vp-p,	-0.5	_	0.5		
	Input Sine Signal, Ta=-40 to 105°C	_	0.5			
Gf10M	Vin=10MHz/1MHz, 1.0Vp-p,	-	0	-	dB	
	input Sine Signal					
CMRR	Vin=20kHz,1Vp-p	-	-55	-	dB	
DG	Vin=1 0Vn-n 10sten Video Signal	_	0.1	_	%	
	1 1 1	_		_	deg	
	I <sub>cc</sub> Vom  Gv  Gf10M	No Signal, RL=OPEN     No Signal, RL=OPEN     Ta=-40 to 105°C     f=100kHz, Input Sine Signal, THD=1%     f=100kHz, Input Sine Signal, THD=1%, Ta=-40 to 105°C     Vin=100kHz, 1.0Vp-p, Input Sine Signal     Vin=100kHz, 1.0Vp-p, Input Sine Signal     Vin=100kHz, 1.0Vp-p, Input Sine Signal, Ta=-40 to 105°C     Gf10M	No Signal, RL=OPEN   -	No Signal, RL=OPEN   -   10.0     No Signal, RL=OPEN   -       Ta=-40 to 105°C   -     Ta=-40 to 105°C       ThD=1%       f=100kHz, Input Sine Signal, ThD=1%, Ta=-40 to 105°C       ThD=1%       ThD=1%       ThD=1%       ThD=1%   Ta=-40 to 105°C       Vin=100kHz, 1.0Vp-p,	No Signal, RL=OPEN   -   10.0   14.0     No Signal, RL=OPEN   -   10.0   14.0     No Signal, RL=OPEN   -   -   14.0     Ta=-40 to 105°C   f=100kHz, Input Sine Signal, THD=1%   f=100kHz, Input Sine Signal, THD=1%   ThD=1%   7   7     ThD=1%   ThD=1%	



#### **■TEST CIRCUIT**

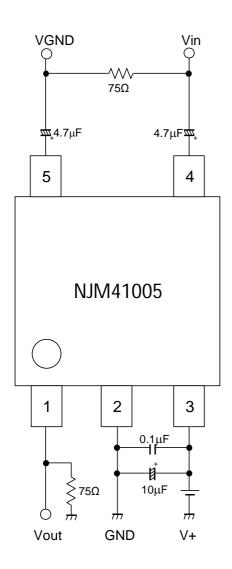
(When measuring CMR, SW2 of VGND is connected to  $\,$  .SW2 is connected to  $\,$  when measuring other electrical characteristics. When Icc is measured, SW1 of Vout is connected to  $\,$  and 75  $\Omega$  is disconnected and opened. SW1 is connected to  $\,$  in other special features.)

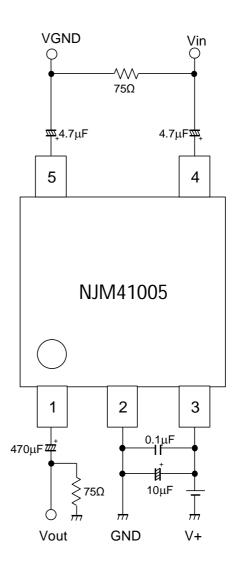




### ■APPLICATION CIRCUIT 1(Output DC Coupling)

### ■APPLICATION CIRCUIT 2(Output AC Coupling)





#### **■**APPLICATION

When using an external ESD protection resistor at the video input terminal, please connect the same resistance at the VIN terminal (pin 4) and the VGND terminal (pin 5). Please verify with resistance value about 100  $\Omega$ .

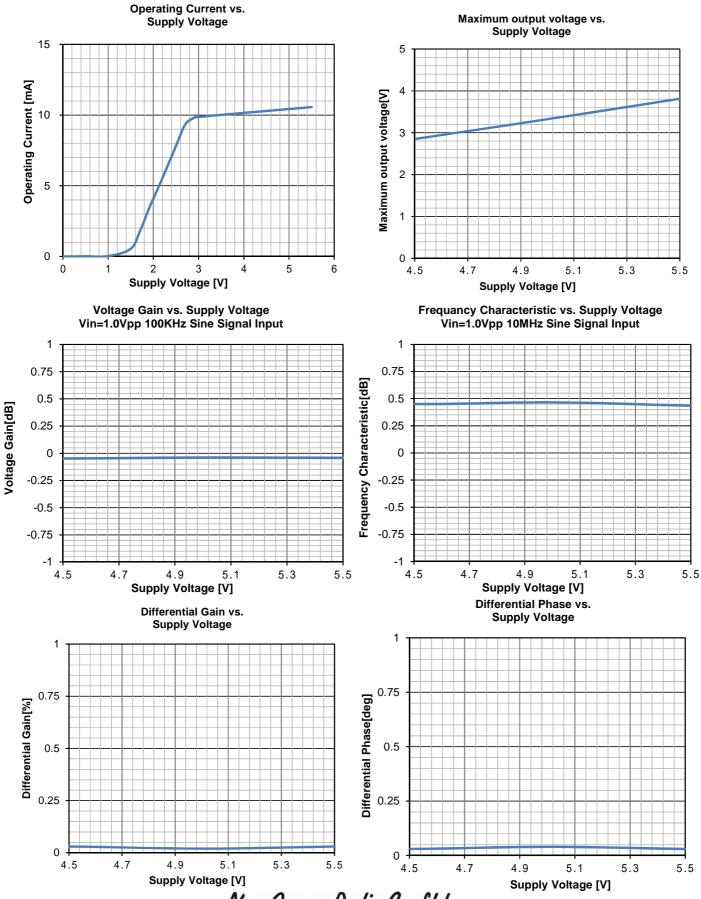


#### **■ EQUIVALENT CIRCUIT**

Pin. No.	Symbol	Function	Inside Equivalent Circuit	Voltage
1	Vout	Video Signal Output	VOUTE VOUTE A STATE OF THE STAT	0.5V
2	GND	GND	-	-
3	V+	Power Supply	-	-
4	Vin	Video Signal Input	VIN	1.5V
5	VGND	GND Input From sourse side	VGND VGND	2.5V

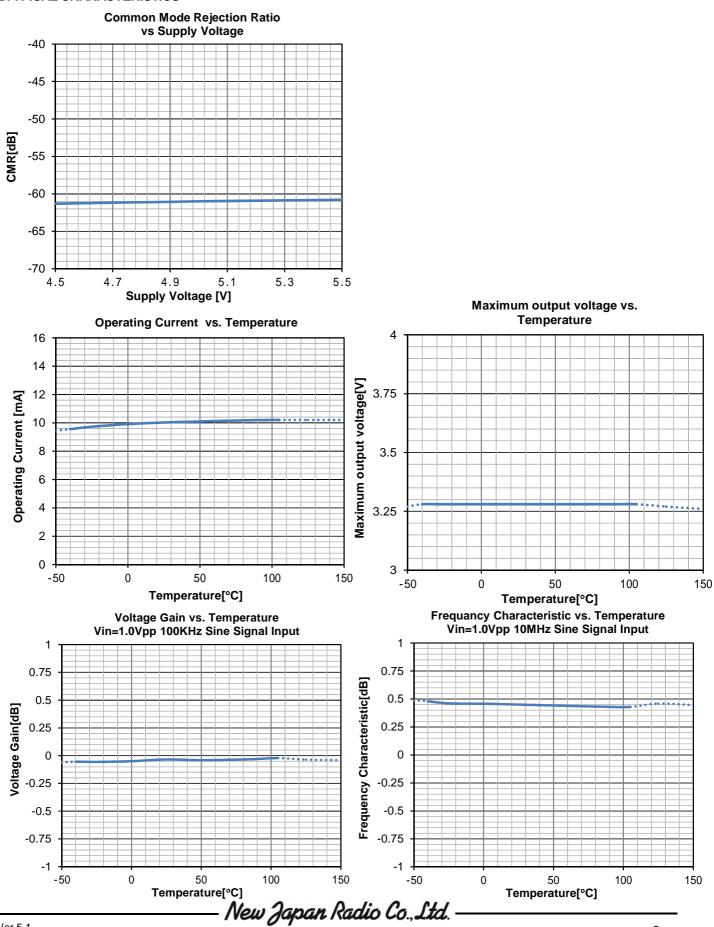


#### **■TYPICAL CHARACTERISTICS**



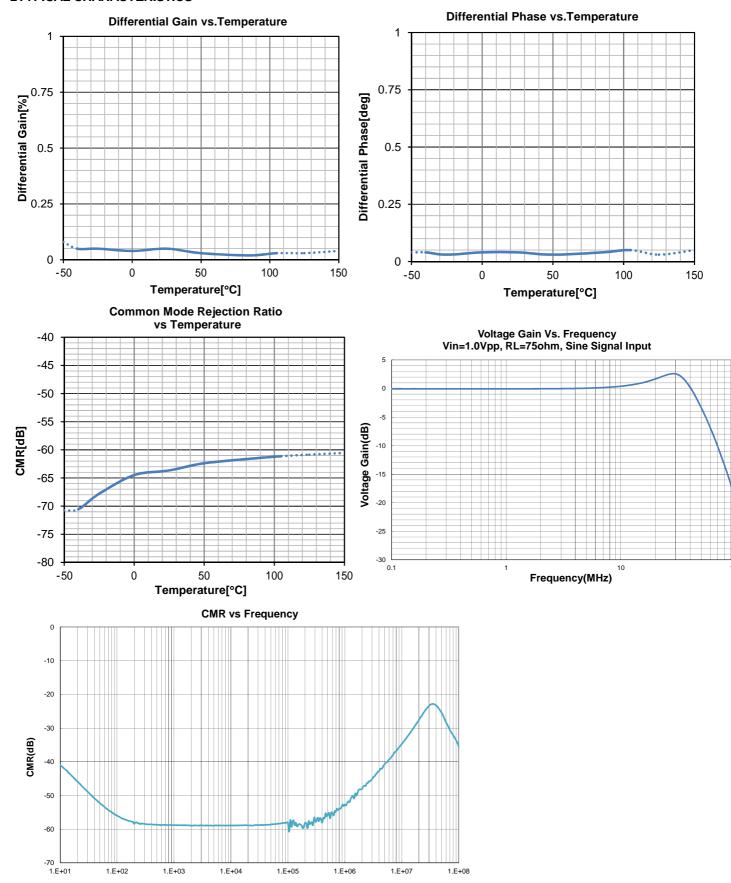


#### **■TYPICAL CHARACTERISTICS**





#### **TYPICAL CHARACTERISTICS**

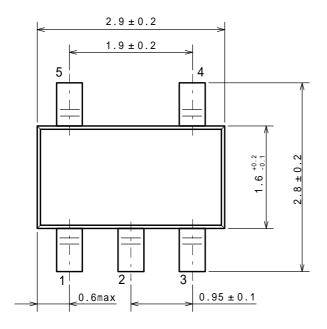


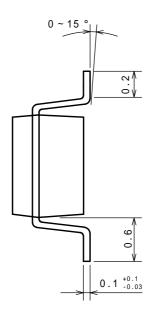


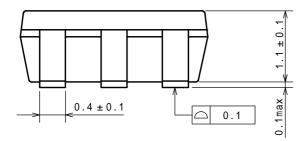
# **SOT-23-5**

Unit: mm

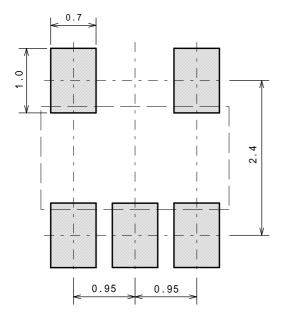
### **PACKAGE DIMENSIONS**







### **EXAMPLE OF SOLDER PADS DIMENSIONS**

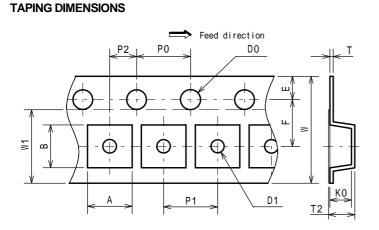




### **SOT-23-5**

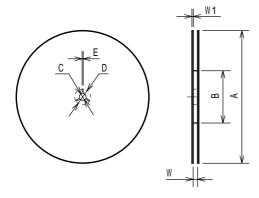
Unit: mm

# PACKING SPEC



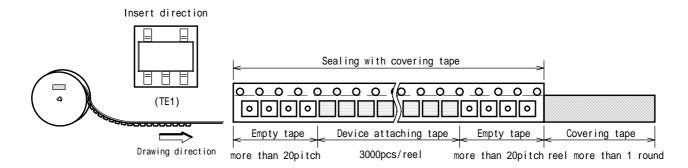
SYMBOL	DIMENSION	REMARKS
Α	$3.3 \pm 0.1$	BOTTOM DIMENSION
В	$3.2 \pm 0.1$	BOTTOM DIMENSION
DO	1.55	
D1	1.05	
Е	$1.75 \pm 0.1$	
F	$3.5 \pm 0.05$	
P0	$4.0 \pm 0.1$	
P1	$4.0 \pm 0.1$	
P2	$2.0 \pm 0.05$	
Τ	$0.25 \pm 0.05$	
T2	1.82	
K0	$1.5 \pm 0.1$	
W	$8.0 \pm 0.3$	
W1	5.5	THICKNESS 0.1MAX

#### **REEL DIMENSIONS**

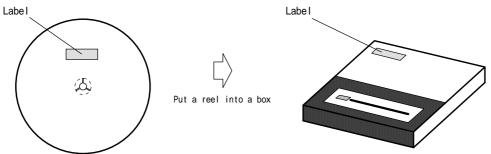


SYMBOL	DIMENSION
Α	180 ± 1
В	60 ± 1
С	13 ± 0.2
D	21 ± 0.8
E	2 ± 0.5
W	$9 \pm 0.5$
W1	1.2±0.2

#### **TAPING STATE**





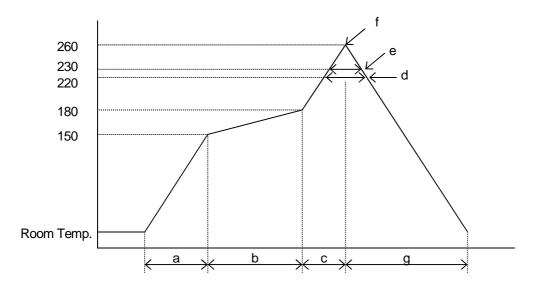




#### INFRARED REFLOW SOLDERING METHOD

EAE-D1006-000-02

\* Recommended reflow soldering procedure



a:Temperature ramping rate :1 to 4 /s b: Pre-heating temperature : 150 to 180 : 60 to 120s time c:Temperature ramp rate :1 to 4 /s : Shorter than 60s d:220 or higher time e:230 or higher time : Shorter than 40s : Lower than 260 f:Peak temperature :1 to 6 /s g:Temperature ramping rate

The temperature indicates at the surface of mold package.



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