



DMN2014LHAB

DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)} max	Ι _D T _A = +25°C
20V	13mΩ @ V _{GS} = 4.5V	9.0A
	14mΩ @ V _{GS} = 4.0V	8.7A
	17mΩ @ V _{GS} = 3.1V	8.0A
	18mΩ @ V _{GS} = 2.5V	6.7A
	28mΩ @ V _{GS} = 1.8V	6.3A

Description

This new generation MOSFET has been designed to minimize the onstate resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

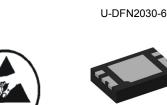
- **Power Management Functions**
- **Battery Pack**
- Load Switch

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

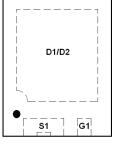
Mechanical Data

- Case: U-DFN2030-6
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.012 grams (approximate)



ESD PROTECTED TO 2kV

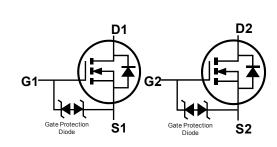
Bottom View



G2

S2

Top View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2014LHAB-7	U-DFN2030-6	3,000 / Tape & Reel
DMN2014LHAB-13	U-DFN2030-6	10,000 / Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

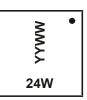
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

Notes:



24W = Product Type Marking Code YYWW = Date Code Marking YY = Last digit of year (ex: 14 for 2014) WW = Week code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Character	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	20	V		
Gate-Source Voltage	V _{GSS}	±12	V		
	Steady State	T _A = +25°C T _A = +70°C	ID	9.0 7.1	А
Continuous Drain Current (Note 6) V _{GS} = 4.5V	t < 10s	T _A = +25°C T _A = +70°C	ID	9.3 7.4	А
Pulsed Drain Current (10µs pulse, duty cycle = 1	I _{DM}	45	А		

Thermal Characteristics

Characteristic		Symbol	Value	Units
Total Dower Dissinction (Note 5)	T _A = +25°C	D	0.8	W
Total Power Dissipation (Note 5)	T _A = +70°C	PD	0.5	vv
Thermal Desistance, Junction to Ambient (Note E)	Steady State	D	157	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t < 10s	R _{θJA}	148	C/W
Total Dower Dissipation (Note 6)	T _A = +25°C	D	1.7	W
Total Power Dissipation (Note 6)	T _A = +70°C	PD	1.1	vv
Thermal Desistance, Junction to Ambient (Note 6)	Steady State	D	73.7	
Thermal Resistance, Junction to Ambient (Note 6)	t < 10s	R _{θJA}	68	°C/W
Thermal Resistance, Junction to Case	R _{θJC}	9.4		
Operating and Storage Temperature Range		T _{J.} T _{STG}	-55 to 150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

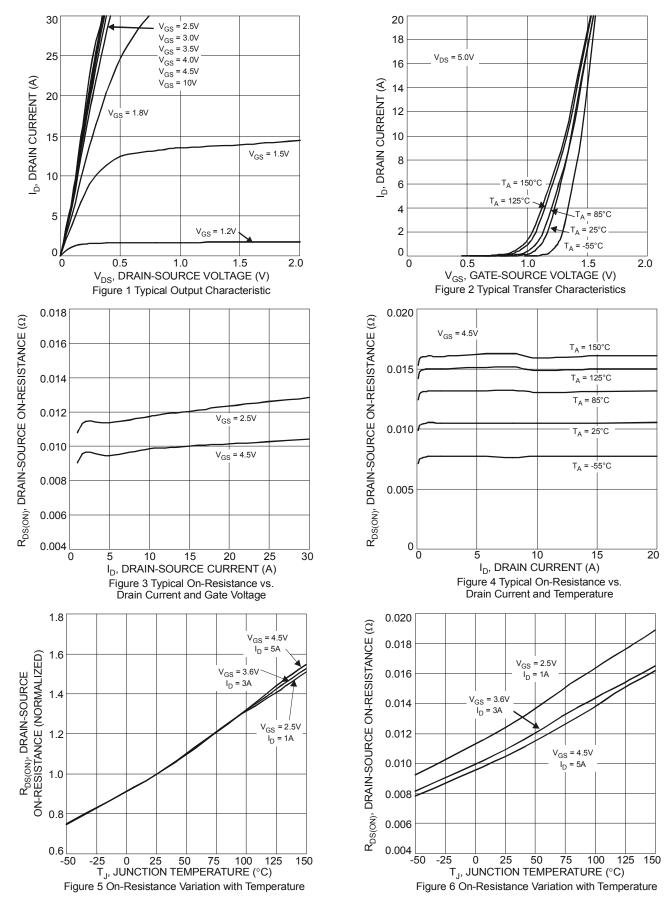
			-				
	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	D) (00			14		
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	—	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	_	—	1.0	μA	V_{DS} = 20V, V_{GS} = 0V	
Gate-Source Leakage	IGSS	—	—	±10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	1				I.		
Gate Threshold Voltage	V _{GS(th)}	0.3	0.71	1.1	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
			10	13		V_{GS} = 4.5V, I _D = 4.0A	
			11	14		V_{GS} = 4.0V, I_{D} = 4.0A	
Static Drain-Source On-Resistance	R _{DS (ON)}	_	12	17	mΩ	V _{GS} = 3.1V, I _D = 4.0A	
			13	18		V _{GS} = 2.5V, I _D = 4.0A	
			19	28		V _{GS} = 1.8V, I _D = 3.5A	
Forward Transfer Admittance	Y _{fs}	_	25		S	V _{DS} = 5V, I _D = 6A	
Diode Forward Voltage	V _{SD}	_	0.75	1.0	V	V _{GS} = 0V, I _S = 1A	
DYNAMIC CHARACTERISTICS (Note 8)	-						
Input Capacitance	C _{iss}	_	1550	—	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	166		pF		
Reverse Transfer Capacitance	C _{rss}		145		pF		
Gate Resistance	Rq		1.37		Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz	
Total Gate Charge (V _{GS} = 2.5V)	Qq	_	8.4		nC		
Total Gate Charge (V _{GS} = 4.5V)	Qq	_	16	—	nC	V _{DS} = 10V, I _D = 6A	
Gate-Source Charge	Q _{gs}		2.3	_	nC		
Gate-Drain Charge	Q _{gd}	_	2.5		nC		
Turn-On Delay Time	t _{D(on)}		6.9	—	ns		
Turn-On Rise Time	tr		15.5		ns	$V_{DD} = 10V, R_{L} = 1.7\Omega,$	
Turn-Off Delay Time	t _{D(off)}		40.9	—	ns	$V_{GS} = 5.0V, R_{G} = 3\Omega$	
Turn-Off Fall Time	tf		12		ns		

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad Notes:

7. Repetitive rating, pulse width limited by junction temperature 8. Guaranteed by design. Not subject to product testing

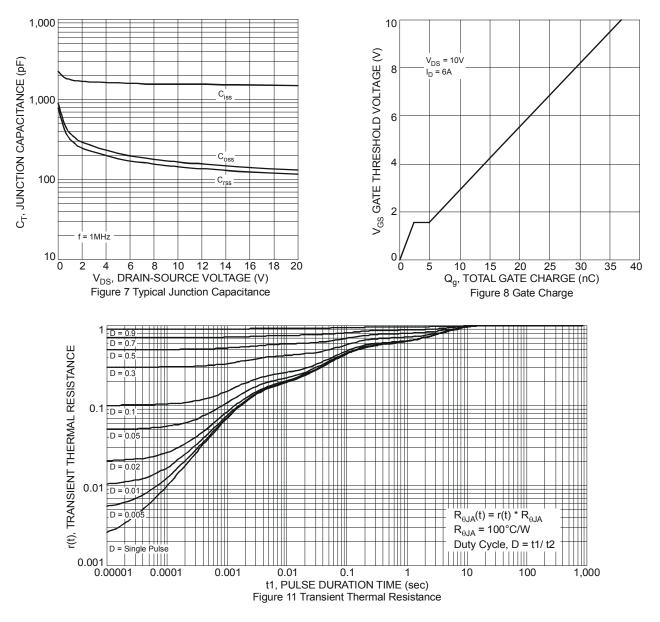


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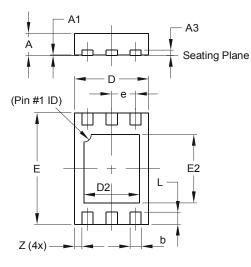
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Package Outline Dimensions

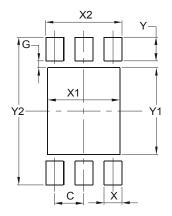
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	U-DFN2030-6					
Туре В						
Dim	Min	Max	Тур			
Α	0.55	0.65	0.60			
A1	0	0.05	0.02			
A3	A3 0.15					
b	0.25	0.35	0.30			
D	1.95	2.05	2.00			
D2	1.40	1.60	1.50			
Ε	2.95	3.05	3.00			
E2	1.74	1.94	1.84			
е	e 0.65					
L	0.28	0.38	0.33			
Ζ	-	-	0.20			
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value			
Dimensions	(in mm)			
С	0.650			
G	0.150			
Х	0.400			
X1	1.600			
X2	1.700			
Y	0.530			
Y1	1.940			
Y2	3.300			



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