

QUICK START GUIDE FOR DEMONSTRATION CIRCUIT 992

MONOLITHIC SYNCHRONOUS BUCK REGULATOR

LTC3549EDCB

DESCRIPTION

Demonstration circuit 992 is a Step-Down Converter, using the LTC3549 monolithic synchronous buck regulator. The DC992A has an input voltage range of 1.6V to 5.5V, and is capable of delivering up to 250 mA of output current at a minimum input voltage of 1.8V. The output voltage of the DC992A can be set as low as 0.611V, the reference voltage of the LTC3549. At low load currents, the DC992A can operate in either noise sensitive applications, due to the capability of the LTC3549 to operate in pulse-skipping mode, or in high efficiency applications, because the LTC3549 can also operate in Burst-Mode. Of course, in continuous mode, or large load current applications, the DC992A is a

high efficiency circuit - over 90%. The DC992A consumes only 50 μ A during sleep mode operation, and during shutdown, it consumes at less than 1 μ A typically. Because of the high switching frequency of the LTC3549 - 2.25 MHz - the DC992A uses low profile surface mount components. These features, plus the LTC3549 coming in a small 6-Lead DFN package, make the DC992A an ideal circuit for use in battery-powered, hand-held applications.

Design files for this circuit board are available. Call the LTC factory.

LTC is a trademark of Linear Technology Corporation

Table 1.

Performance Summary ($T_A = 25^\circ\text{C}$)

PARAMETER	CONDITIONS	VALUE
Minimum Input Voltage		1.6V
Maximum Input Voltage		5.5V
Output Voltage V_{OUT} Regulation	$V_{IN} = 1.6\text{V to } 5.5\text{V}$, $I_{OUT} = 0\text{A to } 250\text{ mA}$	1.2V $\pm 4\%$ (1.152V to 1.248V) 1.5V $\pm 4\%$ (1.44V to 1.56V) 1.8V $\pm 4\%$ (1.728V to 1.872V)
Typical Output Ripple V_{OUT}	$V_{IN} = 3.6\text{V}$, $I_{OUT} = 250\text{ mA}$ (20 MHz BW)	<20mV _{p-p}
Operation Modes	Burst Mode - $V_{IN} = 3.6\text{V}$, $V_{OUT} = 1.8\text{V}$	$\sim 40\text{ mA} \pm 8\text{ mA}$
	Pulse-Skip - $V_{IN} = 3.6\text{V}$, $V_{OUT} = 1.8\text{V}$	$\sim 40\text{ mA} \pm 8\text{ mA}$
Nominal Switching Frequency		2.25 MHz

QUICK START PROCEDURE

Demonstration circuit 992 is created to evaluate the performance of the LTC3549. For proper measurement equipment configuration, set up the circuit according to the diagram in **Figure 1**. Before proceeding to test, insert jumper JP1 shunt into the off position, connecting the RUN pin to ground (GND), which shuts down the circuit. Also insert shunts into the Pulse-Skip position of jumper JP2, and into the 1.2V output voltage position.

NOTE: When measuring the input or output voltage ripple, care must be taken to avoid a long ground lead on the oscilloscope probe. Measure the input or output voltage ripple by touching the probe tip directly across the Vin or Vout and GND terminals. See **Figure 2** for proper scope probe technique.

1. Apply 3.6V at Vin and set the load to 50 mA. Measure Vout; it should read 0V. If desired, one can measure the shutdown supply current at this point. The supply current will be approximately 1 μ A in shutdown.
2. Turn on the circuit by inserting the shunt in jumper JP1 into the ON position. The output voltage should be regulating. Measure Vout - it should measure 1.2V $\pm$ 2% (1.176V to 1.224V).

3. Vary the input voltage from 1.6V to 5.5V and adjust the load current from 0 to 250 mA. Vout should read between 1.2V $\pm$ 4% (1.152V to 1.248V). Measure the output ripple voltage at any output current level above 50 mA; it will measure less than 20 mVAC. The Burst-Mode™ and Pulse-Skip capability of the LTC3549 can also be observed now by setting the JP2 jumper shunt into either the Pulse-Skip position or the Burst-Mode™ position, and decreasing the load current below ~40 mA.
4. Observe the voltage waveform at the switch node. Verify the switching frequency is between 1.8 MHz and 2.7 MHz ($T = 0.556 \mu$ s and 0.37μ s), and that the switch node waveform is rectangular in shape.

Insert jumper JP1 shunt into the OFF position and move the 1.2V Vout shunt into any of the remaining output voltage options: 1.5V or 1.8V. Just as in the 1.2Vout test, the output voltage should read Vout $\pm$ 2% tolerance under static line and load conditions, and another $\pm$ 2% tolerance under dynamic line and load conditions ($\pm$ 4% total). When finished, turn off the circuit by inserting the jumper JP1 shunt into the OFF position.

QUICK START GUIDE FOR DEMONSTRATION CIRCUIT 992

MONOLITHIC SYNCHRONOUS BUCK REGULATOR

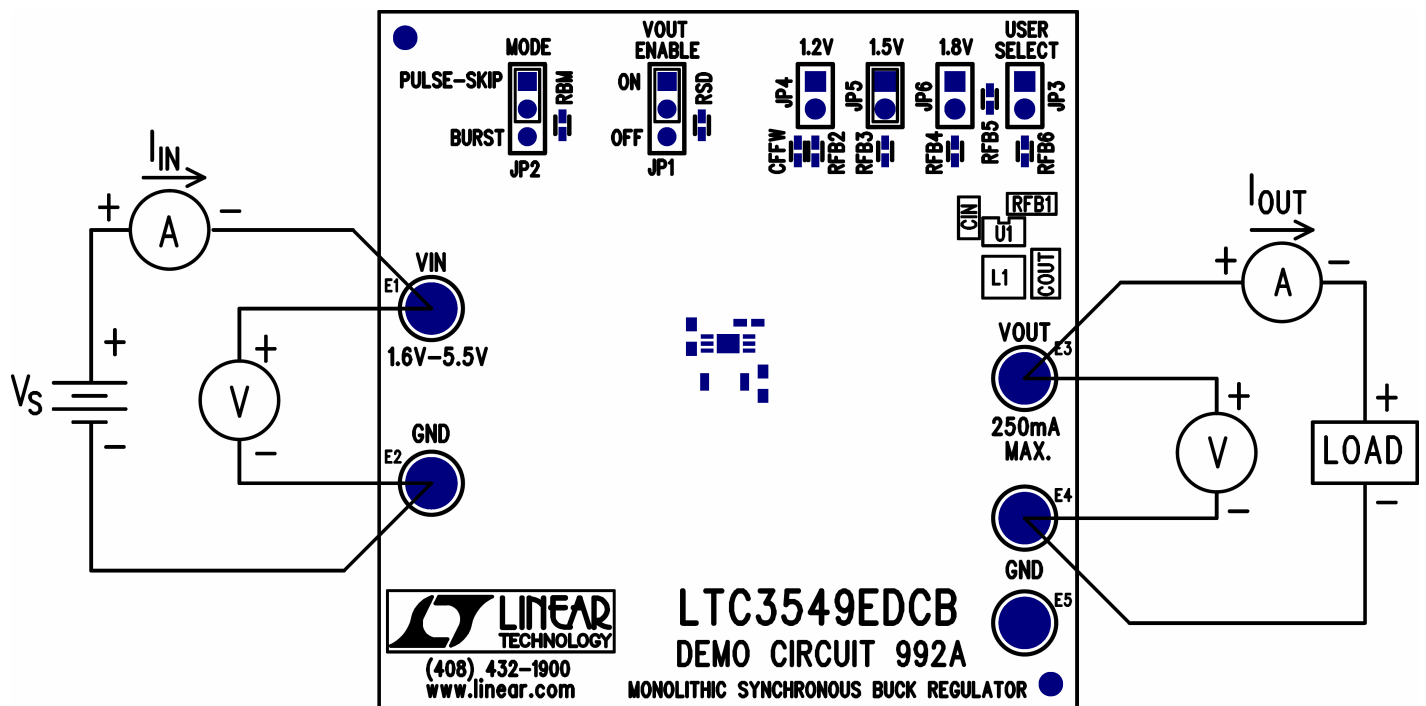


Figure 1. Proper Measurement Equipment Setup

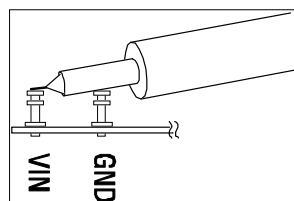
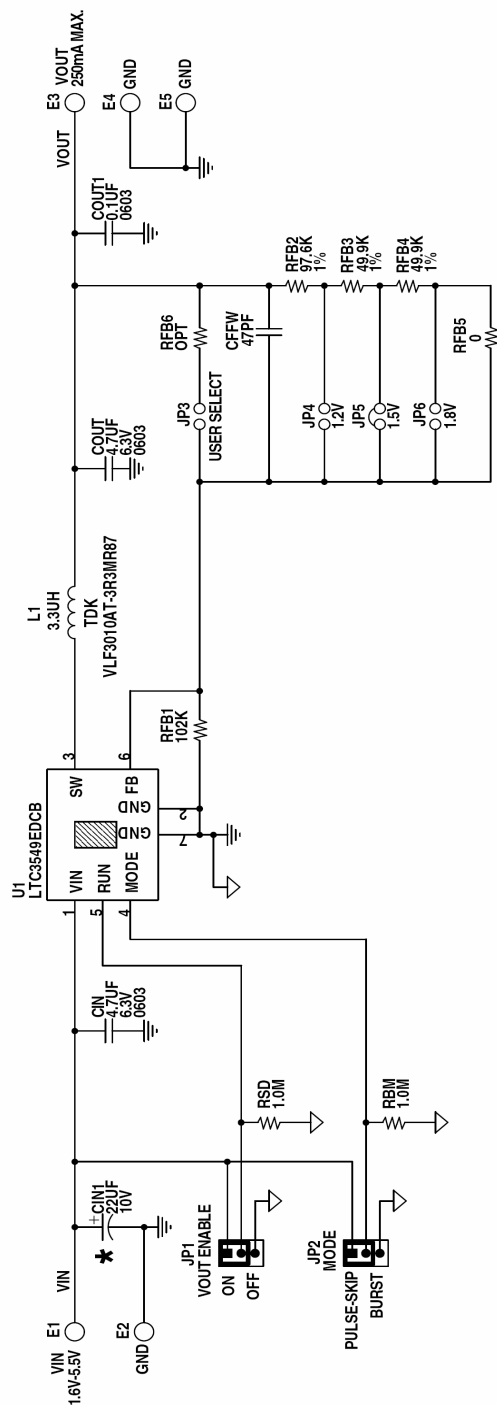


Figure 2. Measuring Input or Output Ripple

QUICK START GUIDE FOR DEMONSTRATION CIRCUIT 992

MONOLITHIC SYNCHRONOUS BUCK REGULATOR



NOTES: UNLESS OTHERWISE SPECIFIED

- ALL RESISTORS ARE IN OHMS, 0402.
ALL CAPACITORS ARE 0402.
 - INSTALL SHUNT ON JP1-JP2 & JP5 PIN 1 AND 2.
- ★ CIN1 IS AN OPTIONAL CAPACITOR. IT IS INSERTED ON THE DC992A TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE DUE TO THE LONG INPUT LEADS. ON A NORMAL, TYPICAL PCB, WITH SHORT TRACES, THE CAPACITOR IS NOT NEEDED.

CUSTOMER NOTICE LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		CONTRACT NO.	1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0507 LTC Confidential-For Customer Use Only
APPROVALS	KIM T.	CHECKED:	APPROVED:
DESIGNED:	DESIGNED:	ENGINEER: TOM G.	DESIGNED:
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		DATE:	Wednesday, August 23, 2006
TITLE: SCHEMATIC MONOLITHIC SYNCHRONOUS BUCK REGULATOR		SIZE	A
DWG NO.		REV	A-1
DC992A-1 * LTC3549EDCB		SHEET	1 OF 1