

High Performance Schottky Rectifier, 440 A



PRODUCT SUMMARY				
I _{F(AV)}	440 A			
V_{R}	30 V			
Package	TO-244			
Circuit	Two diodes common cathode			

FEATURES

- 150 °C T_J operation
- · Center tap module
- · Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-440CNQ030PbF center tap, high current, Schottky rectifier module has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, welding and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	440	Α			
V _{RRM}		30	V			
I _{FSM}	t _p = 5 μs sine	27 000	Α			
V _F	220 A _{pk} , T _J = 125 °C (per leg)	0.41	V			
T _J	Range	-55 to 150	°C			

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-440CNQ030PbF	UNITS
Maximum DC reverse voltage	V_{R}	30	V
Maximum working peak reverse voltage	V_{RWM}	30	V

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average	per module	l=	50 % duty cycle at T _C = 125 °C, rectangular waveform		50 % duty cycle at Ta = 125 °C rectangular waveform		440	
forward current (fig. 5)	per leg	I _{F(AV)}			220	Α		
Maximum peak one cycle non-repetitive surge current per leg (fig. 7)		I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	27 000	, ,		
			10 ms sine or 6 ms rect. pulse	V _{RRM} applied	3000			
Non-repetitive avalanche	energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 20 A, L = 1 mH		198	mJ		
Repetitive avalanche curre	ent per leg	I _{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T _J maximum V _A = 1.5 x V _R typical		44	Α		



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	220 A	T 05 °C	0.51	V
Maximum forward voltage drop per leg		440 A	- T _J = 25 °C	0.63	
(fig. 1)		220 A	T 105 00	0.41	
		440 A	- T _J = 125 °C	0.55	
Maximum reverse leakage current per leg		T _J = 25 °C	V _B = Rated V _B	20	mA
(fig. 2)	I _{RM} ⁽¹⁾	T _J = 125 °C	VR = nateu VR	1120	IIIA
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		14 800	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane		5	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R 10 000			V/µs

Note

 $^{^{(1)}\,}$ Pulse width $<300~\mu s,$ duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}	-55	=	150	°C	
Thermal resistance, junction to case per leg	Б	-	=	0.19		
Thermal resistance, junction to case per module	R _{thJC}	-	=	0.095	°C/W	
Thermal resistance, case to heatsink	R _{thCS}	-	0.10	-		
Weight		-	68	-	g	
		-	2.4	-	oz.	
Mounting torque		35.4 (4)	=	53.1 (6)		
Mounting torque center hole		30 (3.4)	=	40 (4.6)	lbf ⋅ in (N ⋅ m)	
Terminal torque		30 (3.4)	=	44.2 (5)		
Vertical pull		-	=	80	lbf ⋅ in	
2" lever pull		-	-	35		

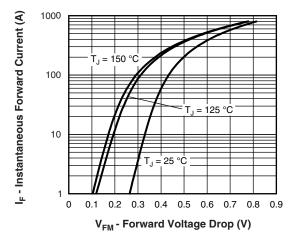


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

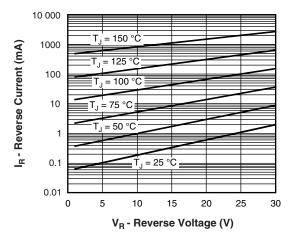


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

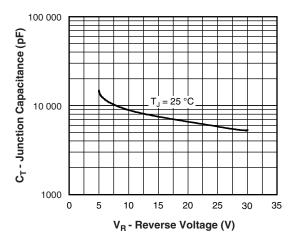


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

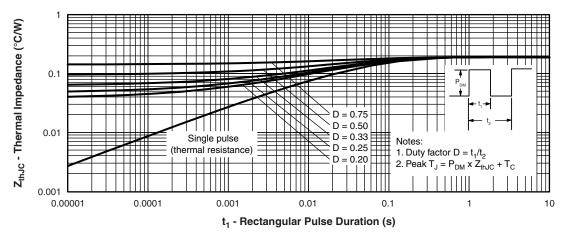


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

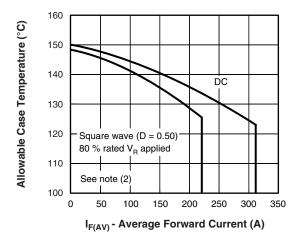


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

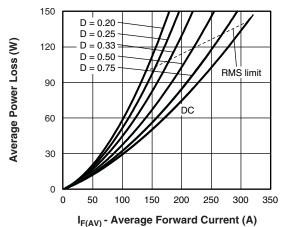
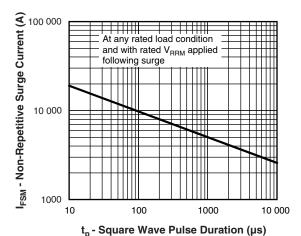


Fig. 6 - Forward Power Loss Characteristics (Per Leg)



tp - Square wave Fulse Duration (µs)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

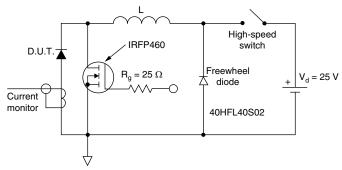


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

ORDERING INFORMATION TABLE

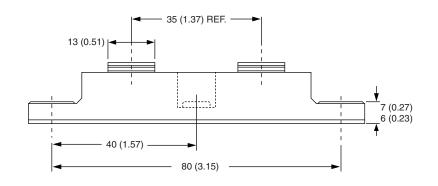
Device code VS-44 0 C Q 030 **PbF** Ν 2 (3) (4)(5) (6)8 Vishay Semiconductors product Average current rating (x 10) Product silicon identification C = Circuit configuration N = Not isolated Q = Schottky rectifier diode Voltage rating (030 = 30 V) Lead (Pb)-free 8 Tube standard pack quantity: 25 pieces

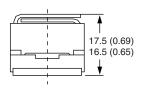
LINKS TO RELATED DOCUMENTS				
nensions <u>www.vishay.com/doc?95021</u>				

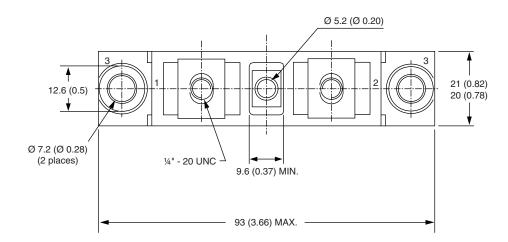


TO-244

DIMENSIONS in millimeters (inches)









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