



Is Now Part of



ON Semiconductor®

To learn more about ON Semiconductor, please visit our website at
www.onsemi.com

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.



USB1T20 — Universal Serial Bus Transceiver

Features

- Complies with Universal Serial Bus Specification 2.0 for FS/LS Applications
- Utilizes Digital Inputs and Outputs to Transmit and Receive USB Cable Data
- Supports 12Mbit/s Full Speed (FS) and 1.5Mbit/s Low Speed (LS) Serial Data Transmission
- Supports Single-ended and Differential Data Interface as Function of MODE
- Single 3.3 V Supply
- ESD Performance: Human Body Model
 - 9.5 kV on D-, D+ Pins Only
 - 4.0 kV on All Other Pins

Description

USB1T20 is a generic USB 2.0 compliant transceiver. Using a single voltage supply, the USB1T20 provides an ideal USB interface solution for any electronic device able to supply 3.0 V to 3.6 V. It is designed to allow 5.0 V or 3.3 V programmable and standard logic to interface with the physical layer of the Universal Serial Bus (USB). It is capable of transmitting and receiving serial data at both full speed (12Mbit/s) and low speed (1.5Mbit/s) data rates.

Packaged in industry-standard TSSOP package. The USB1T20 is ideal for mobile electronics and other space-constrained applications.

Ordering Information

Part Number	Operating Temperature Range	Package	Packing Method
USB1T20MTCX	-40° to +85°C	14-Lead, Thin-Shrink Small-Outline Package (TSSOP) JEDEC MO-153, 4.4mm Wide	Tape and Reel

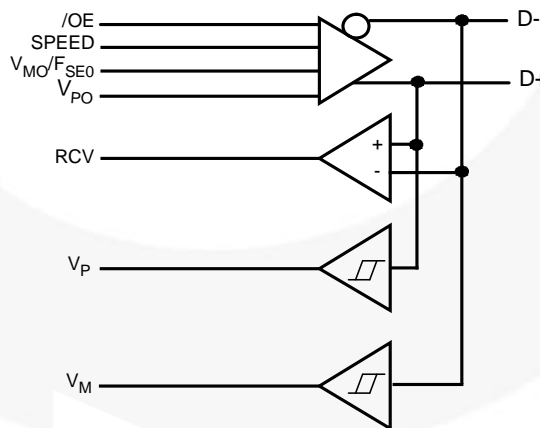


Figure 1. Logic Diagram

Pin Configuration

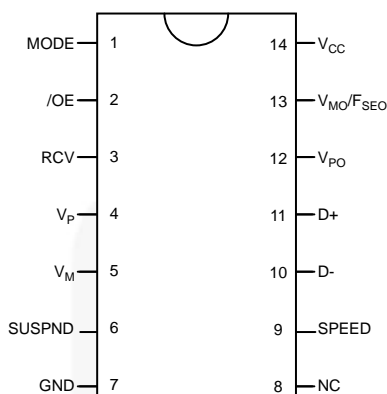


Figure 2. Pin Configuration (Top View)

Pin Definitions

Pin #	Name	I/O	Description			
1	MODE	I	Mode. When left unconnected, a weak pull-up transistor pulls mode pin to V _{CC} and, in this GND, the V _{MO} /F _{SEO} pin takes the function of F _{SEO} (force SEO).			
2	/OE	I	Output Enable. Active LOW; enables the transceiver to transmit data on the bus. When not active, the transceiver is in receive mode.			
3	RCV	O	Receive Data. CMOS-Level output for USB differential input.			
4, 5	V _P , V _M	O	Gated version of D- and D+. Outputs are logic “0” and logic “1.” Used to detect single ended zero (/SEO), error conditions, and interconnected speed. (Input to SIE).			
			V _P	V _M	RESULT	
			0	0	/SEO	
			0	1	Low Speed	
			1	0	Full Speed	
			0	1	Error	
6	SUSPND	I	Suspend. Enables a low-power state while the USB bus is inactive. While the suspend pin is active, it drives the RCV pin to a logic “0” state. Both D+ and D- are 3-state.			
7	GND		Ground reference.			
8	NC		No connect.			
9	SPEED	I	Edge Rate Control. Logic “1” operates at edge rates for full speed. Logic “0” operates edge rates for low speed.			
10, 11	D-, D+	AI/O	Data+, Data-. Differential data bus conforming to the Universal Serial Bus standard.			
12, 13	V _{PO} , V _{MO} /F _{SEO}	I	Inputs to differential driver. (Outputs from SIE.)			
			Mode	V _{PO}	V _{MO} /F _{SEO}	RESULT
			0	0	0	Logic “0”
				0	1	/SEO
				1	0	Logic “1”
				1	1	/SEO
			1	0	0	/SEO
				0	1	Logic “0”
				1	0	Logic “1”
				1	1	Illegal Code
14	V _{CC}		3.0 to 3.6 power supply.			

Functional Truth Table

Input					I/O		Outputs			
Mode	V _{PO}	V _{MO} /F _{SEO}	/OE	SUSPND	D+	D-	RCV	V _P	V _M	Result
0	0	0	0	0	0	1	0	0	1	Logic "0"
0	0	1	0	0	0	0	Undefined State	0	0	/SEO
0	1	0	0	0	1	0	1	1	0	Logic "1"
0	1	1	0	0	0	0	Undefined State	0	0	/SEO
1	0	0	0	0	0	0	Undefined State	0	0	/SEO
1	0	1	0	0	0	1	0	0	1	Logic "0"
1	1	0	0	0	1	0	1	1	0	Logic "1"
1	1	1	0	0	1	1	Undefined State	Undefined State	Undefined State	Illegal Code
Don't Care	Don't Care	Don't Care	1	0	3-State	3-State	Undefined State	Undefined State	Undefined State	D+/D-Hi-Z
Don't Care	Don't Care	Don't Care	1	1	3-State	3-State	Undefined State	Undefined State	Undefined State	D+/D-Hi-Z

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max.	Unit
V_{CC}	DC Supply Voltage	-0.5	7.0	V
I_{IK}	DC Input Diode Current, $V_{IN} < 0$ V		-50	mA
V_{IN}	Input Voltage ⁽¹⁾	-0.5	5.5	V
$V_{I/O}$	Input / Output Voltage	-0.5	$V_{CC} + 0.5$	V
I_{OK}	Output Diode Current, $V_O > V_{CC}$ or $V_O < 0$ V		± 50	mA
V_O	Output Voltage ⁽¹⁾	-0.5	$V_{CC} + 0.5$	V
I_O	Output Source or Sink Current ($V_O = 0$ to V_{CC})		± 15	mA
			± 50	
I_{CC} / I_{GND}	V_{CC} / GND Current		± 100	mA
T_{STG}	Storage Temperature Range	-60	+150	°C

Note:

- The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit
V_{CC}	Supply Voltage	3.0	3.6	V
V_{IN}	Input Voltage	0	5.5	V
$V_{AI/O}$	Input Range for AI/O	0	V_{CC}	V
V_O	Output Voltage	0	V_{CC}	V
T_A	Operating Ambient Temperature, Free Air	-40	+85	°C

DC Electrical Characteristics Digital Pins

Over the recommended range of supply voltage and operating free air temperature unless otherwise noted. $V_{CC} = 3.0\text{ V to }3.6\text{ V}$.

Symbol	Parameter	Conditions	T _A = -40 to +85°C			Units
			Min.	Typ.	Max.	
Input Levels						
V _{IL}	Low-Level Input Voltage				0.8	V
V _{IH}	High-Level Input Voltage		2			V
Output Levels						
V _{OL}	Low-Level Output Voltage	I _{OL} = 4 mA			0.4	V
		I _{OL} = 20 μA			0.1	
V _{OH}	High-Level Output Voltage	I _{OH} = 4 mA	2.4			V
		I _{OH} = 20 μA	V _{CC} -0.1			
Leakage Current						
I _{IN}	Input Leakage Current	V _{CC} = 3.0 to 3.6 V			±5	μA
I _{CCFS}	Supply Current, Full Speed	V _{CC} = 3.0 to 3.6 V			5	mA
I _{CCLS}	Supply Current, Low Speed	V _{CC} = 3.0 to 3.6 V			5	mA
I _{CCQ}	Quiescent Supply Current	V _{CC} = 3.0 to 3.6 V, V _{IN} = V _{CC} or GND			5	mA
I _{CCS}	Supply Current in Suspend	V _{CC} = 3.0 to 3.6 V, Mode = V _{CC}			10	μA

DC Electrical Characteristics D+/D- Pins

Over the recommended range of supply voltage and operating free air temperature unless otherwise noted. $V_{CC} = 3.0\text{ V to }3.6\text{ V}$.

Symbol	Parameter	Conditions	T _A =-40 to +85°C			Units
			Min.	Typ.	Max.	
Input Levels						
V _{DI}	Differential Input Sensitivity	(D+) – (D-)	0.2			V
V _{CM}	Differential Common-Mode Range	Includes V _{DI} Range	0.8		2.5	V
V _{SE}	Single-Ended Receiver Threshold		0.8		2.0	V
Output Levels						
V _{OL}	Static Output Low-Voltage	R _L of 1.5 kΩ to 3.6 V			0.3	V
V _{OH}	Static Output High-Voltage	R _L of 1.5 kΩ to GND	2.8		3.6	V
V _{CR}	Differential Crossover		1.3		2.0	V
Leakage Current						
I _{OZ}	High-Z State Data Line Leakage Current	0 V<V _{IN} <3.3 V			±5	μA
Capacitance						
C _{IN}	Transceiver Capacitance ⁽²⁾	Pin to GND			10	pF
	Capacitance Match ⁽²⁾				10	%
Output Resistance						
Z _{DRV}	Driver Output Resistance ⁽³⁾	Steady-State Drive	4		20	Ω
	Resistance Match ⁽³⁾				10	%

Notes:

- This specification is guaranteed by design and statistical process distribution.
- Excludes external resistor. To comply with USB specification 1.1, external series resistors of $24\text{ }\Omega \pm 1\%$ each on D+ and D- are recommended.

AC Electrical Characteristics D+/D- Pins, Full Speed

Over the recommended range of supply voltage and operating free air temperature unless otherwise noted.

$V_{CC} = 3.0\text{ V to }3.6\text{ V}$; $C_L = 50\text{ pF}$; $R_L = 1.5\text{ k}\Omega$ on D+ to V_{CC} .

Symbol	Parameter	Conditions	T _A =-40 to +85°C			Units
			Min.	Typ.	Max.	
Driver Characteristics						
t _R , t _F	Rise and Fall Time	10 and 90%, Figure 3	4		20	ns
t _{RFM}	Rise/Fall Time Matching	t _r / t _f	90		110	%
V _{CRS}	Output Signal Crossover Voltage		1.3		2.0	V
Driver Timings						
t _{PLH}	Driver Propagation Delay (V _{PO} , V _{MO} /F _{SEO} to D+D-)	Figure 4			18	ns
t _{PHZ} , t _{PLZ}	Driver Disable Delay (/OE to D+/D-)	Figure 6			13	ns
t _{PZH} , t _{PZL}	Driver Enable Delay (/OE to D+/D-)	Figure 6			17	ns
Receiver Timings						
t _{PLH}	Receiver Propagation Delay D+/D- to RVC	Figure 5			16	ns
t _{PHL}					19	ns
t _{PLH} , t _{PHL}	Single-ended Receiver Delay (D+,D- to V _P , V _M)	Figure 5			8	ns

AC Electrical Characteristics D+/D- Pins, Low Speed

Over the recommended range of supply voltage and operating free air temperature unless otherwise noted.

$V_{CC} = 3.0\text{ V to }3.6\text{ V}$; $C_L = 200\text{ pF to }600\text{ pF}$; $R_L = 1.5\text{ k}\Omega$ on D- to V_{CC} .

Symbol	Parameter	Conditions	T _A =-40 to +85°C			Units
			Min.	Typ.	Max.	
Driver Characteristics						
t _{LR} , t _{LF}	Rise and Fall Time	10 and 90%, Figure 3	75		300	ns
t _{RFM}	Rise/Fall Time Matching	t _r / t _f	80		120	%
V _{CRS}	Output Signal Crossover Voltage		1.3		2.0	V
Driver Timings						
t _{PLH} , t _{PHL}	Driver Propagation Delay (V _{PO} , V _{MO} /F _{SEO} to D+/D-)	Figure 4			300	ns
t _{PHZ} , t _{PLZ}	Driver Disable Delay (/OE to D+/D-)	Figure 6			13	ns
t _{PZH} , t _{PZL}	Driver Enable Delay (/OE to D+/D-)	Figure 6			205	ns
Receiver Timings						
t _{PLH} , t _{PHL}	Receiver Propagation Delay (D+/D- to RVC)	Figure 5			18	ns
t _{PLH} , t _{PHL}	Single-ended Receiver Delay (D+, D- to V _P , V _M)	Figure 5			28	ns

AC Loadings and Waveforms

V_{OL} and V_{OH} are the typical output voltage drops that occur with the output load. V_{CC} never goes below 3.0 V.

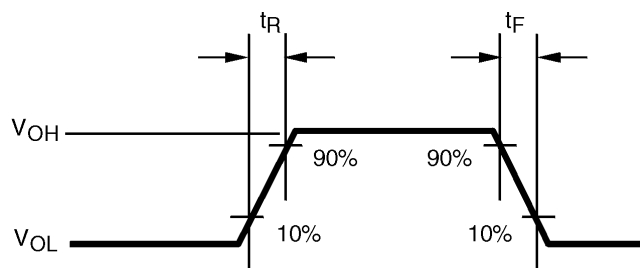


Figure 3. Rise and Fall Times

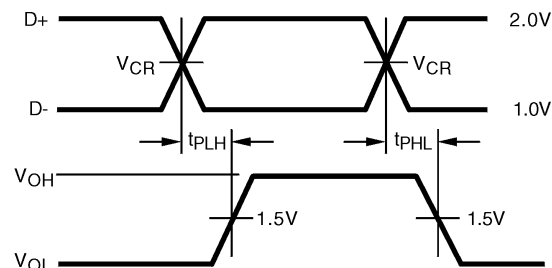


Figure 4. V_{PO} , V_{MO}/F_{SEO} to D+/D-

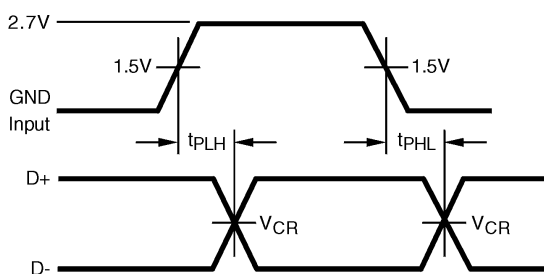


Figure 5. D+/D- to RCV, V_P/V_M

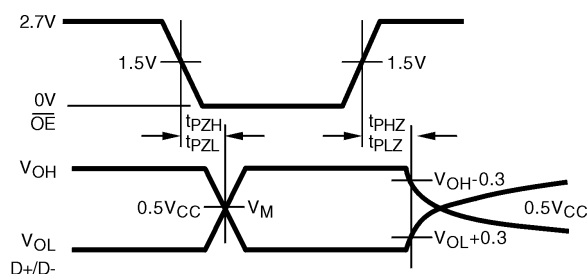


Figure 6. /OE to D+/D-

Test Circuits and Waveforms

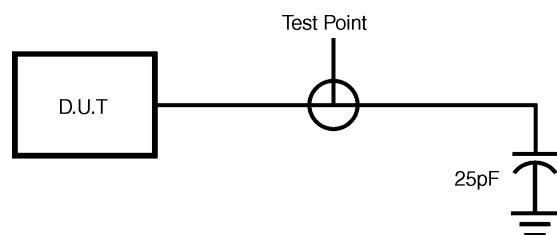


Figure 7. Load for V_M/V_P and RCV

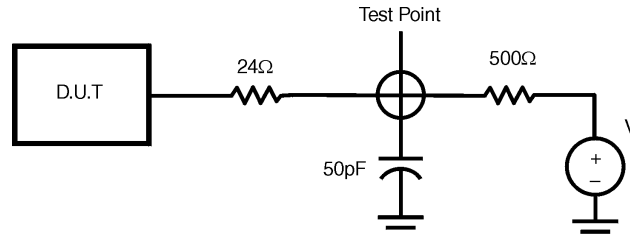
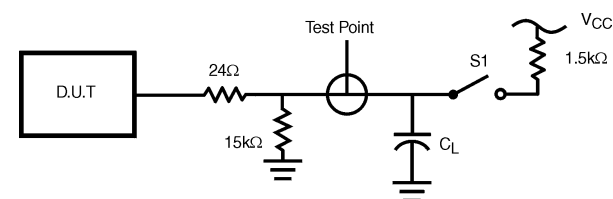


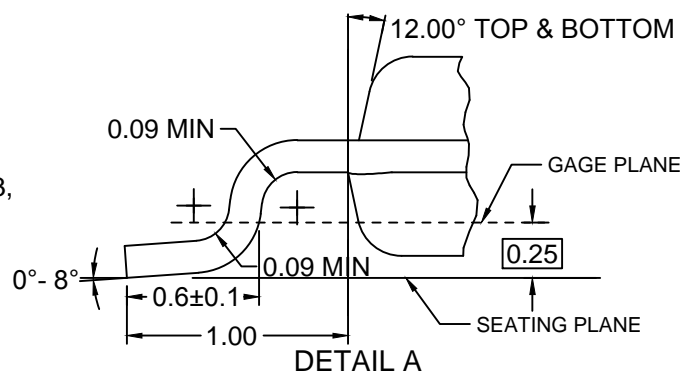
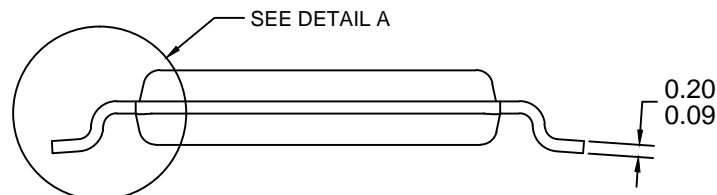
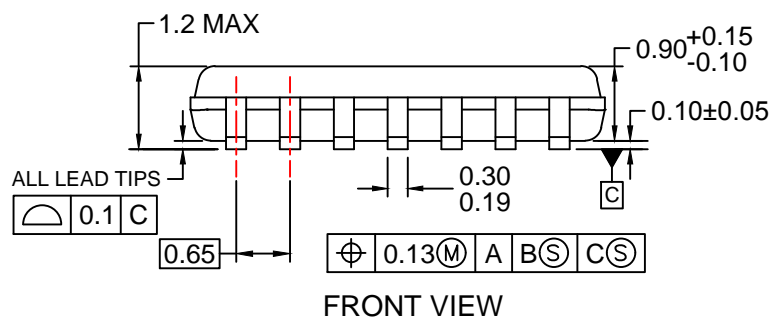
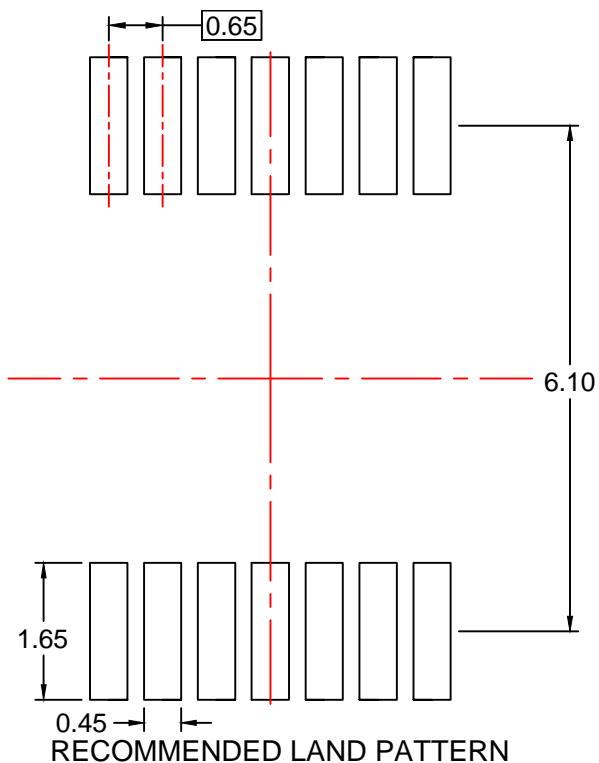
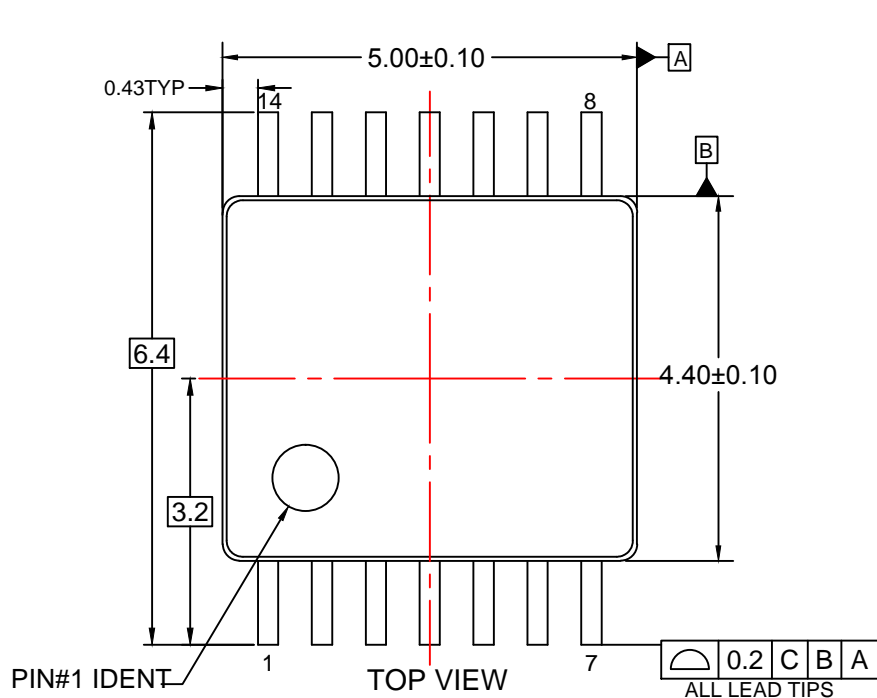
Figure 8. Load for Enable and Disable Times



$C_L=50\text{pF}$, Full Speed
 $C_L=200\text{pF}$, Low Speed (Minimum Timing)
 $C_L=600\text{pF}$, Low Speed (Maximum Timing)
 1.5kΩ on D- (Low Speed) or D+ (Full Speed) only

Test	S1
D-/LS	Close
D+/LS	Open
D-/FS	Open
D+/FS	Close

Figure 9. Load for D+/D-



NOTES:

- CONFORMS TO JEDEC REGISTRATION MO-153, VARIATION AB, REF NOTE 6
- DIMENSIONS ARE IN MILLIMETERS.
- DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS
- DIMENSIONING AND TOLERANCES PER ANSI Y14.5M, 2009.
- LANDPATTERN STANDARD: SOP65P640X110-14M.
- DRAWING FILE NAME: MKT-MTC14rev7.



ON Semiconductor and  are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor
19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada

Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910

Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: <http://www.onsemi.com/orderlit>

For additional information, please contact your local
Sales Representative

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[ON Semiconductor:](#)

[USB1T20MTCX](#) [USB1T20MTC](#)