

SURFACE MOUNT SILICON DUAL, ISOLATED, OPPOSING HIGH SPEED SWITCHING DIODES



The CMLD4448DOG is *Halogen Free* by design



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLD4448DO and CMLD4448DOG each contain two (2) isolated opposing configuration, silicon switching diodes, manufactured by the epitaxial planar process, epoxy molded in an SOT-563 surface mount package. These devices are designed for high speed switching applications.

MARKING CODES: CMLD4448DO: C40 CMLD4448DOG: 4CG

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	V _{RRM}	120	V
Continuous Forward Current	١ _F	250	mA
Peak Repetitive Forward Current	IFRM	500	mA
Peak Forward Surge Current, tp=1.0µs	IFSM	4.0	А
Peak Forward Surge Current, tp=1.0s	IFSM	1.0	А
Power Dissipation	PD	250	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	Θ_{JA}	500	°C/W

ELECTRICAL CHARACTERISTICS PER DIODE: (T_A=25°C unless otherwise noted)

		` A		,		
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS	
I _R	V _R =50V			300	nA	
IR	V _R =50V, T _A =125°C			100	μA	
IR	V _R =100V			500	nA	
BVR	Ι _R =100μΑ	120	150		V	
VF	I _F =1.0mA	0.55	0.59	0.65	V	
VF	I _F =10mA	0.67	0.72	0.77	V	
VF	I _F =100mA	0.85	0.91	1.0	V	
СЈ	V _R =0, f=1.0MHz			1.5	pF	
t _{rr}	$I_R=I_F=10$ mA, $I_{rr}=1.0$ mA, $R_L=100\Omega$		2.0	4.0	ns	

R6 (15-June 2015)





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SOT-563 CASE - MECHANICAL OUTLINE



PIN CONFIGURATION

– C –



DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
A	0.0027	0.007	0.07	0.18			
В	0.008		0.20				
С	0.017	0.024	0.45	0.60			
D	0.059	0.067	1.50	1.70			
E	0.020		0.50				
F	0.059	0.067	1.50	1.70			
G	0.043	0.051	1.10	1.30			
Н	0.006	0.012	0.15	0.30			
SOT-563 (REV: R2)							

SOI-563 (REV: R2)

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CMLD4448DO CMLD4448DOG



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SERVICES

- · Bonded Inventory
- Custom Electrical Screening
- Custom Electrical Characteristic Curves
- SPICE Models
- Custom Packaging
- Package Base Options
- Custom Device Development/Multi Discrete Modules (MDM[™])
- Bare Die Available for Hybrid Applications

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