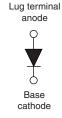


High Performance Schottky Rectifier, 240 A



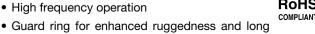


HALF-PAK (D-67)

PRODUCT SUMMARY				
I _{F(AV)}	240 A			
V_{R}	45 V			
Package	HALF-PAK (D-67)			
Circuit	Single diode			

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation



- · Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

term reliability

The VS-241NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	240	Α		
V _{RRM}		45	V		
I _{FSM}	t _p = 5 μs sine	25 000	А		
V _F	240 A _{pk} , T _J = 125 °C	0.64	V		
T _J	Range	-55 to 175	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-241NQ045PbF	UNITS		
Maximum DC reverse voltage	V_{R}	45	V		
Maximum working peak reverse voltage	V _{RWM}	45	V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 144 °C	C, rectangular waveform	240	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and	25 000	Α
non-repetitive surge current See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	with rated V _{RRM} applied	3450	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 26 A, L = 1 mH		324	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T _J maximum V _A = 1.5 x V _R typical 48		Α	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		240 A	T _J = 25 °C	0.80	. V
Maximum forward voltage drop	V _{FM} ⁽¹⁾	480 A		1.11	
See fig. 1	VFM ('')	240 A	T _J = 125 °C	0.64	
		480 A		0.86	
Maximum reverse leakage current	1 (4)	T _J = 25 °C	V _R = Rated V _R	20	mA
See fig. 2	I _{RM} (1)	T _J = 125 °C	$v_R = Rated v_R$	1120	MA
Maximum junction capacitance	C _T	C _T V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		14 800	pF
Typical series inductance	L _S	From top of terminal hole to mounting plane		5.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/		V/µs	

Note

 $^{(1)}$ Pulse width $< 500 \ \mu s$

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and ste temperature range	orage	T _J , T _{Stg}		-55 to 175	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	0.19	°C/W
Typical thermal resistance	e, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.05	
Approximate weight				30	g
				1.06	oz.
Marintina tararra	minimum		Non-lubricated threads	3 (26.5)	
Mounting torque	maximum			4 (35.4)	N ⋅ m (lbf ⋅ in)
Terminal torque	minimum			3.4 (30)	
	maximum			5 (44.2)	
Case style			HALF-PA	K module	

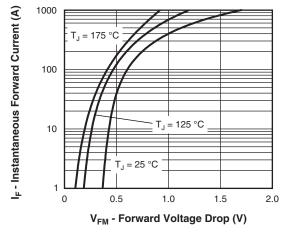


Fig. 1 - Maximum Forward Voltage Drop Characteristics

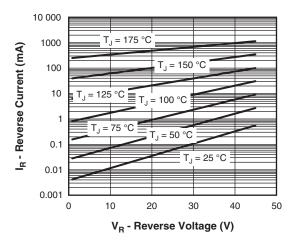


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



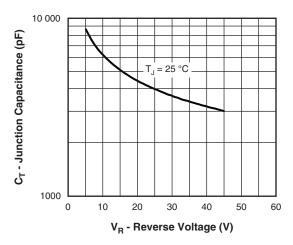


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

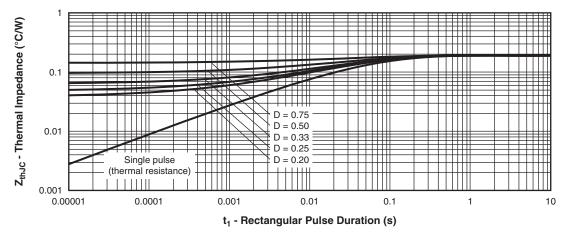


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

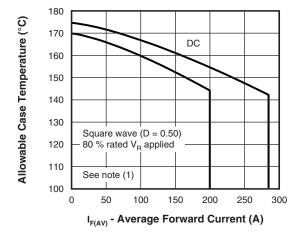


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

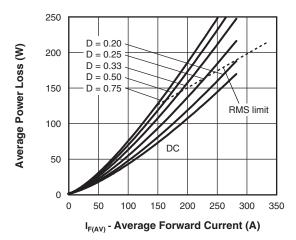
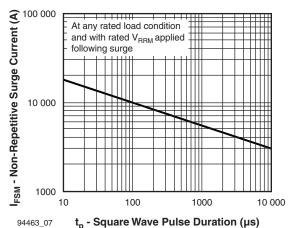


Fig. 6 - Forward Power Loss Characteristics



5_0/ Lp - Oquale wave I disc Duration (µs)

Fig. 7 - Maximum Non-Repetitive Surge Current

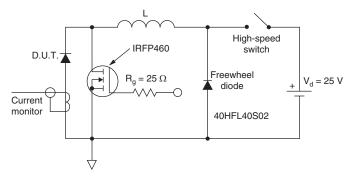


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (\text{see fig. 6}); \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \ x \ I_R \ (1 - D); \ I_R \ at \ V_{R1} = Rated \ V_R \end{array}$

ORDERING INFORMATION TABLE

VS-**Device code** 24 1 Ν Q 045 **PbF** (2) (3)(4)(5)(6) Vishay Semiconductors product Average current rating (x 10) 3 Product silicon identification N = Not isolated

6	-	Voltage rating (045 = 45 V)
		Load (Db) from

Q = Schottky rectifier diode

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95020			



D-67 HALF-PAK

DIMENSIONS in millimeters (inches)









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Vishay

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