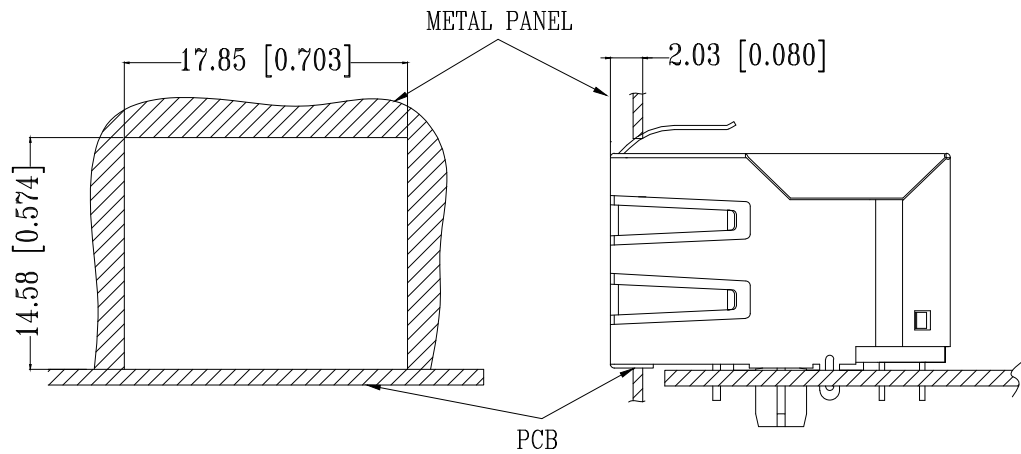
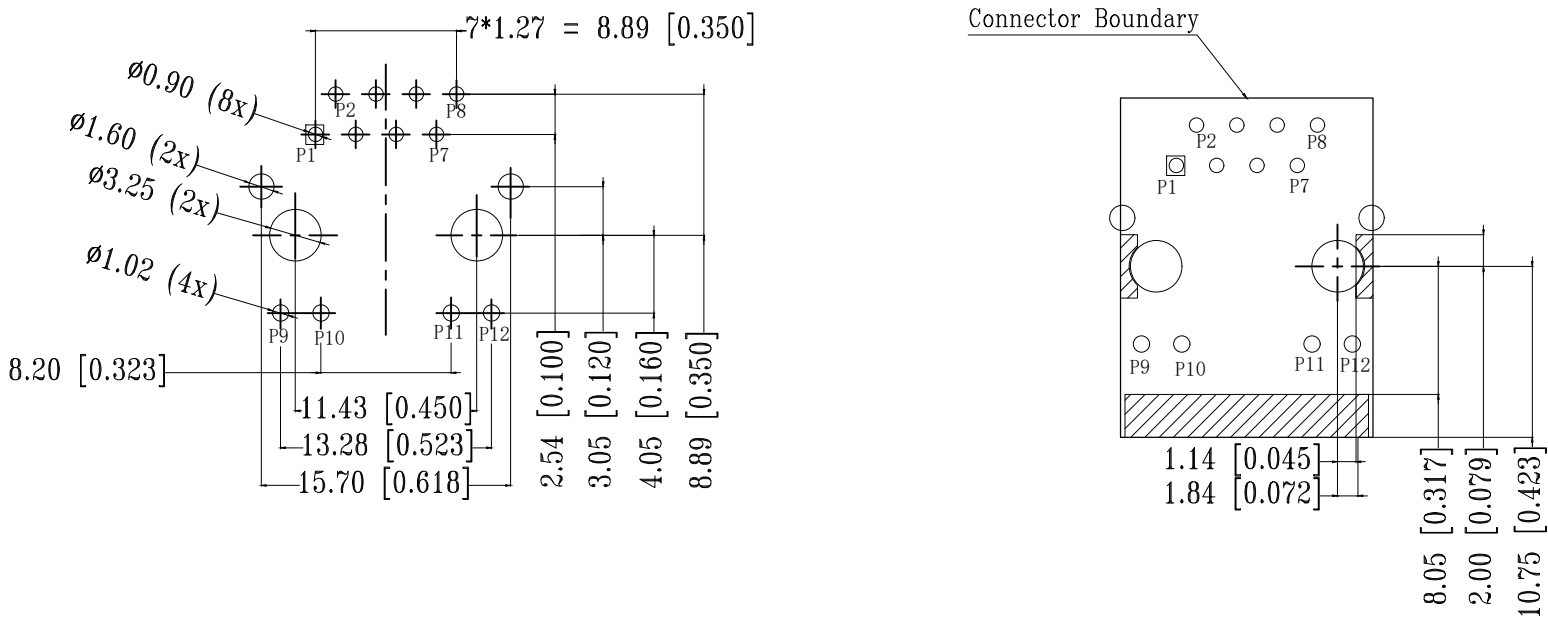


SUGGESTED PCB LAYOUT

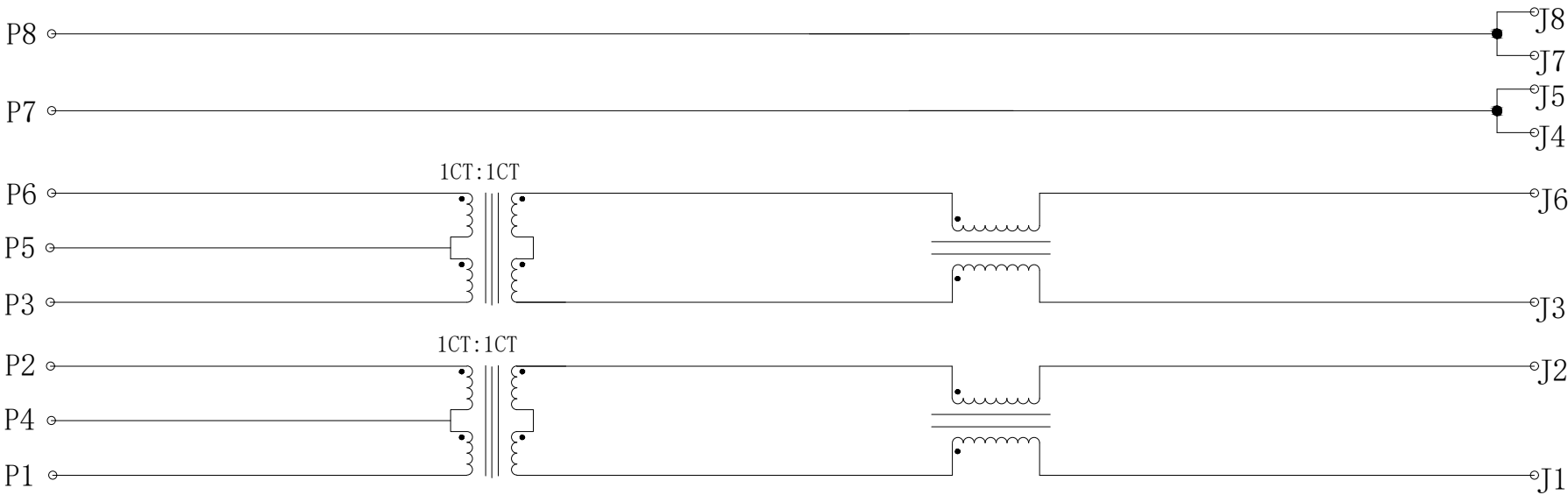


UNIT: mm / inch
TOLERANCES: ±0.10 / 0.004

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN GANESH C M	26NOV2018	TE Connectivity		
		CHK O. WIBORG	26NOV2018			
		APVD J. DE BRUIJN	26NOV2018	NAME RJ45 JACK MAG. POE 10/100 LED 1x1		
		PRODUCT SPEC 108-94694				
		APPLICATION SPEC 114-94556				
		WEIGHT		SIZE A3	CAGE CODE 00779	DRAWING NO C-2337992-4
		CUSTOMER DRAWING		SCALE 3:1	SHEET 2 OF 3	REV A

REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	—	SEE SHEET 1	—	—	—

ELECTRICAL CIRCUIT DIAGRAM



Pin	Green		Yellow
P9	+	P11	—
P10	—	P12	+

- 1.0 Turn Ratio @100kHz: (P1~P2):(J1~J2) = 1:1±2%
(P3~P6):(J3~J6) = 1:1±2%
- 2.0 Primary Inductance: 350µH MIN. @100kHz,0.1V 8mA DC BIAS
- 3.0 DC Resistance: 1.2 OHMS MAX.
- 4.0 Insertion Loss: 1–100MHz –1.2dB MAX.
- 5.0 Return Loss: 1–30MHz –16dB MIN.
30–60MHz –12dB MIN.
60–80MHz –10dB MIN.
- 6.0 CROSS TALK: 1–100MHz –27.1dB MIN.
- 7.0 COMMON TO COMMON MODE ATTENUATION: 1–100MHz –30dB MIN.
- 8.0 Isolation: PHY Side to Line Side: 2250VDC
- 9.0 DC CURRENT/VOLTAGE RATING–PSE PINS: 720mA @57V(CONTINUOUS)

2337992-4
WWYY-xxxx

CONTROL CODE

DATE CODE
"0213"

YY-YEAR

WW-WEEK

FCC

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN GANESH C M 26NOV2018		TE Connectivity					
DIMENSIONS: mm [INCHES]		CHK O. WIBORG 26NOV2018							
	TOLERANCES UNLESS OTHERWISE SPECIFIED:		NAME						
	0 PLC ± – 1 PLC ± – 2 PLC ± – 3 PLC ± – 4 PLC ± – ANGLES ± –		RJ45 JACK MAG. POE 10/100 LED 1x1						
			PRODUCT SPEC						
			108–94694						
		APPLICATION SPEC		SIZE		CAGE CODE	DRAWING NO	RESTRICTED TO	
		114–94556		A3		00779	C=2337992–4		
MATERIAL		FINISH		WEIGHT					
				CUSTOMER DRAWING		SCALE 3:1		SHEET 3 of 3	REV A