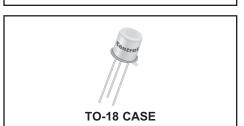
CS18B CS18D CS18M CS18N

SILICON CONTROLLED RECTIFIER 1 AMP, 200 THRU 800 VOLTS





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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CS18B series types are hermetically sealed silicon controlled rectifiers manufactured in a TO-18 case, designed for control systems and sensing circuit applications.

MARKING: FULL PART NUMBER

$\textbf{MAXIMUM RATINGS:} \ (T_{\mbox{\scriptsize A}} = 25 \ ^{\circ}\mbox{C unless otherwise noted})$

	SYMBOL	<u>CS18B</u>	<u>CS18D</u>	<u>CS18M</u>	<u>CS18N</u>	UNITS
Peak Repetitive Off-State Voltage	$V_{DRM, V_{RRM}}$	200	400	600	800	V
RMS On-State Current (T _C =90°C)	I _T (RMS)		1	.0		Α
Nonrept. On-State Current	ITSM		1	0		Α
Fusing Current (t=10ms)	I ² t		0.	24		A^2s
Peak Gate Current (t=10µs)	I_{GM}		1	.0		Α
Peak Gate Dissipation (t=10µs)	P_{GM}		2	.0		W
Gate Dissipation	P _{G(AV)}		0	.1		W
Operating Junction Temperature	TJ		-40 to	+125		°C
Storage Temperature	T _{stg}		-40 to	+150		°C
Thermal Resistance	Θ JC		3	2		°C/W
Thermal Resistance	Θ_{JA}		20	00		°C/W

$\textbf{ELECTRICAL CHARACTERISTICS:} \ (T_{\mbox{A}} = 25 \ ^{\circ}\mbox{C unless otherwise noted})$

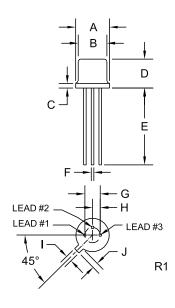
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _{DRM,} I _{RRM}	Rated V _{DRM} , V _{RRM} , R _{GK} =1.0KΩ			1.0	μΑ
I _{DRM} , I _{RRM}	Rated V _{DRM} , V _{RRM} , R _{GK} =1.0KΩ, T _C =125°C			0.1	mA
V_{TM}	I _T =2.0A		1.6	2.15	V
I_{GT}	V_D =12V, R_L =10 Ω		20	200	μΑ
V_{GT}	V_D =12V, R_L =10 Ω		0.65	8.0	V
lΗ	R_{GK} =1.0 $K\Omega$		0.5	5.0	mA
dv/dt	V_D =0.67V x V_{DRM} , R_{GK} =1.0K Ω , T_C =125°C	25			V/µs

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TO-18 CASE - MECHANICAL OUTLINE



DIMENSIONS						
	INC	HES	MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
A (DIA)	0.209	0.230	5.31	5.84		
B (DIA)	0.178	0.195	4.52	4.95		
С	-	0.030	-	0.76		
D	0.170	0.210	4.32	5.33		
E	0.500	-	12.70	-		
F (DIA)	0.016	0.019	0.41	0.48		
G (DIA)	0.100		2.54			
Н	0.050		1.27			
I	0.036	0.046	0.91	1.17		
J	0.028	0.048	0.71	1.22		
TO 40 (DEV/, D4)						

TO-18 (REV: R1)

LEAD CODE:

- 1) CATHODE
- 2) GATE
- 3) ANODE

MARKING: FULL PART NUMBER

R2 (18-January 2010)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA

Main Tel: (631) 435-1110 Main Fax: (631) 435-1824

Support Team Fax: (631) 435-3388

www.centralsemi.com

Worldwide Field Representatives: www.centralsemi.com/wwreps

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