SWITCHMODE™ Power Rectifier 100 V, 60 A

Features and Benefits

- Low Forward Voltage: 0.72 V @ 125°C
- Low Power Loss/High Efficiency
- High Surge Capacity
- 175°C Operating Junction Temperature
- 60 A Total (30 A Per Diode Leg)
- These are Pb-Free Devices
- NRVB Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable

Applications

- Power Supply Output Rectification
- Power Management
- Instrumentation

Mechanical Characteristics:

- Case: Epoxy, Molded
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight (Approximately): 1.9 Grams (TO-220)

1.7 Grams (D²PAK)

- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes:

260°C Max. for 10 Seconds

• ESD Rating: Human Body Model = 3B

Machine Model = C

MAXIMUM RATINGS

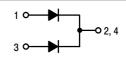
Please See the Table on the Following Page



ON Semiconductor®

http://onsemi.com

SCHOTTKY BARRIER RECTIFIER 60 AMPERES, 100 VOLTS



TO-220AB

CASE 221A



MARKING DIAGRAM





D²PAK CASE 418AJ STYLE 3



A = Assembly Location

Y = Year
WW = Work Week
B60H100 = Device Code
G = Pb-Free Package
AKA = Polarity Designator

ORDERING INFORMATION

| Device | Package | Shipping [†] |
|------------------|---------------------------------|-----------------------|
| MBR60H100CTG | TO-220 (Pb-Free) | 50 Units/Rail |
| MBRB60H100CTT4G | D ² PAK (Pb-Free) | 800/ Tape & Reel |
| NRVBB60H100CTT4G | D ² PAK (Pb-Free) | 800/ Tape & Reel |

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MAXIMUM RATINGS (Per Diode Leg)

| Rating | Symbol | Value | Unit |
|--|--|-----------------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | V _{RRM} V _{RWM} V _R | 100 | V |
| Average Rectified Forward Current (T _C = 155°C) Per Diode Per Device | I _{F(AV)} | 30 60 | А |
| Peak Repetitive Forward Current (Square Wave, 20 kHz, T _C = 151°C) | I _{FRM} | 60 | Α |
| Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz) | I _{FSM} | 350 | Α |
| Operating Junction Temperature (Note 1) | TJ | +175 | °C |
| Storage Temperature | T _{stg} | -65 to +175 | °C |
| Voltage Rate of Change (Rated V _R) | dv/dt | 10,000 | V/μs |
| Controlled Avalanche Energy (see test conditions in Figures 9 and 10) | W _{AVAL} | 400 | mJ |
| ESD Ratings: Machine Model = C Human Body Model = 3B | | > 400 > 8000 | V |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

| Characteristic | | Value | Unit |
|--|----------------|-------|------|
| Maximum Thermal Resistance - Junction-to-Case (Min. Pad) | $R_{	heta JC}$ | 1.0 | °C/W |
| Junction-to-Ambient (Min. Pad) | $R_{	hetaJA}$ | 70 | |

ELECTRICAL CHARACTERISTICS (Per Diode Leg)

| Characteristic | Symbol | Min | Тур | Max | Unit |
|--|----------------|-------------|------------------------------|------------------------------|------|
| Maximum Instantaneous Forward Voltage (Note 2) $ \begin{aligned} &(i_F=30~A,~T_J=25^\circ\text{C})\\ &(i_F=30~A,~T_J=125^\circ\text{C})\\ &(i_F=60~A,~T_J=25^\circ\text{C})\\ &(i_F=60~A,~T_J=125^\circ\text{C}) \end{aligned} $ | VF | - - - | 0.80 0.68 0.93 0.81 | 0.84 0.72 0.98 0.84 | ٧ |
| Maximum Instantaneous Reverse Current (Note 2) (Rated DC Voltage, $T_J = 125^{\circ}C$) (Rated DC Voltage, $T_J = 25^{\circ}C$) | i _R | | 2.0 0.0013 | 10 0.01 | mA |

^{2.} Pulse Test: Pulse Width = 300 µs, Duty Cycle ≤ 2.0%.

^{1.} The heat generated must be less than the thermal conductivity from Junction-to-Ambient: $dP_D/dT_J < 1/R_{\theta JA}$.

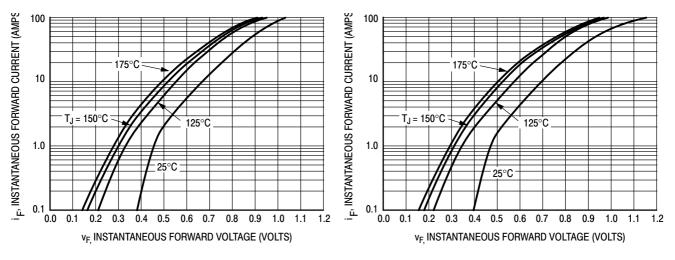


Figure 1. Typical Forward Voltage

Figure 2. Maximum Forward Voltage

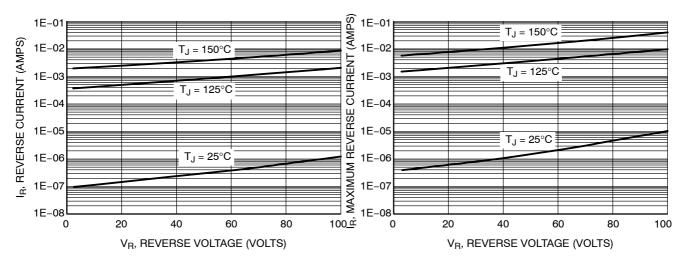


Figure 3. Typical Reverse Current

Figure 4. Maximum Reverse Current

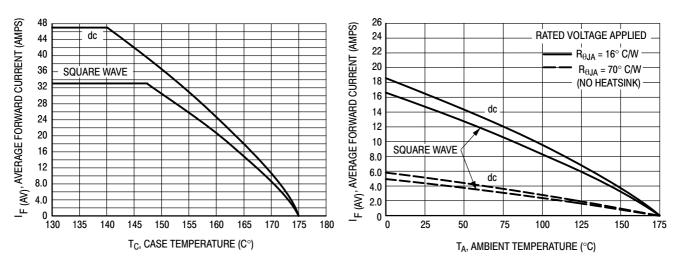
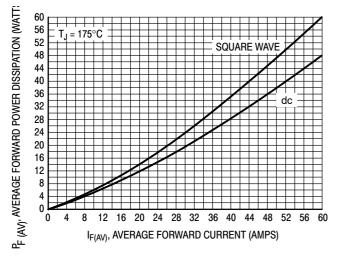


Figure 5. Current Derating, Case Per Leg

Figure 6. Current Derating, Ambient Per Leg



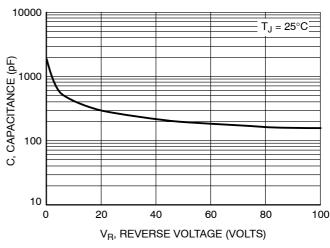


Figure 7. Forward Power Dissipation

Figure 8. Capacitance

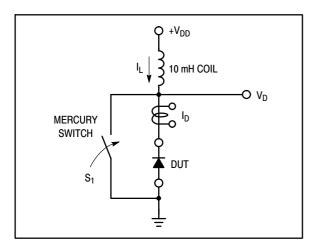


Figure 9. Test Circuit

The unclamped inductive switching circuit shown in Figure 9 was used to demonstrate the controlled avalanche capability of this device. A mercury switch was used instead of an electronic switch to simulate a noisy environment when the switch was being opened.

When S_1 is closed at t_0 the current in the inductor I_L ramps up linearly; and energy is stored in the coil. At t_1 the switch is opened and the voltage across the diode under test begins to rise rapidly, due to di/dt effects, when this induced voltage reaches the breakdown voltage of the diode, it is clamped at BV_{DUT} and the diode begins to conduct the full load current which now starts to decay linearly through the diode, and goes to zero at t_2 .

By solving the loop equation at the point in time when S_1 is opened; and calculating the energy that is transferred to the diode it can be shown that the total energy transferred is equal to the energy stored in the inductor plus a finite amount of energy from the V_{DD} power supply while the diode is in breakdown (from t_1 to t_2) minus any losses due to finite component resistances. Assuming the component resistive

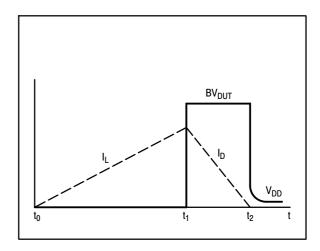


Figure 10. Current-Voltage Waveforms

elements are small Equation (1) approximates the total energy transferred to the diode. It can be seen from this equation that if the V_{DD} voltage is low compared to the breakdown voltage of the device, the amount of energy contributed by the supply during breakdown is small and the total energy can be assumed to be nearly equal to the energy stored in the coil during the time when S_1 was closed, Equation (2).

EQUATION (1):

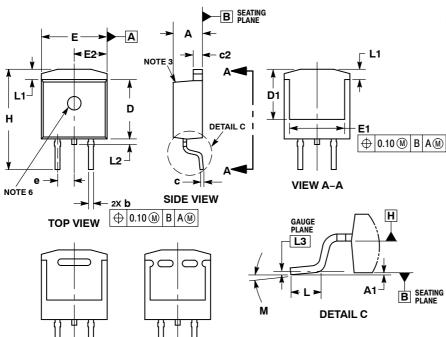
$$W_{AVAL} \approx \frac{1}{2} LI_{LPK}^2 \left(\frac{BV_{DUT}}{BV_{DUT} W_{DD}} \right)$$

EQUATION (2):

$$W_{AVAL} \approx \frac{1}{2} LI_{LPK}^2$$

PACKAGE DIMENSIONS

D²PAK-3 (TO-263, 3-LEAD) CASE 418AJ **ISSUE A**



VIEW A-A **OPTIONAL CONSTRUCTIONS**

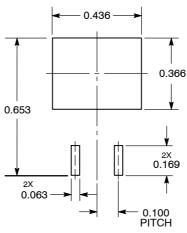
NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.

- Y14.5M, 1994.
 2. CONTROLLING DIMENSION: INCHES.
 3. CHAMFER OPTIONAL
 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.005 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.
 5. THERMAL PAD CONTOUR IS OPTIONAL WITHIN DIMENSIONS E, L1, D1 AND E1.
 6. OPTIONAL MOLD FEATURE

| // 11010 II | | | | | |
|---|---|---|---|--|--|
| INCHES | | MILLIMETERS | | | |
| MIN | MAX | MIN | MAX | | |
| 0.160 | 0.190 | 4.06 | 4.83 | | |
| 0.000 | 0.010 | 0.00 | 0.25 | | |
| 0.020 | 0.039 | 0.51 | 0.99 | | |
| 0.012 | 0.029 | 0.30 | 0.74 | | |
| 0.045 | 0.065 | 1.14 | 1.65 | | |
| 0.330 | 0.380 | 8.38 | 9.65 | | |
| 0.260 | | 6.60 | | | |
| 0.380 | 0.420 | 9.65 | 10.67 | | |
| 0.245 | | 6.22 | | | |
| 0.100 | 0.100 BSC | | BSC | | |
| 0.575 | 0.625 | 14.60 | 15.88 | | |
| 0.070 | 0.110 | 1.78 | 2.79 | | |
| | 0.066 | | 1.68 | | |
| | 0.070 | | 1.78 | | |
| 0.010 | BSC | 0.25 | BSC | | |
| 0° | 8° | 0° | 8° | | |
| | MIN 0.160 0.000 0.020 0.012 0.045 0.330 0.260 0.345 0.100 0.575 0.070 0.010 | MIN MAX 0.160 0.190 0.000 0.010 0.020 0.039 0.012 0.029 0.045 0.065 0.330 0.380 0.260 0.380 0.420 0.245 0.100 BSC 0.575 0.625 0.070 0.110 0.066 0.070 0.010 BSC | MIN MAX MIN 0.160 0.190 4.06 0.000 0.010 0.00 0.020 0.039 0.51 0.012 0.029 0.30 0.045 0.065 1.14 0.330 0.380 8.38 0.260 6.60 0.380 0.420 9.65 0.245 6.22 0.100 BSC 2.54 0.575 0.625 14.60 0.070 0.110 1.78 0.066 0.070 0.010 BSC 0.25 | | |

RECOMMENDED SOLDERING FOOTPRINT*

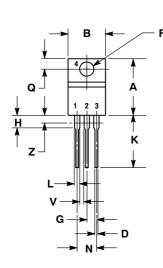


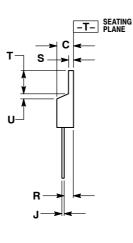
DIMENSIONS: INCHES

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AF**





- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
 DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

| | INCHES | | MILLIMETERS | |
|-----|--------|-------|-------------|-------|
| DIM | MIN | MAX | MIN | MAX |
| Α | 0.570 | 0.620 | 14.48 | 15.75 |
| В | 0.380 | 0.405 | 9.66 | 10.28 |
| С | 0.160 | 0.190 | 4.07 | 4.82 |
| D | 0.025 | 0.035 | 0.64 | 0.88 |
| F | 0.142 | 0.161 | 3.61 | 4.09 |
| G | 0.095 | 0.105 | 2.42 | 2.66 |
| Н | 0.110 | 0.155 | 2.80 | 3.93 |
| J | 0.014 | 0.025 | 0.36 | 0.64 |
| K | 0.500 | 0.562 | 12.70 | 14.27 |
| L | 0.045 | 0.060 | 1.15 | 1.52 |
| N | 0.190 | 0.210 | 4.83 | 5.33 |
| Q | 0.100 | 0.120 | 2.54 | 3.04 |
| R | 0.080 | 0.110 | 2.04 | 2.79 |
| S | 0.045 | 0.055 | 1.15 | 1.39 |
| T | 0.235 | 0.255 | 5.97 | 6.47 |
| U | 0.000 | 0.050 | 0.00 | 1.27 |
| ٧ | 0.045 | | 1.15 | |
| Z | | 0.080 | | 2.04 |

STYLE 6:

PIN 1. ANODE

- CATHODE
- ANODE
- CATHODE

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