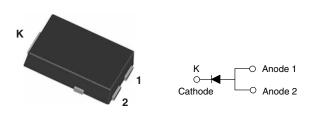
Vishay Semiconductors





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TO-277A (SMPC)

PRODUCT SUMMARY						
Package	TO-277A (SMPC)					
I _{F(AV)}	6 A					
V _R	200 V					
V _F at I _F	0.74 V					
t _{rr (typ.)}	28 ns					
T _J max.	175 °C					
Diode variation	Single die					

FEATURES

- Hyperfast recovery time, reduced Q_{rr}, and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 gualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, piezo-injection, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Peak repetitive reverse voltage	V _{RRM}		200	V				
Average rectified forward current	I _{F(AV)}	T _{Sp} = 160 °C	6					
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	150	A				
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C				

ELECTRICAL SPECIFICATIONS (T _J = 25 $^{\circ}$ C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-		
Forward voltage	V _F	I _F = 6 A	-	0.87	0.94	V	
Forward voltage		I _F = 6 A, T _J = 125 °C	-	0.74	0.8		
Deverse la clusica compart		$V_{R} = V_{R}$ rated	-	-	2		
Reverse leakage current	IR	$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	3	15	μA	
Junction capacitance	CT	V _R = 200 V	-	33	-	pF	

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 $^{\circ}$ C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS		
	t _{rr}	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	28	-			
Povoroo rocovor timo		I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	25			
Reverse recovery time		T _J = 25 °C		-	22	-	A nC		
		T _J = 125 °C	I _F = 6 A dI _F /dt = 200 A/μs V _R = 160 V	-	33	-			
Deals recovery ourrent	I _{RRM}	T _J = 25 °C		-	2.4	-			
Peak recovery current		T _J = 125 °C		-	5.0	-			
	Q _{rr}	T _J = 25 °C		-	27	-			
Reverse recovery charge		T _J = 125 °C		-	80	-			

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER SYMBOL		TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C	
Thermal resistance, junction to solder pad	R _{thJ-Sp}		-	2.2	3	°C/W	
Approximate weight				0.1		g	
Approximate weight				0.0035		oz.	
Marking device		Case style TO-277A (SMPC)		NE	H2		

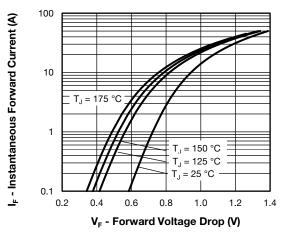


Fig. 1 - Typical Forward Voltage Drop Characteristics

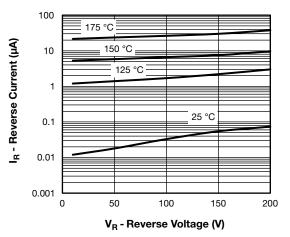


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

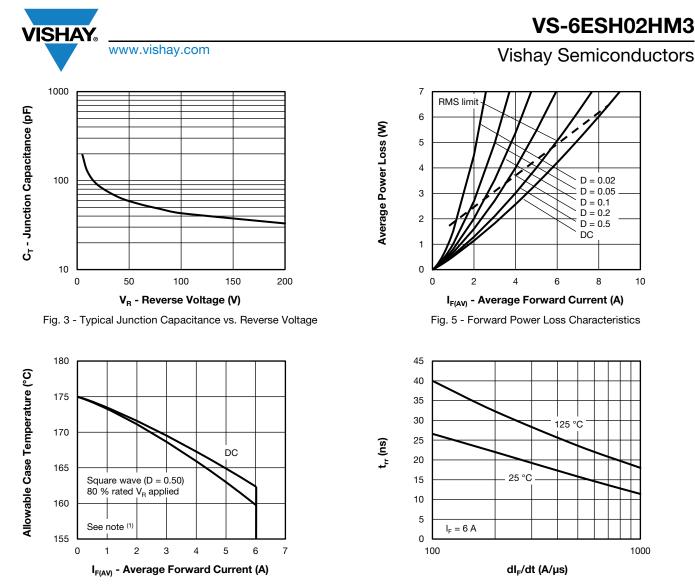


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current



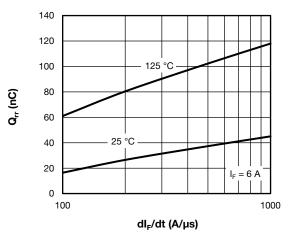


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{5}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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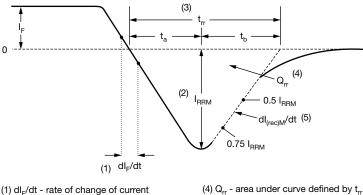
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VS-6ESH02HM3

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- (1) dl_F/dt rate of change of current through zero crossing
- and I_{RRM}
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

 $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

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Device code

	VS-		6	Е	S	н	02	Н	М3		
-	1		2	3	4	5	6	7	8		
	1	-		,	niconduo		oduct				
	3										
	4	-	S = SMPC package								
	5	 Process type, H = hyperfast recovery 									
	6	-	Volt	Voltage code (02 = 200 V)							
	7	-	H =	H = AEC-Q101 qualified							
	8	-	M3	= halog	en-free,	RoHS-	complia	nt, and	terminat	tions lead (Pb)-	

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-6ESH02HM3/86A	1500	1500	7" diameter plastic tape and reel				
VS-6ESH02HM3/87A	6500	6500	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95570					
Part marking information	www.vishay.com/doc?95565					
Packaging information	www.vishay.com/doc?88869					

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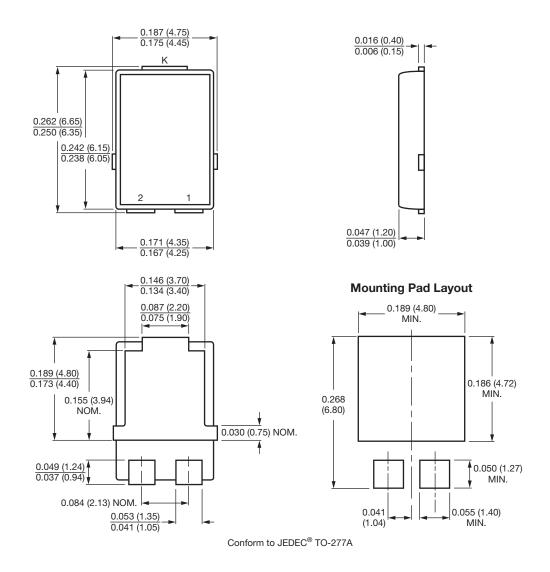
Outline Dimensions





TO-277A (SMPC)

DIMENSIONS in inches (millimeters)





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