

N0604N

N-channel MOSFET 60 V, 82 A, 6.5 m Ω

R07DS0850EJ0100 Rev.1.00 Aug 27, 2012

Description

The N0604N is N-channel MOS Field Effect Transistor designed for high current switching applications.

Features

• Low on-state resistance

 $R_{DS \mbox{ (on)}} = 6.5 \mbox{ m}\Omega$ MAX. $(V_{GS} = 10 \mbox{ V}, \mbox{ I}_D = 41 \mbox{ A})$

• Low input capacitance

 $C_{iss} = 4150 \text{ pF TYP}. (V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V})$

• High current

 $I_{D(DC)} = \pm 82 \text{ A}$

• RoHS Compliant

Ordering Information

Part No.	Lead Plating	Packing	Package
N0604N-S19-AY *1	Pure Sn (Tin)	Tube	TO-220
		50 p/tube	1.9 g TYP.

Note: *1. Pb-free (This product does not contain Pb in the external electrode.)

Absolute Maximum Ratings (T_A = 25°C, all terminals are connected)

Item	Symbol	Ratings	Unit
Drain to Source Voltage ($V_{GS} = 0 V$)	V _{DSS}	60	V
Gate to Source Voltage ($V_{DS} = 0 V$)	V _{GSS}	±20	V
Drain Current (DC)	I _{D(DC)}	±82	А
Drain Current (pulse) *1	I _{D(pulse)}	±200	А
Total Power Dissipation ($T_C = 25^{\circ}C$)	P _{T1}	116	W
Total Power Dissipation ($T_A = 25^{\circ}C$)	P _{T2}	1.5	W
Channel Temperature	T _{ch}	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C
Single Avalanche Current *2	I _{AS}	35	А
Single Avalanche Energy *2	E _{AS}	125	mJ

Thermal Resistance

Channel to Case (Drain) Thermal Resistance	R _{th(ch-C)}	1.08	°C/W
Channel to Ambient Thermal Resistance *2	R _{th(ch-A)}	83.3	°C/W

Notes: *1. PW \leq 10 μ s, Duty Cycle \leq 1%

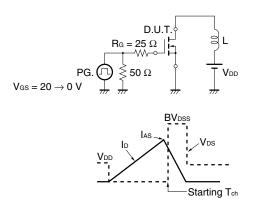
^{*}2. Starting T_{ch} = 25°C, R_G = 25 Ω , V_{DD} = 30 V, V_{GS} = 20 \rightarrow 0 V, L = 100 μ H



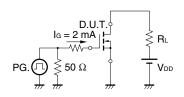
Item	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions
Zero Gate Voltage Drain Current	I _{DSS}			1	μA	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$
Gate Leakage Current	I _{GSS}			±100	nA	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$
Gate to Source Cut-off Voltage	V _{GS(off)}	2.0		4.0	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Forward Transfer Admittance *1	y _{fs}	30			S	$V_{DS} = 5 V, I_D = 41 A$
Drain to Source On-state Resistance * ¹	R _{DS(on)}		5.1	6.5	mΩ	$V_{GS} = 10 \text{ V}, I_D = 41 \text{ A}$
Input Capacitance	C _{iss}		4150		pF	V _{DS} = 25 V,
Output Capacitance	C _{oss}		310		pF	$V_{GS} = 0 V,$
Reverse Transfer Capacitance	C _{rss}		165		pF	f = 1 MHz
Turn-on Delay Time	t _{d(on)}		24		ns	$V_{DD} = 30 \text{ V}, I_D = 41 \text{ A},$
Rise Time	t _r		8		ns	V _{GS} = 10 V,
Turn-off Delay Time	t _{d(off)}		64		ns	$R_G = 0 \Omega$
Fall Time	t _f		7		ns	
Total Gate Charge	Q _G		75		nC	V _{DD} = 48 V,
Gate to Source Charge	Q _{GS}		21		nC	V _{GS} = 10 V,
Gate to Drain Charge	Q _{GD}		21		nC	I _D = 82 A
Body Diode Forward Voltage *1	V _{F(S-D)}			1.5	V	$I_F = 82 \text{ A}, V_{GS} = 0 \text{ V}$
Reverse Recovery Time	t _{rr}		38		ns	$I_F = 82 \text{ A}, V_{GS} = 0 \text{ V},$
Reverse Recovery Charge	Qrr		39		nC	di/dt = 100 A/µs

Note: *1. Pulsed

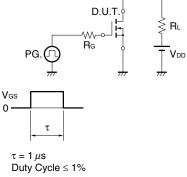
TEST CIRCUIT 1 AVALANCHE CAPABILITY

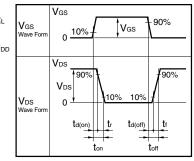


TEST CIRCUIT 3 GATE CHARGE



TEST CIRCUIT 2 SWITCHING TIME

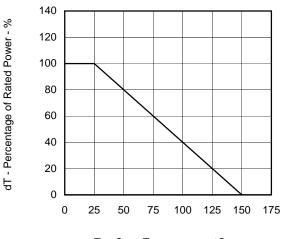






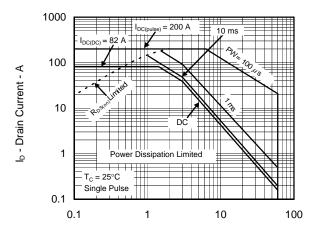
Typical Characteristics (T_A = 25°C)

DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA

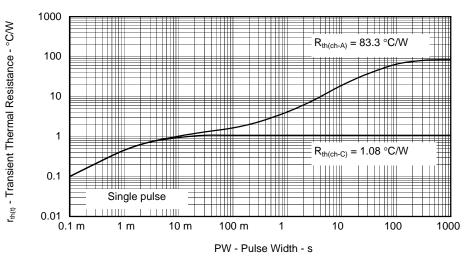


T_c - Case Temperature - °C



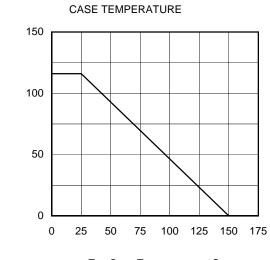


V_{DS} - Drain to Source Voltage - V



TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

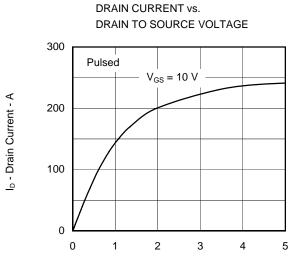
 P_{T} - Total Power Dissipation - W



TOTAL POWER DISSIPATION vs.

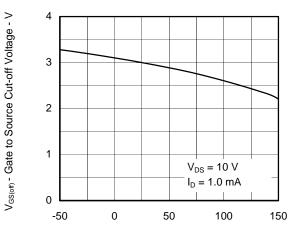
 T_C - Case Temperature - $^\circ C$





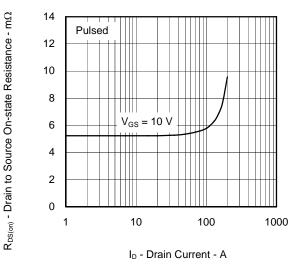


GATE TO SOURCE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE

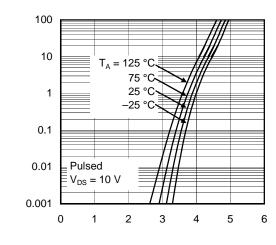


T_{ch} - Channel Temperature - °C

DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



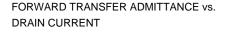
FORWARD TRANSFER CHARACTERISTICS

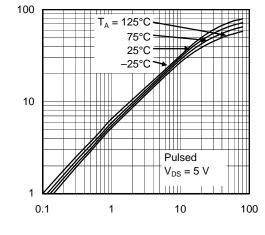


I_D - Drain Current - A

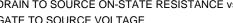
y_{is} | - Forward Transfer Admittance - S

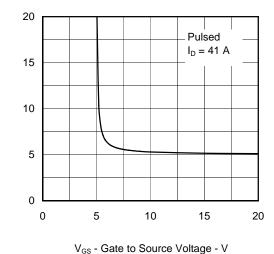






I_D - Drain Current - A



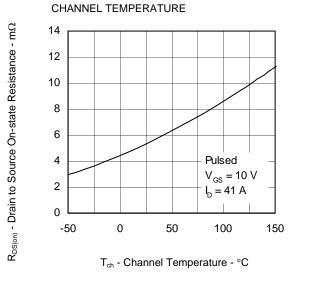




GATE TO SOURCE VOLTAGE

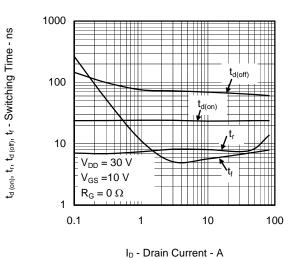


 $R_{\text{DS(on)}}$ - Drain to Source On-state Resistance - $m\Omega$

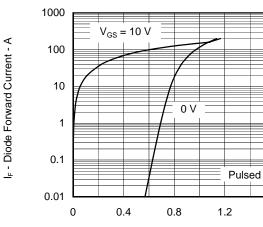


DRAIN TO SOURCE ON-STATE RESISTANCE vs.

SWITCHING CHARACTERISTICS

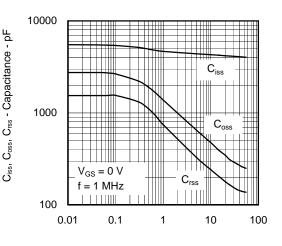


SOURCE TO DRAIN DIODE FORWARD VOLTAGE



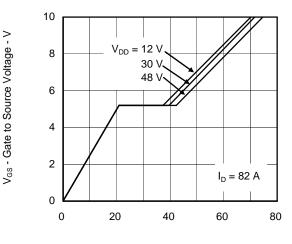
V_{F(S-D)} - Source to Drain Voltage - V

CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



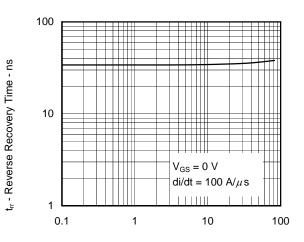
V_{DS} - Drain to Source Voltage - V

DYNAMIC INPUT CHARACTERISTICS



Q_G - Gate Charge - nC

REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT



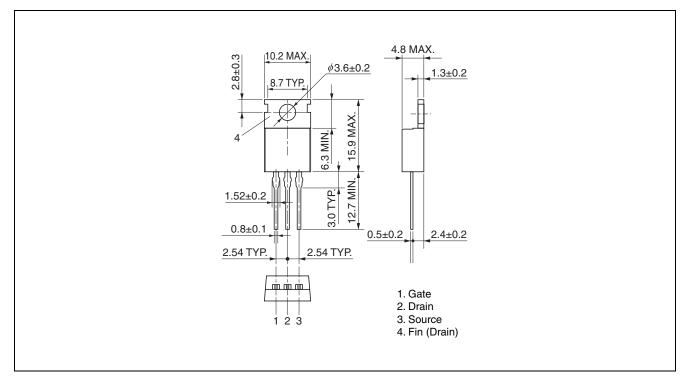
IF - Diode Forward Current - A



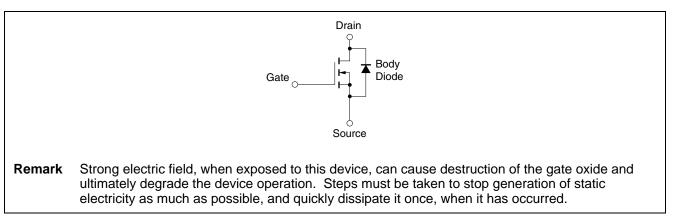
1.6

Package Drawing (Unit: mm)

TO-220



Equivalent Circuit





Revision History	N0604N Data Sheet
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		Description		
Rev.	Date	Page	Summary	
1.00	Aug 27, 2012	-	First Edition Issued	

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