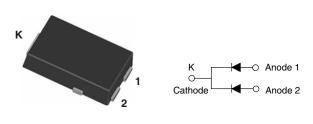
RoHS

COMPLIANT HALOGEN

FREE

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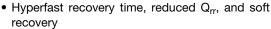
Hyperfast Rectifier, 2 x 2 A FRED Pt®

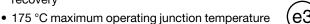


TO-277A	/CN/IDC	*1
1U-2//A	ISIVIPU	

PRODUCT SUMMARY				
Package	TO-277A (SMPC)			
I _{F(AV)}	2 x 2 A			
V_{R}	100 V			
V _F at I _F	0.75 V			
t _{rr (typ.)}	24 ns			
T _J max.	175 °C			
Diode variation	Dual die			

FEATURES





- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage		V_{RRM}		100	V	
Average rectified forward current	per device	I _{F(AV)}	T _{Sp} = 165 °C	4	A	
	per diode			2		
Non-repetitive peak surge current	per device		T 25 °C	90		
	per diode	I _{FSM}	T _J = 25 °C	50		
Operating junction and storage temperatures		T _J , T _{Stg}		-65 to +175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	MBOL TEST CONDITIONS MIN. TYP. MAX.		MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	100	-	-	
Forward voltage, per diode V _F	V	I _F = 2 A	-	0.88	0.95	V
	VF	I _F = 2 A, T _J = 125 °C	-	0.75	0.82	
Reverse leakage current, per diode	I _R	$V_R = V_R$ rated	-	-	2	
		T _J = 125 °C, V _R = V _R rated	-	0.5	8	- μΑ
Junction capacitance	C _T	V _R = 100 V	-	8	-	pF



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		I _F = 1 A, dI _F /dt = 50 A/µs, V _R = 30 V		-	24	-	
Reverse recovery time t _{rr}		I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	24	
	L _{rr}	T _J = 25 °C	I _F = 2 A	-	16	-	ns
		T _J = 125 °C		-	22	-	
Peak recovery current I _{RRM}		T _J = 25 °C		-	2	-	
	T _J = 125 °C	dl _F /dt = 200 A/μs V _R = 160 V	-	3	-	Α Α	
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	16	-	
	Q _{rr}	T _J = 125 °C		-	30	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction to solder pad, per diode	R _{thJ-Sp}		-	4.5	5.5	°C/W
Approximate weight				0.1		g
Approximate weight		0.0035 o		oz.		
Marking device		Case style TO-277A (SMPC)		JC	H1	

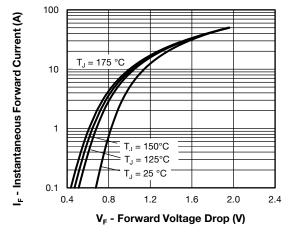


Fig. 1 - Typical Forward Voltage Drop Characteristics

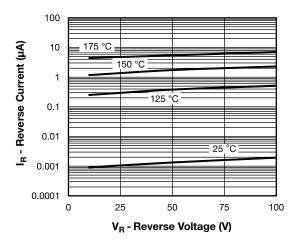


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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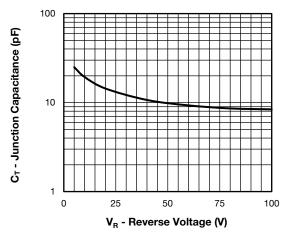


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

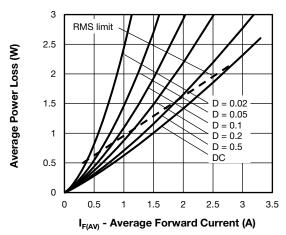


Fig. 5 - Forward Power Loss Characteristics

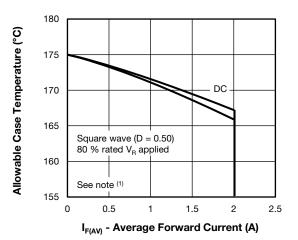


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

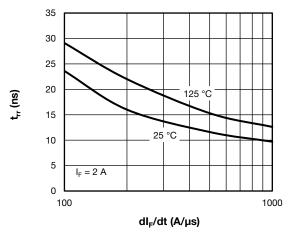


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

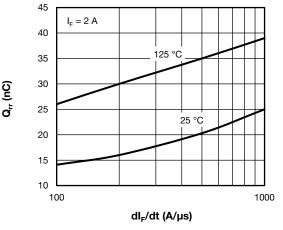
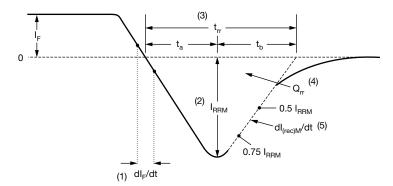


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times R_{\text{thJC}}; \\ \text{Pd} & = \text{Forward power loss} = I_{\text{F(AV)}} \times V_{\text{FM}} \text{ at } (I_{\text{F(AV)}}/D) \text{ (see fig. 5)}; \\ \text{Pd}_{\text{REV}} & = \text{Inverse power loss} = V_{\text{R1}} \times I_{\text{R}} \text{ (1 - D)}; I_{\text{R}} \text{ at } V_{\text{R1}} = \text{rated } V_{\text{R}} \\ \end{array}$

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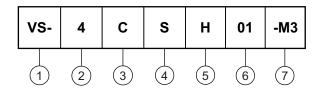


- (1) dl_F/dt rate of change of current through zero crossing
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}
- (2) I_{RRM} peak reverse recovery current
- $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_{r}$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (5) $dI_{(rec)M}/dt$ peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product
- **2** Current rating (4 = 4 A)
- Circuit configuration:

C = common cathode

- S = SMPC package

5 - Process type,

H = hyperfast recovery

Voltage code (01 = 100 V)

7 - -M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-4CSH01-M3/86A	1500	1500	7" diameter plastic tape and reel			
VS-4CSH01-M3/87A	6500	6500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95570			
Part marking information	www.vishay.com/doc?95565			
Packaging information	www.vishay.com/doc?88869			



Vishay Semiconductors

TO-277A (SMPC)

DIMENSIONS in inches (millimeters)





Legal Disclaimer Notice

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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