1200V 20A SiC Schottky MPS™ Diode



V_{RRM} = 1200 V $I_{F (Tc = 135^{\circ}C)}$ = 27 A Q_{C} = 47 nC

Silicon Carbide Schottky Diode

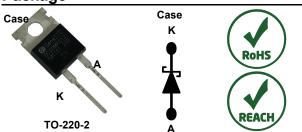
Features

- High Avalanche (UIS) Capability
- Enhanced Surge Current Capability
- Superior Figure of Merit Q_C/I_F
- Low Thermal Resistance
- 175 °C Maximum Operating Temperature
- Temperature Independent Switching Behavior
- Positive Temperature Coefficient of V_F
- Extremely Fast Switching Speeds

Advantages

- Low Standby Power Losses
- Improved Circuit Efficiency (Lower Overall Cost)
- Low Switching Losses
- Ease of Paralleling without Thermal Runaway
- Smaller Heat Sink Requirements
- Low Reverse Recovery Current
- Low Device Capacitance
- Low Reverse Leakage Current

Package



Applications

- Boost Diode in Power Factor Correction (PFC)
- Switched Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Motor Drives
- Freewheeling / Anti-parallel Diode in Inverters
- Solar Inverters & Wind Energy Converters
- Electric Vehicles (EV) & DC Fast Charging
- Induction Heating & Welding

Absolute Maximum Ratings (At T_C = 25 °C Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit	
Repetitive Peak Reverse Voltage	V_{RRM}		1200	V	
		T _C = 25 °C, D = 1	58		
Continuous Forward Current	I_{F}	$T_C = 135 ^{\circ}C, D = 1$	27	Α	
		$T_C = 150 ^{\circ}C, D = 1$	20		
Non-Repetitive Peak Forward Surge Current, Half Sine Wave	I _{F,SM}	T_C = 25 °C, t_P = 10 ms	160	Δ.	
		T_{C} = 150 °C, t_{P} = 10 ms	128	Α	
Repetitive Peak Forward Surge Current, Half Sine Wave	$I_{F,RM}$	$T_C = 25 ^{\circ}\text{C}, t_P = 10 \text{ms}$	96	^	
		T_C = 150 °C, t_P = 10 ms	68	Α	
Non-Repetitive Peak Forward Surge Current	I _{F,max}	T _C = 25 °C, t _P = 10 μs	800	А	
i ² t Value	∫i² dt	T_C = 25 °C, t_P = 10 ms	128	A ² s	
Non-Repetitive Avalanche Energy	E _{AS}	L = 1.3 mH, I _{AS} = 20 A	245	mJ	
Diode Ruggedness	dV/dt	V _R = 0 ~ 960 V	200	V/ns	
Power Dissipation	P _{tot}	T _C = 25 °C	242	W	
Operating and Storage Temperature	T _j , T _{stg}		-55 to 175	°C	

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Electrical Characteristics

Developer	Cymahal	Conditions		Values			11!4
Parameter	Symbol			Min.	Тур.	Max.	Unit
Diode Forward Voltage	V	I _F = 20 A, T _j = 25 °C			1.5	1.8	V
	V_{F}	$I_F = 20 A, T_j =$		2	2.4		
Reverse Current		V _R = 1200 V, T _j = 25 °C			3	15	
	I _R	V_R = 1200 V, T_j = 175 °C			30	150	μΑ
Total Capacitive Charge	0		V _R = 400 V		34		nC
	Q_{C}	$I_F \le I_{F,MAX}$	V _R = 800 V		47		
Switching Time	4	$dI_F/dt = 200 A/\mu s$ $T_j = 175 °C$	V _R = 400 V		< 10		ns
	t_s		$V_{R} = 800 \text{ V}$		< 10		
Total Capacitance	С	$V_R = 1 V, f = 1 MHz$			1080		pF
	C	$V_R = 800 \text{ V}, f = 1 \text{ MHz}$			70		

Thermal / Mechanical Characteristics

Thermal Resistance, Junction - Case	R _{thJC}		0.62		°C/W
Weight	W_{T}		2		g
Mounting Torque	T _M	M3 Screw	(0.8	Nm

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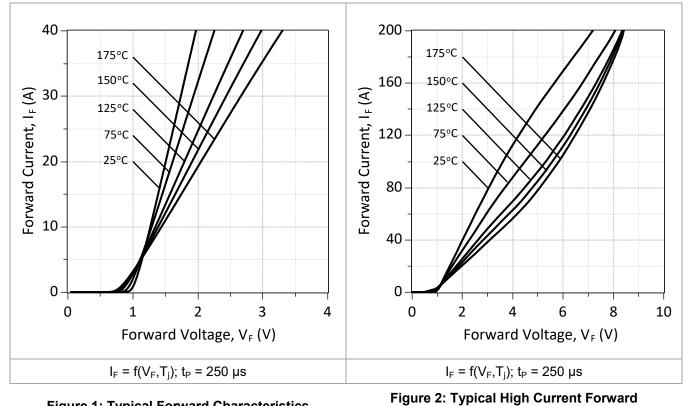


Figure 1: Typical Forward Characteristics

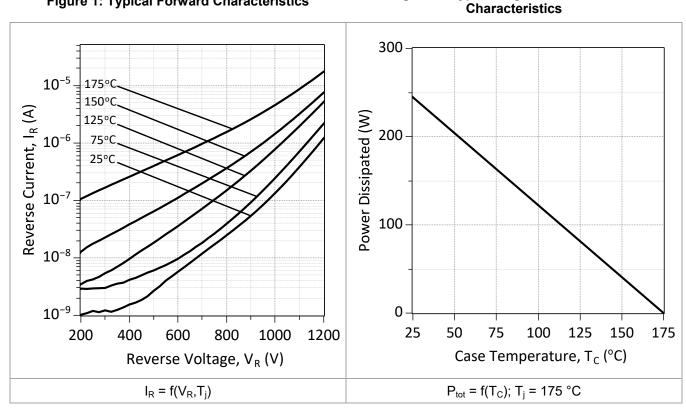


Figure 3: Typical Reverse Characteristics

Figure 4: Power Derating Curve

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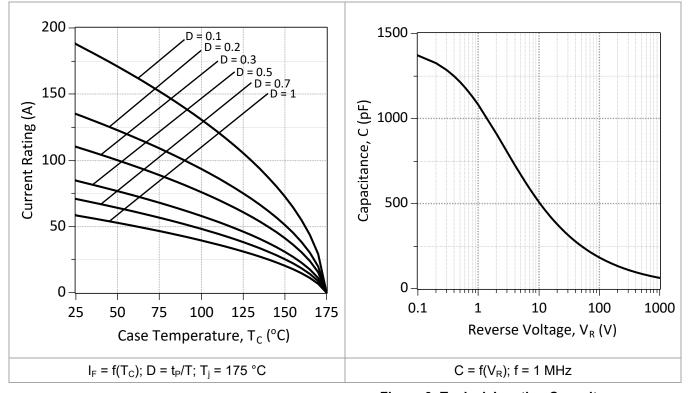


Figure 5: Current Derating Curves

60 (a) 45 (b) 45 (c) 45

Figure 6: Typical Junction Capacitance vs Reverse Voltage Characteristics

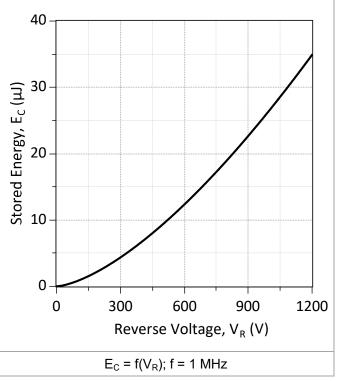


Figure 7: Typical Capacitive Charge vs Reverse Voltage Characteristics

Figure 8: Typical Capacitive Energy vs Reverse Voltage Characteristics

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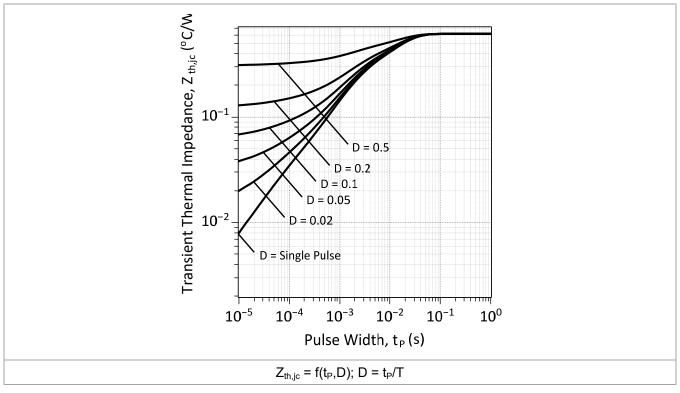


Figure 9: Transient Thermal Impedance

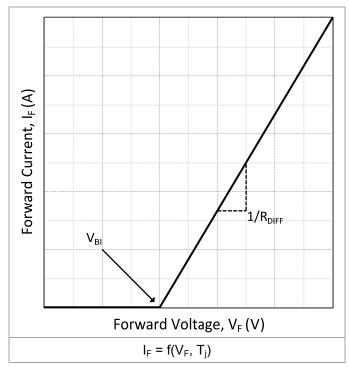


Figure 10: Forward Curve Model

$$I_F = (V_F - V_{BI})/R_{DIFF}$$
 (A)

Built-In Voltage (V_{BI}):

$$V_{BI}(T_j) = m^*T_j + n (V),$$

 $m = -1.48e-03, n = 1.08$

Differential Resistance (RDIFF):

$$R_{DIFF}(T_j) = a^*T_j^2 + b^*T_j + c (\Omega);$$

 $a = 7.45e-07, b = 8.84e-05, c = 0.0192$

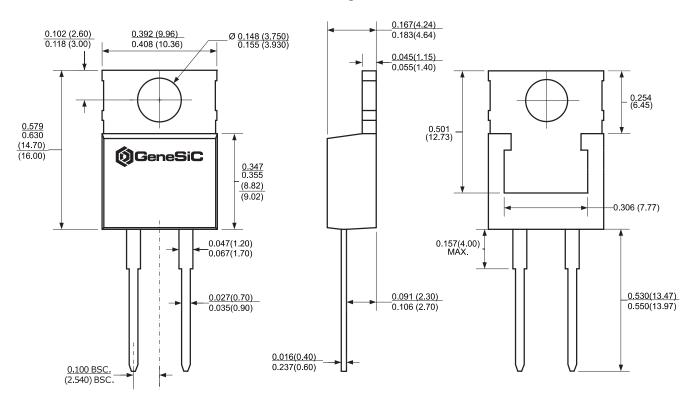
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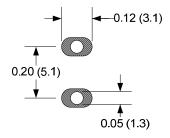
Package Dimensions

TO-220-2

Package Outline



Recommended Solder Pad Layout



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS



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RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

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REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

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Related Links

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